

BOOK 1

CONFIGURATIONS

- RACK MOUNT

- CONSOLE MOUNT

INSTALLATION

- UNPACKING

- ASSEMBLY

- LOCATION

- AREA

- ENVIRONMENT

SIGNAL CONNECTIONS

- VIDEO SIGNALS REQUIRED

- VIDEO SIGNAL OUTPUTS

- AUDIO SIGNAL INPUTS

- POWER CONNECTIONS

- REMOTE CONTROL CONNECTIONS

- ACCESSORY CONNECTIONS

SYSTEM OVERALL

- SYSTEM OVERALL SIGNAL FLOW

- BLOCK DIAGRAM (SHOWING SUBCOMPONENTS)

- SYSTEM INTERCONNECT

- DIGITAL CABLES

- ANALOG CABLES

- AC POWER

- DC POWER

SYSTEM SUBCOMPONENTS (IN ALPHABETICAL ORDER)

- COLOR CONTROLLER

- BLOCK DIAGRAM OF NEST AND CONTROL PANEL

- LIST OF CIRCUIT CARDS

- CONTROL PANEL ELECTRONICS

- BLOCK DIAGRAM OF NEST AND CONTROL PANEL

- LIST OF CIRCUIT CARDS

- DATA CASSETTE

- DEI TAPE DRIVE

- MICROCOMPUTER POWER SUPPLY

- FUNCTION GENERATOR NEST

- BLOCK DIAGRAM OF NEST

- LIST OF CIRCUIT CARDS

- MONITOR VIDEO PROCESSOR

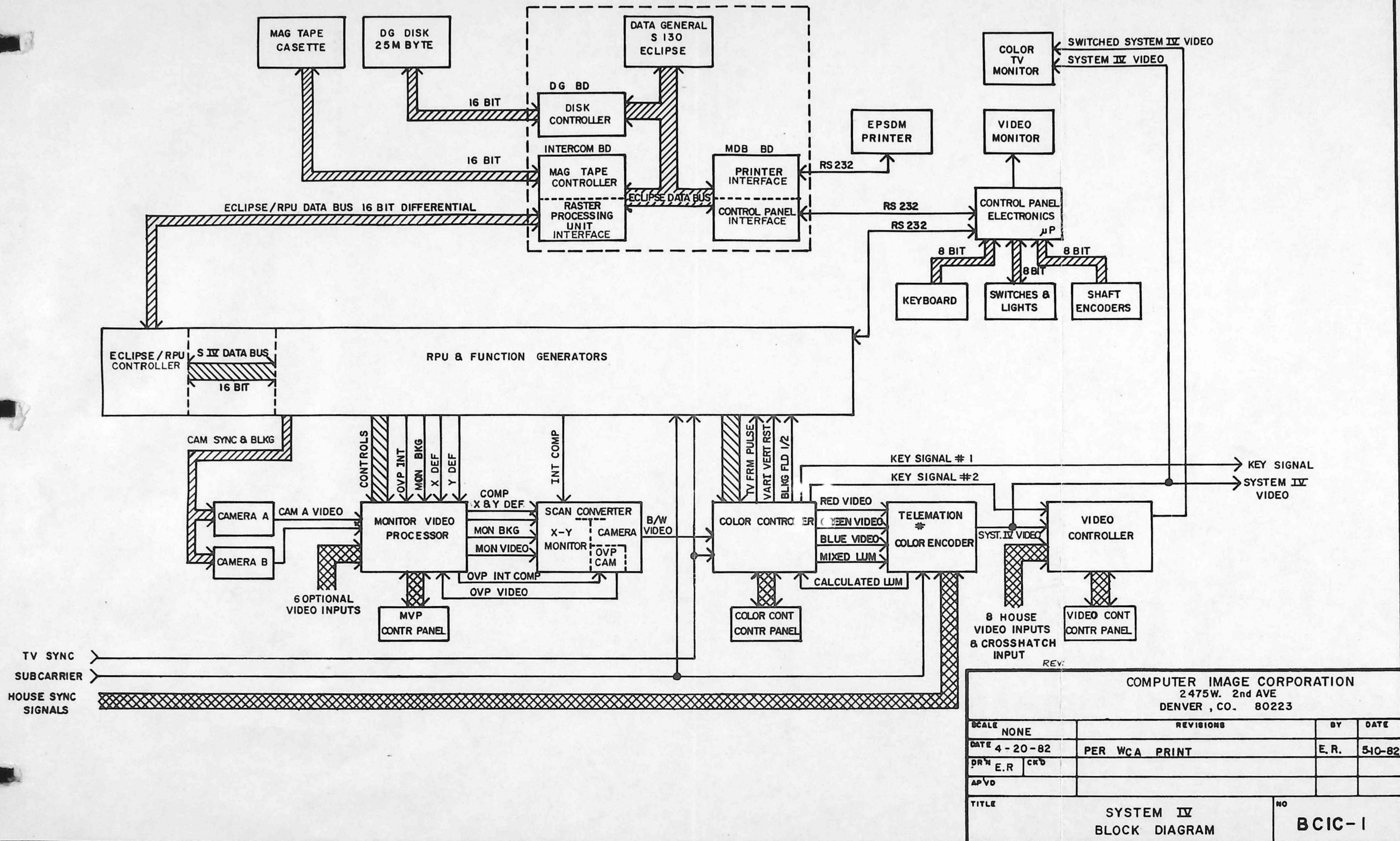
- BLOCK DIAGRAM OF NEST AND CONTROL PANEL

- LIST OF CIRCUIT CARDS

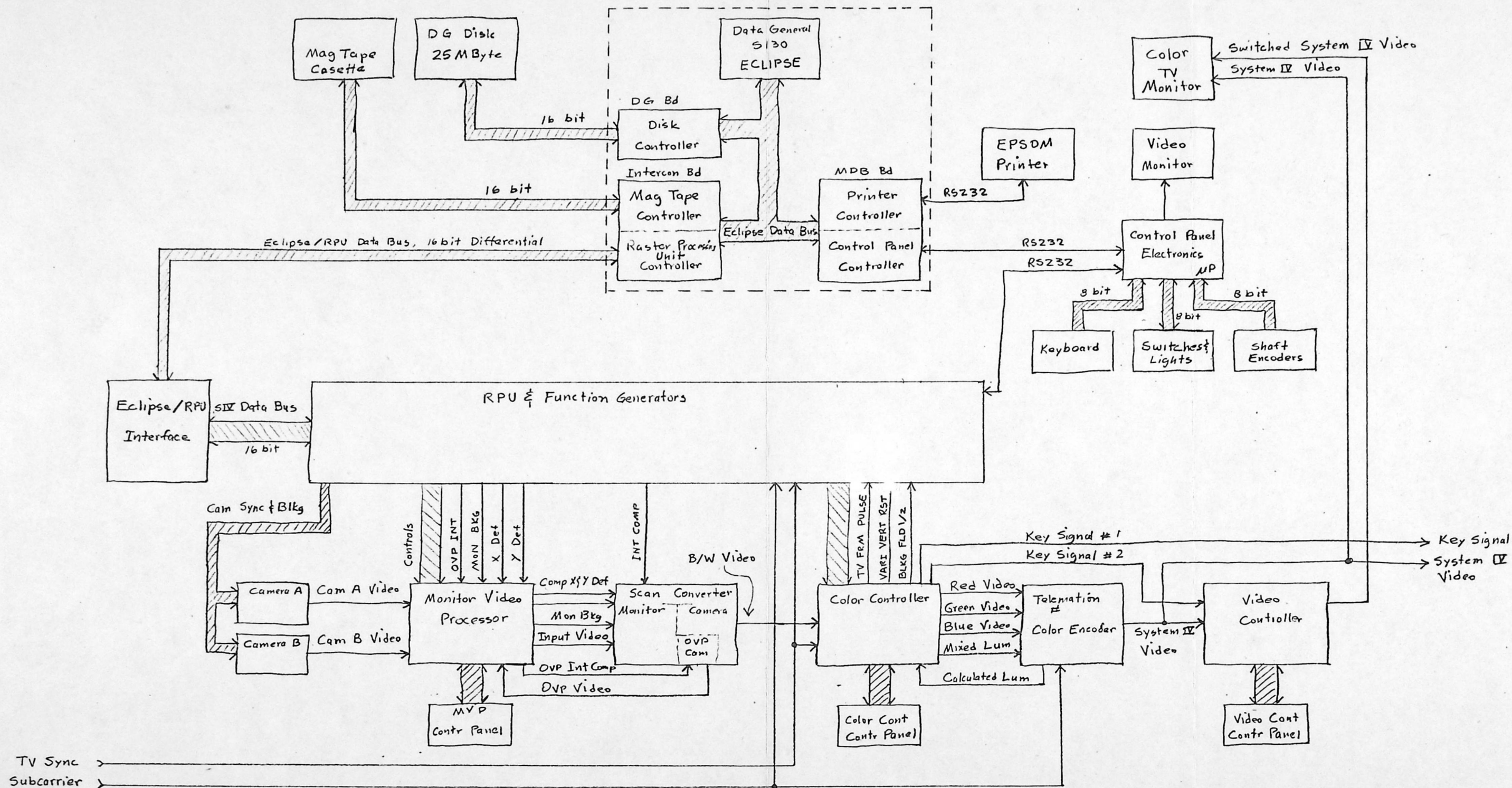
- POWER SUPPLY, MAIN SYSTEM

- POWER SUPPLY DESCRIPTION

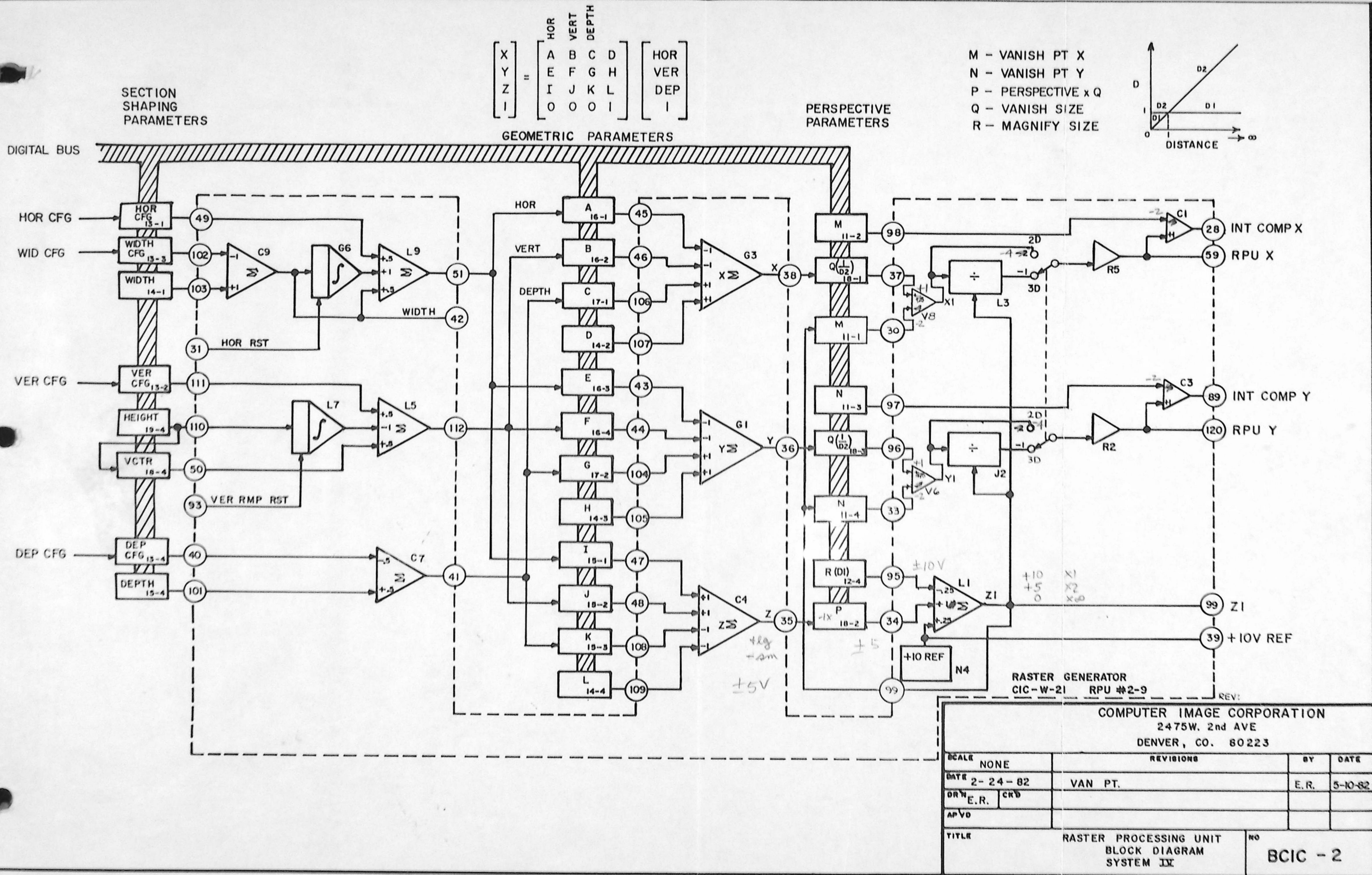
- LIST OF SUBCOMPONENTS



COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 4-20-82	PER WCA PRINT	E.R.	5-10-82
DRN E.R.	CKD		
APVD			
TITLE SYSTEM IV BLOCK DIAGRAM	NO BCIC-1		



SYSTEM IV
Block Diagram
CIC-B-1



COMPUTER IMAGE CORPORATION 2475W. 2nd AVE DENVER, CO. 80223			
SCALE	NONE	REVISIONS	BY
DATE	2-24-82	VAN PT.	E.R.
DRN	E.R.	CRD	
APVD			
TITLE		RASTER PROCESSING UNIT BLOCK DIAGRAM SYSTEM IX	
		NO BCIC - 2	

WRONG REV

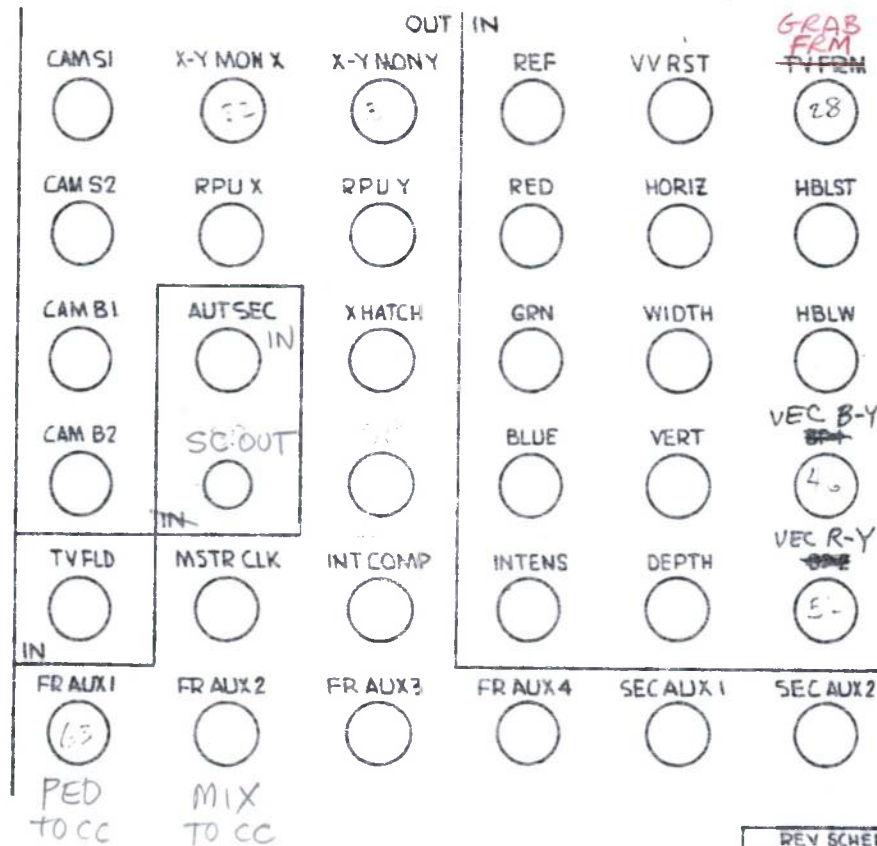
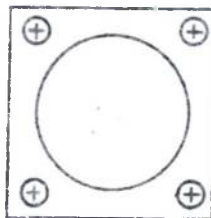
CABLING VIEW

TOP

RASTER PROCESSING UNIT #2

TOP

FG PWR OUT



SEN CONV



COL CTL



REV SCHEDULE
11/11/02 ORIG EVL
A 10/4/84 EVL

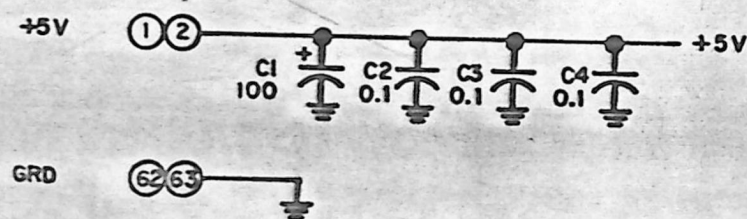
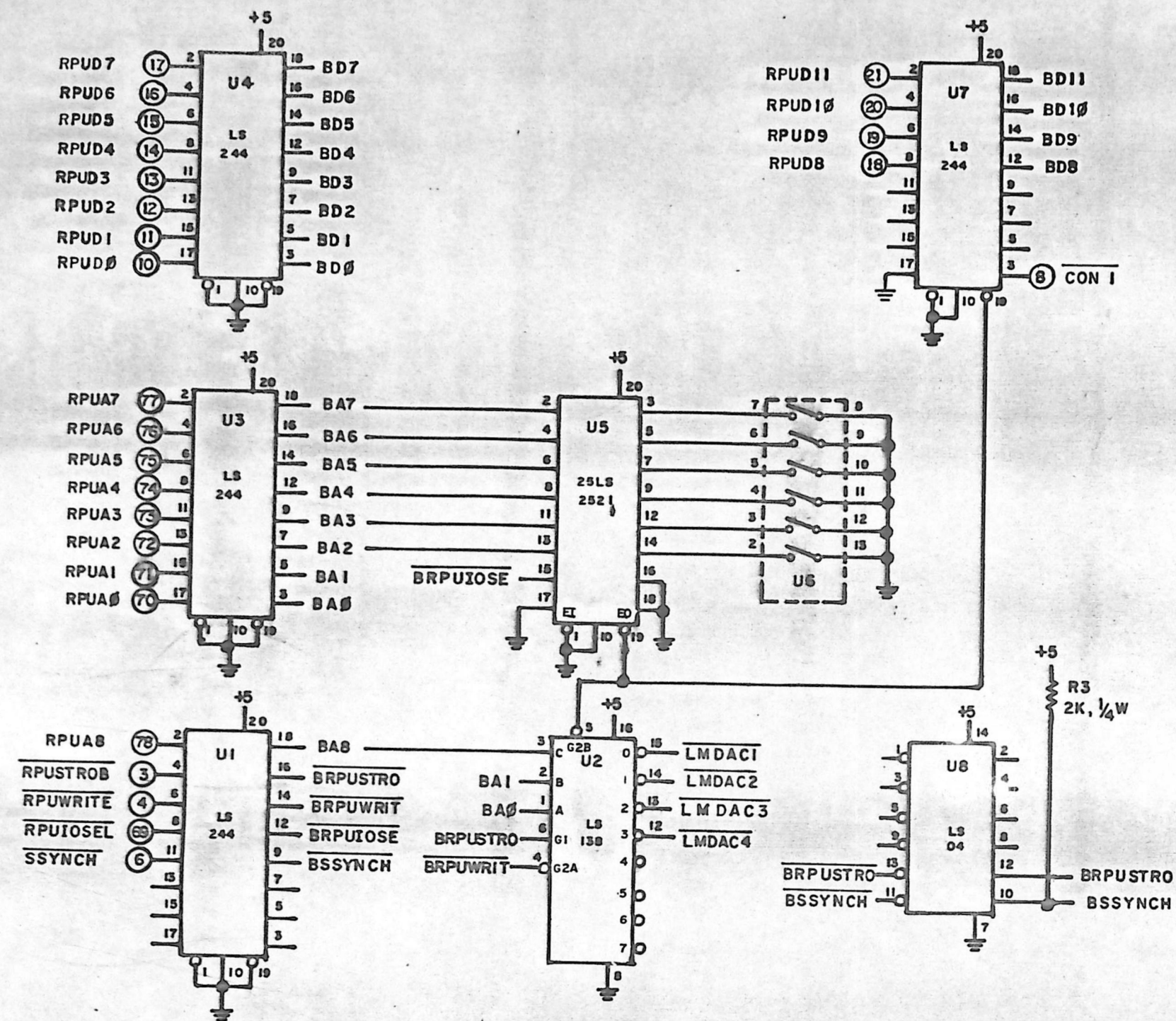
COMPUTER IMAGE CORP

RPU#2 REAR PANEL - UPRIGHT

CONNECTOR FUNCTION

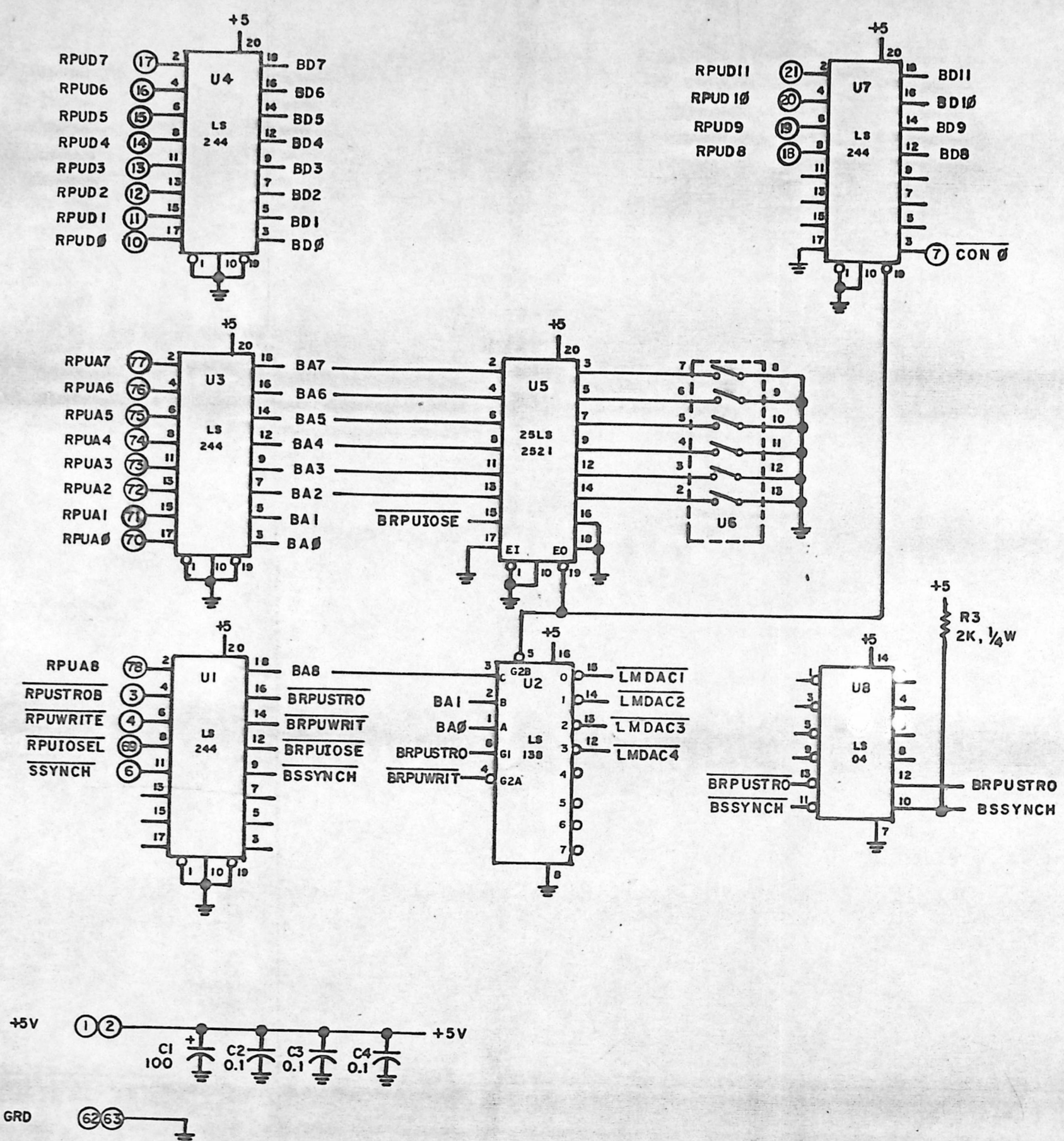
FCIC-7

SH 1 OF 2

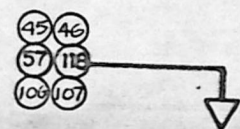
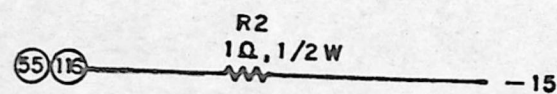
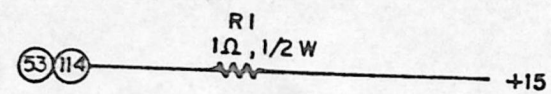
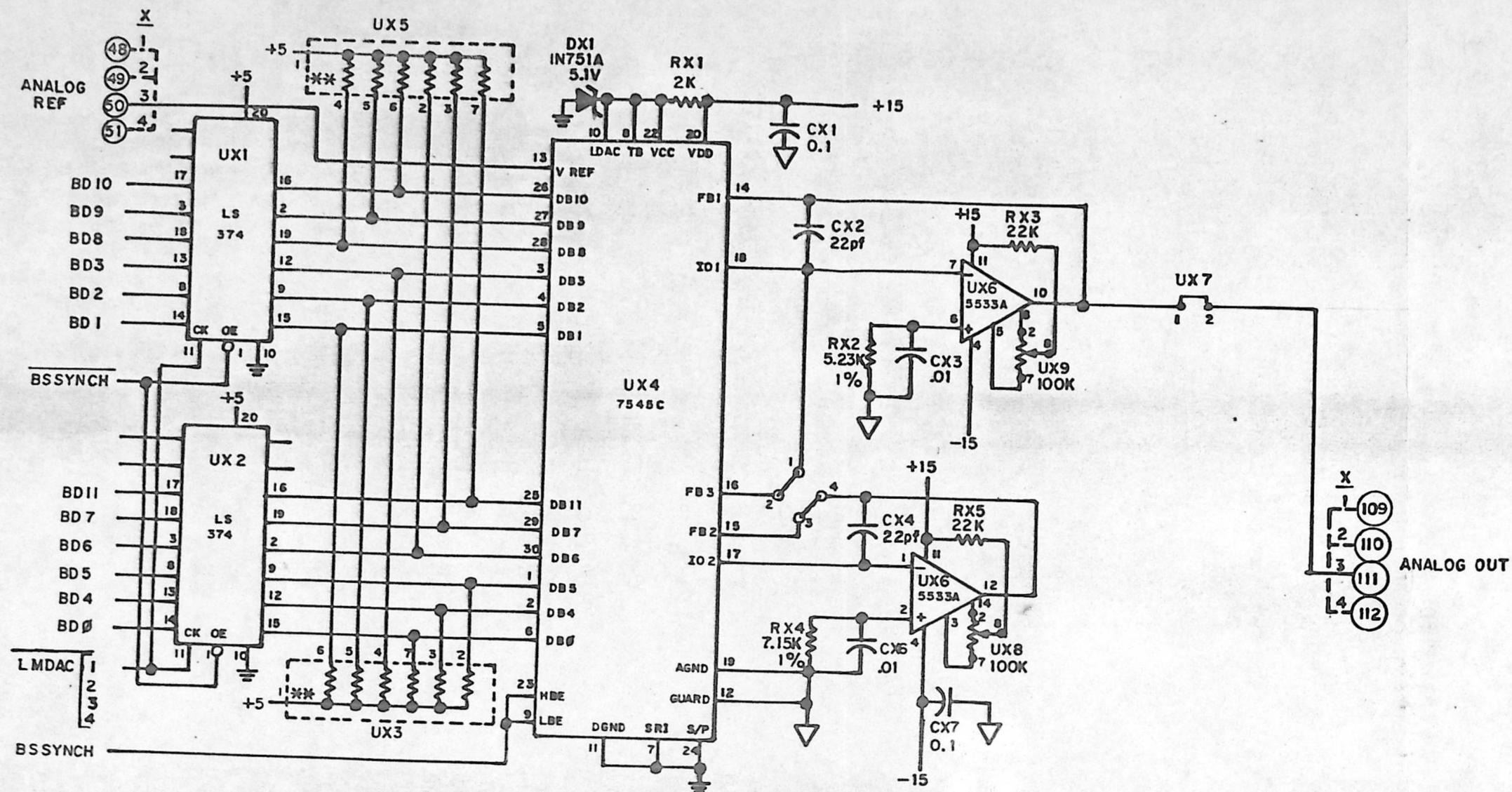


REV:A,B;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 9-14-81	PER W.C.A. PRINT	E.R.	3-10
DRW E.R. CKD			
APVD			
TITLE	DIGITAL CONTROL FOR FOUR 12 BIT M DAC's		NO CIC-1-1 SHEET 1 OF 2



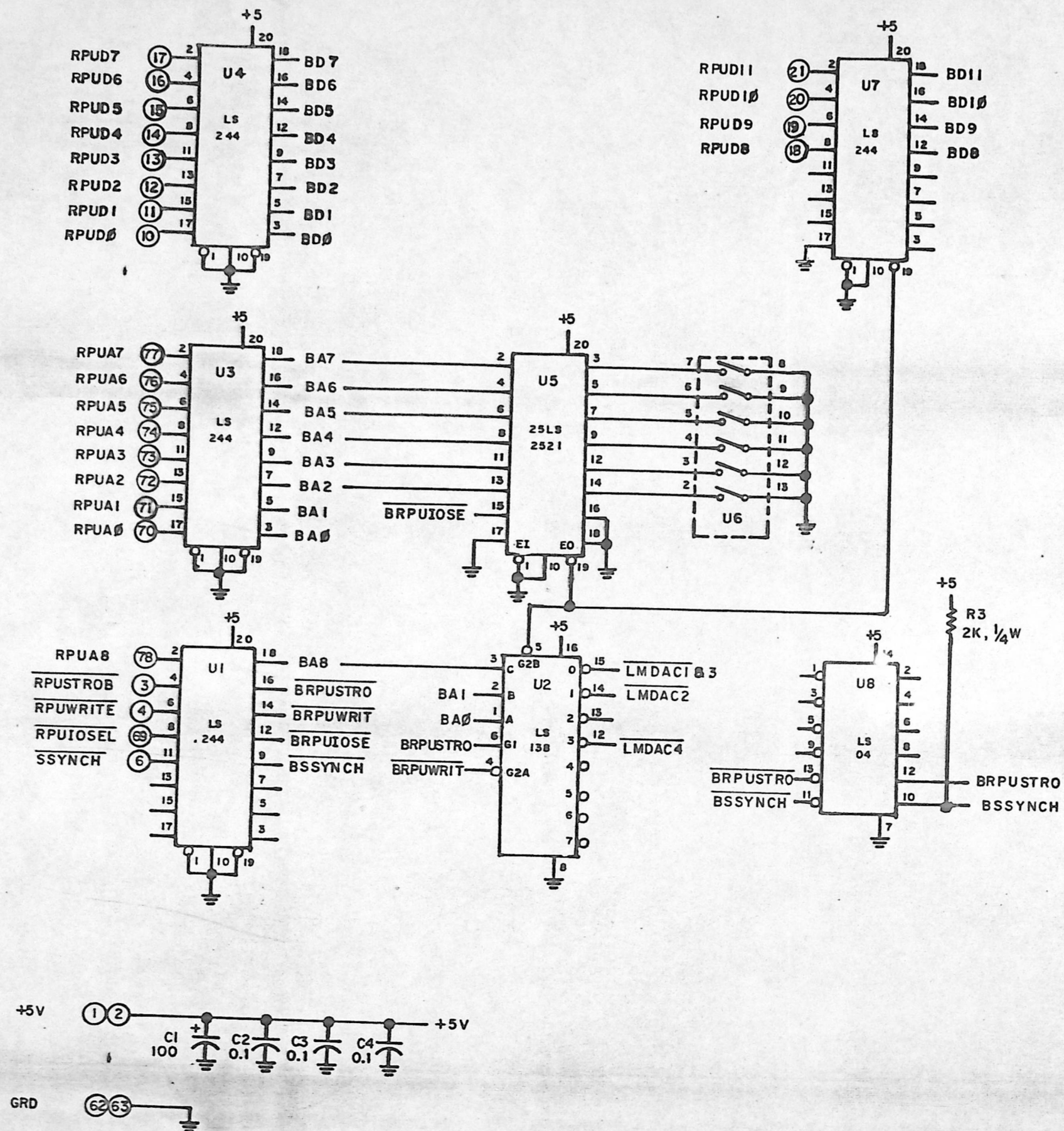
REV: A;B; COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 9-14-81	PER WCA PRINT	E.R.	3-10
DRW E.R.	CRD		
APVD			
TITLE DIGITAL CONTROL FOR FOUR 12 BIT MDAC's		NO CIC-1-2 SHEET 1 OF 2	



UNLESS OTHERWISE SPECIFIED
 ALL RESISTORS ARE 1/4 W, 5%
 ALL CAPACITORS ARE IN MFD's
 ▽ DENOTES ANALOG GROUND
 * DENOTES BECKMAN 694-3-RIOKD
 4 RESISTOR NETWORK
 ** 9 RESISTOR NETWORK, 2.0K EACH

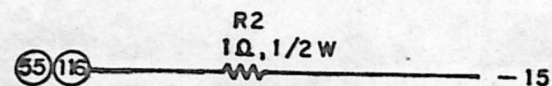
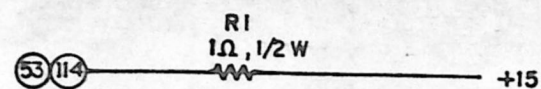
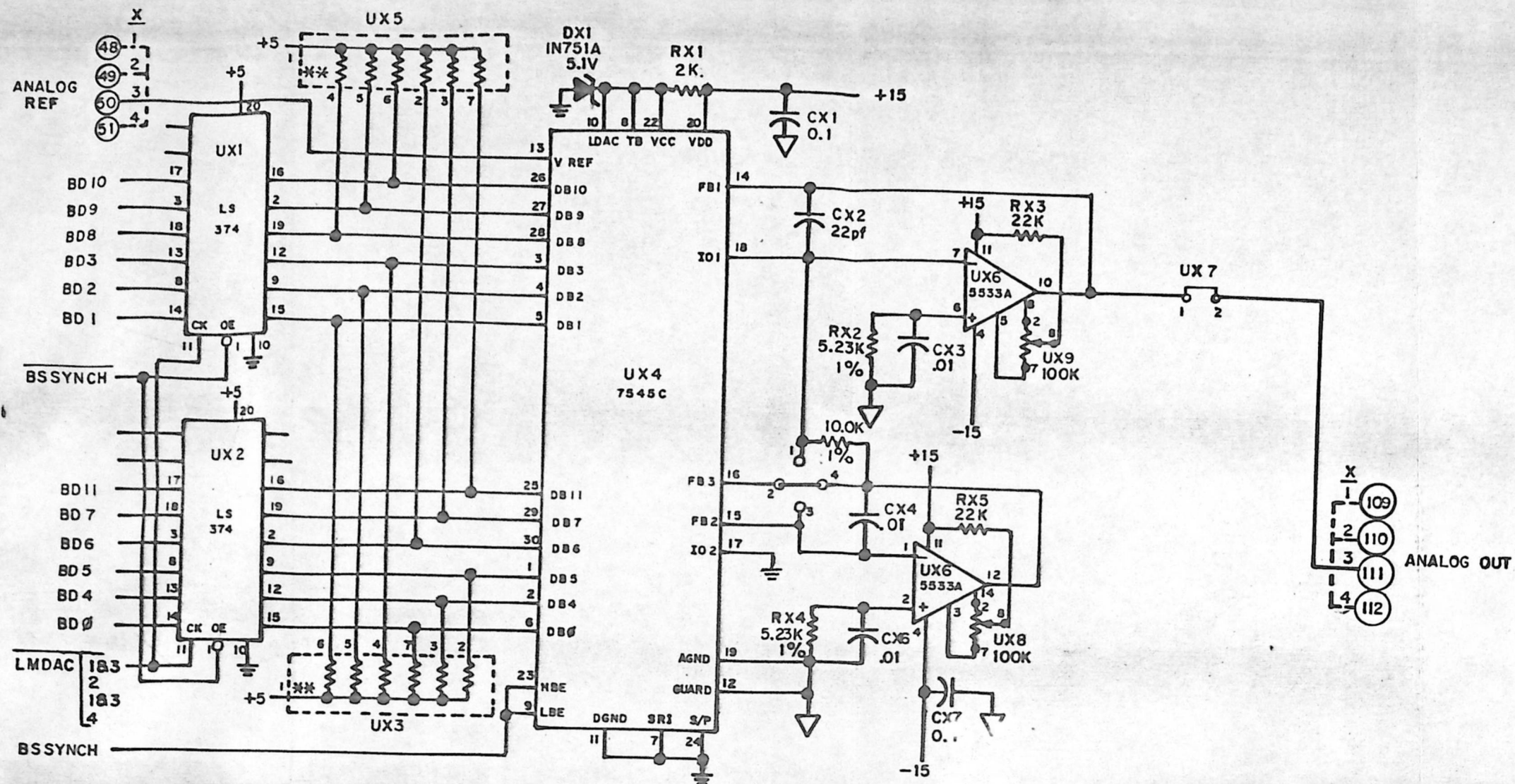
REV: A; B;

COMPUTER IMAGE CORPORATION				
2475 W. 2nd AVE				
DENVER, CO. 80223				
SCALE	REVISIONS		BY	DATE
NONE				
DATE	9-14-81	PER W.C.A. PRINT	E.R.	3-10
DRN	E.R.	CKD		
APVD				
TITLE			NO	
12 BIT MDAC's			CIC-1-2	
(ONE OF FOUR PER BOARD)			SHEET 2 OF 2	



REV: A;

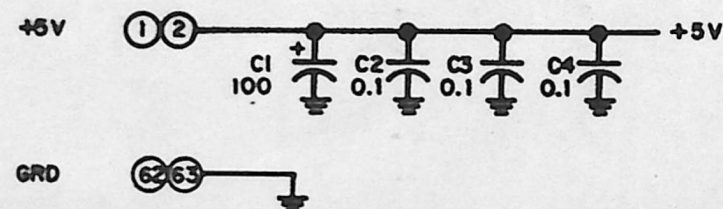
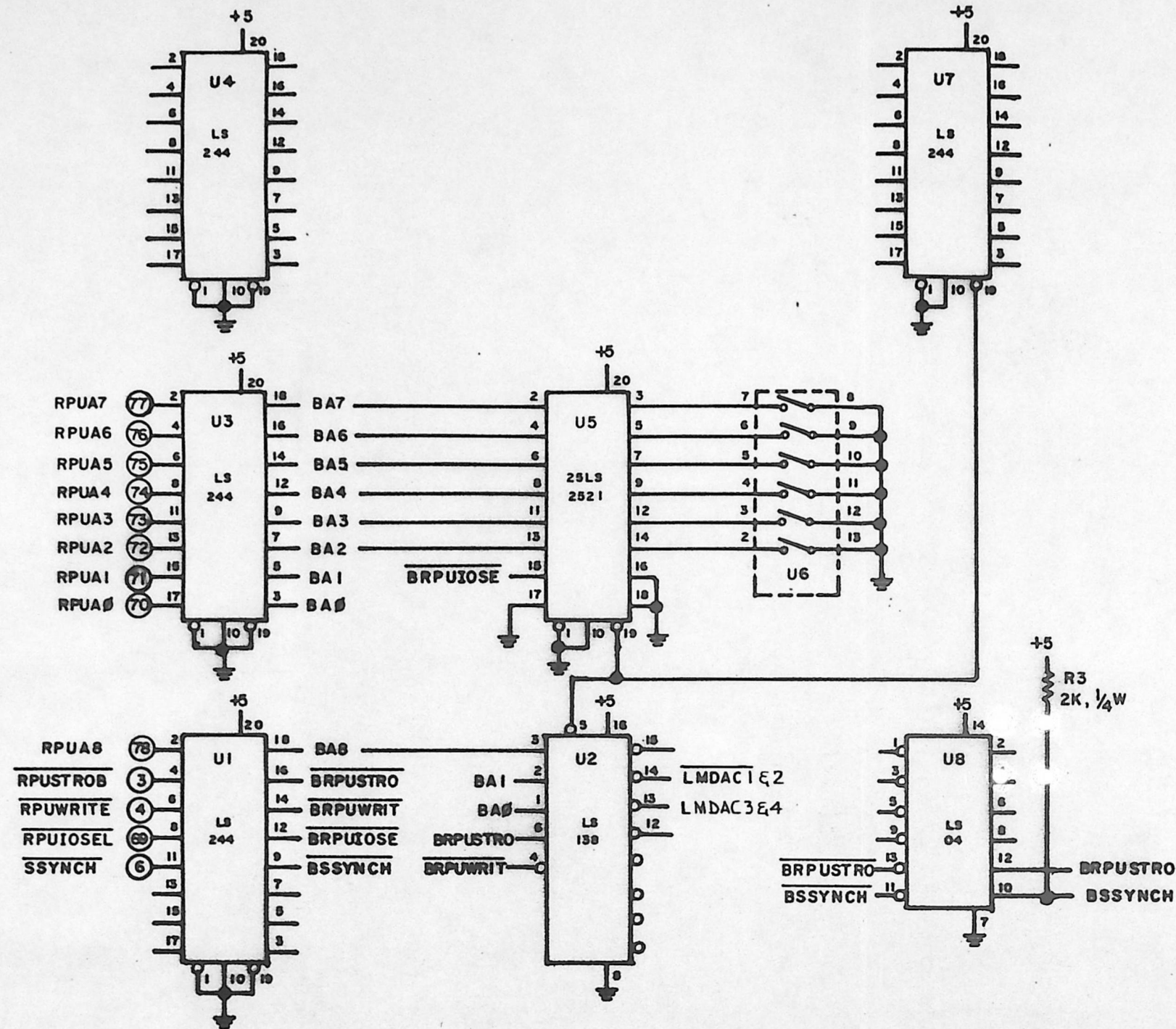
COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	NONE	REVISIONS	BY DATE
DATE	9-14-81	PER W.C.A. PRINT	E.R. 3-10
DRW	E.R. CKD		
APVD			
TITLE	DIGITAL CONTROL FOR FOUR 12 BIT M DAC's		NO
			CIC-1-3
			SHEET 1 OF 2



UNLESS OTHERWISE SPECIFIED
 ALL RESISTORS ARE 1/4W, 5%
 ALL CAPACITORS ARE IN MFD's
 ▽ DENOTES ANALOG GROUND
 * DENOTES BECKMAN 694-3-RIOKD
 4 RESISTOR NETWORK
 ** 9 RESISTOR NETWORK, 2.0K EACH

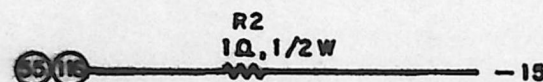
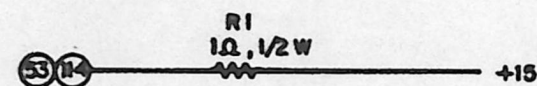
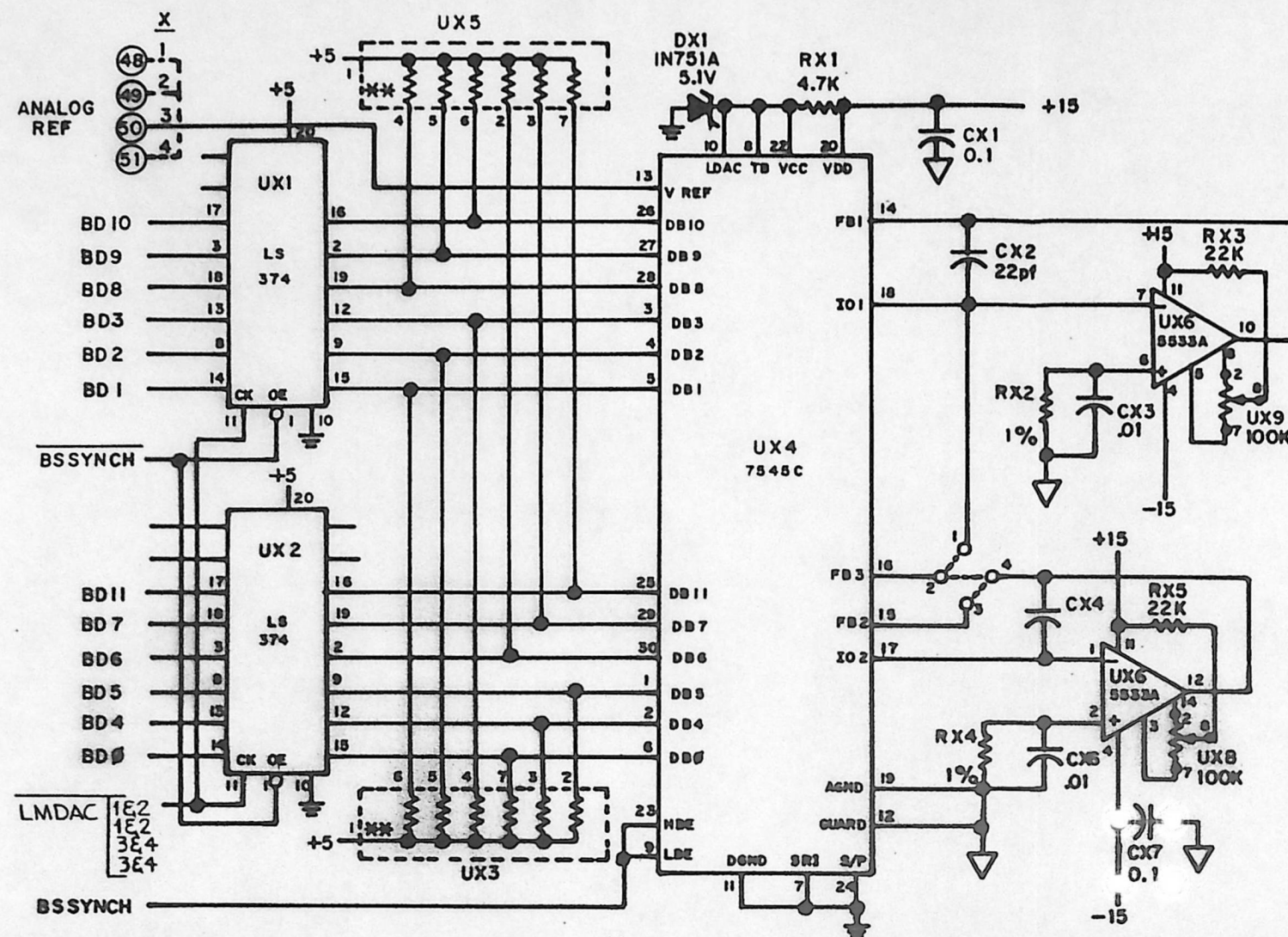
REV: A;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE	NONE	REVISIONS	BY DATE
DATE	9-14-81	PER WCA. PRINT	E.R. 3-10
DRN	E.R.	CHKD	
APVD			
TITLE		NO	
12 BIT MDAC's (ONE OF FOUR PER BOARD)		CIC-1-3 SHEET 2 OF 2	



REV:

COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	REVISIONS	BY	DATE
NONE			
DATE			
DRN E.R. CKD			
APVD			
TITLE		NO	
DIGITAL CONTROL FOR FOUR		CIC-1-6	
12 BIT M DAC's		SHEET 1 OF 2	

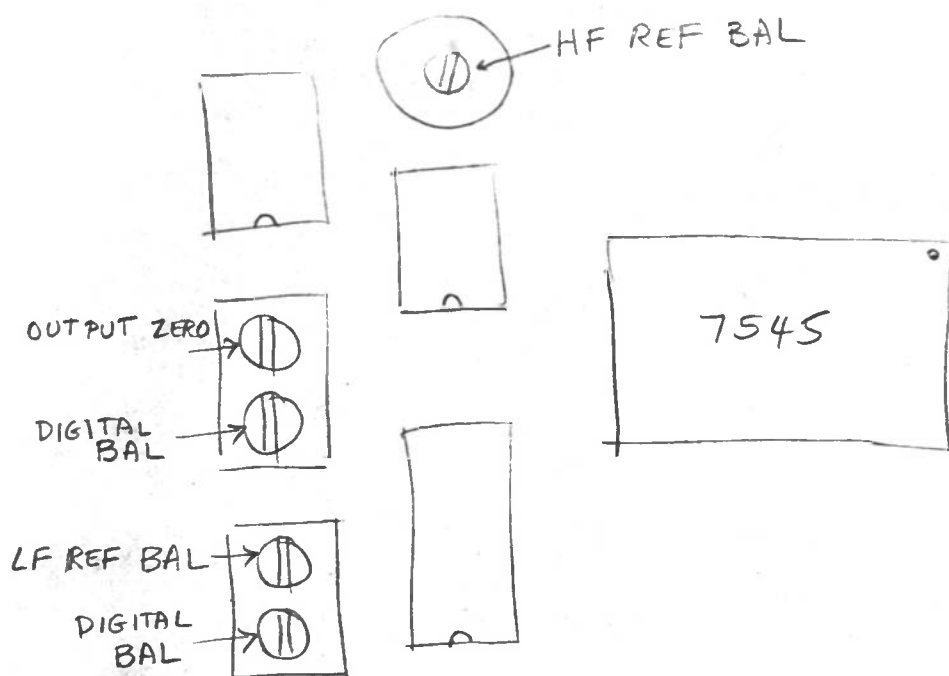


UNLESS OTHERWISE SPECIFIED
ALL RESISTORS ARE 1/4W, 5%
ALL CAPACITORS ARE IN MFD's
▽ DENOTES ANALOG GROUND
* DENOTES BECKMAN 694-3-RIOKD
4 RESISTOR NETWORK
** 9 RESISTOR NETWORK, 2.0K EACH

REV:

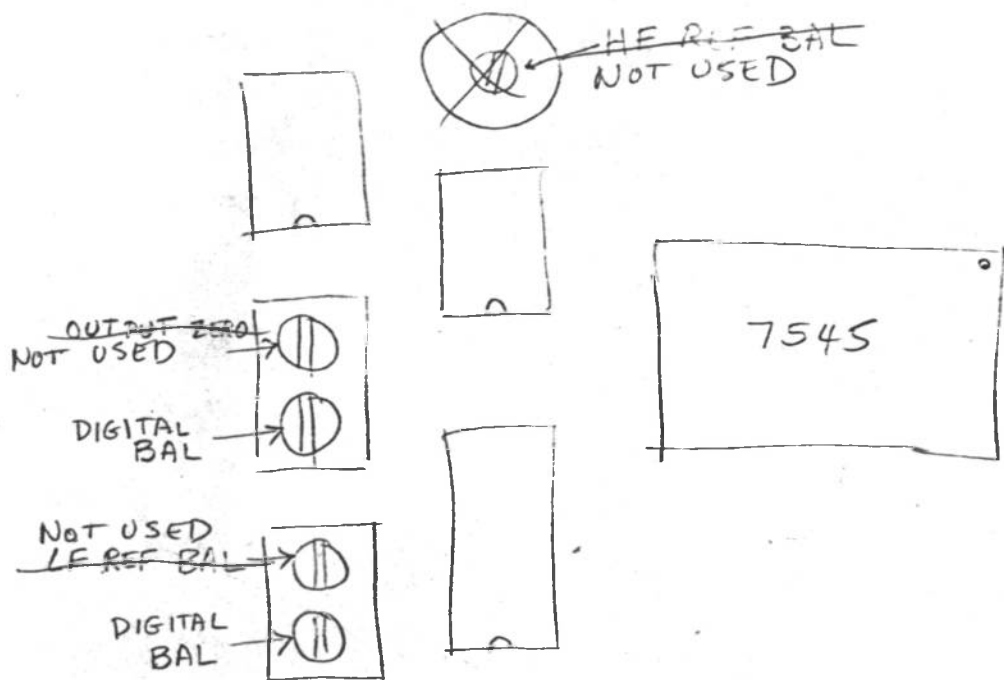
COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223				
SCALE	REVISIONS	BY	DATE	
NONE				
DATE				
DRN E.R. CKD				
APVD				
TITLE			NO	
12 BIT MDAC's (ONE OF FOUR PER BOARD)			CIC-1-6 SHEET 2 OF 2	

CIC-1-1 ADJUSTMENTS



1. a. Feed $\pm 10V$, $1KHz$ square wave to Ref. input
 b. Set digital data to "800".
 c. Adjust LF REF BAL for min. $1KHz$ at output
2. a. Ground Ref. input.
 b. Sequence digital data through all values with sequence program.
 c. Adjust both DIGITAL BAL for min. level variation at output
 d. Adjust OUTPUT ZERO for $0V$ d.c. at output
3. a. Feed $\pm 10V$, $100KHz$ square wave to Ref. input
 b. Set digital data to "800".
 c. Adjust HF REF BAL for min $100KHz$ at output.
 d. Alternate digital data between "7FF" and "800" and readjust HF REF BAL for the same p-p $100KHz$ feedthru at both values.

CIC-1-2 ADJUSTMENTS



1. ~~a. Feed $\pm 10V$, 1KHz square wave to Ref. input~~
~~b. Set digital data to "800."~~
~~c. Adjust LF REF BAL for min. 1KHz at output~~
2. a. Ground Ref. input,
 b. Sequence digital data through all values with sequence program.
 c. Adjust both DIGITAL BAL for min. level variation at output
~~d. Adjust OUTPUT ZERO for 0V d.c. at output~~
3. ~~a. Feed $\pm 10V$, 100 KHz square wave to Ref. input~~
~~b. Set digital data to "800."~~
~~c. Adjust HF REF BAL for min 100 KHz at output.~~
~~d. Alternate digital data between "7FF" and "800" and readjust HF REF BAL for the same p-p 100KHz frequency at both values.~~

CIC-1 MDAC TESTS

	DIGITAL DATA	ANALOG REF	ANALOG OUTPUT TO	TEST	-1	-2
SEQUENCE PROGRAM	0 thru FFF	0V	Scope	Zero adjust	$\pm 1\text{mV}$	$\pm 1\text{mV}$
	↓	0V	↓	RPU Strobe feedthru	2mV pp	2mV pp
	↓	+10V	↓	Linearity about 800		
	↓	+10V		Settling $< 2\mu\text{S}$	—	
	↓	-10V	↓	Settling $< 2\mu\text{S}$	—	
STATIC VALUES	7FF, 800, 800	+10V	Scope	Linearity		
	↓	1KHz SW	↓	LF Balance $< 3\text{m}$	Adj R	$< 3\text{mV pp}$
	↓	100KHz SW	↓	HF Feedthru, Ca	Adj C	3.5V pp -4V pp
	0, 400, C00, FFF	↓	↓	Transient response		
	0, 7FF, 800, FFF	+10V	↓	Oscillation, noise		
	↓	+10V	DVM	DC gain		
0	80					
1	40					
2	200					
3	60					
				LF HF		
				1 ✓ ✓ 150		
				2 ✓ ✓ 150		
				3 ✓ ✓ 125		
				4 ✓ ✓ 100		

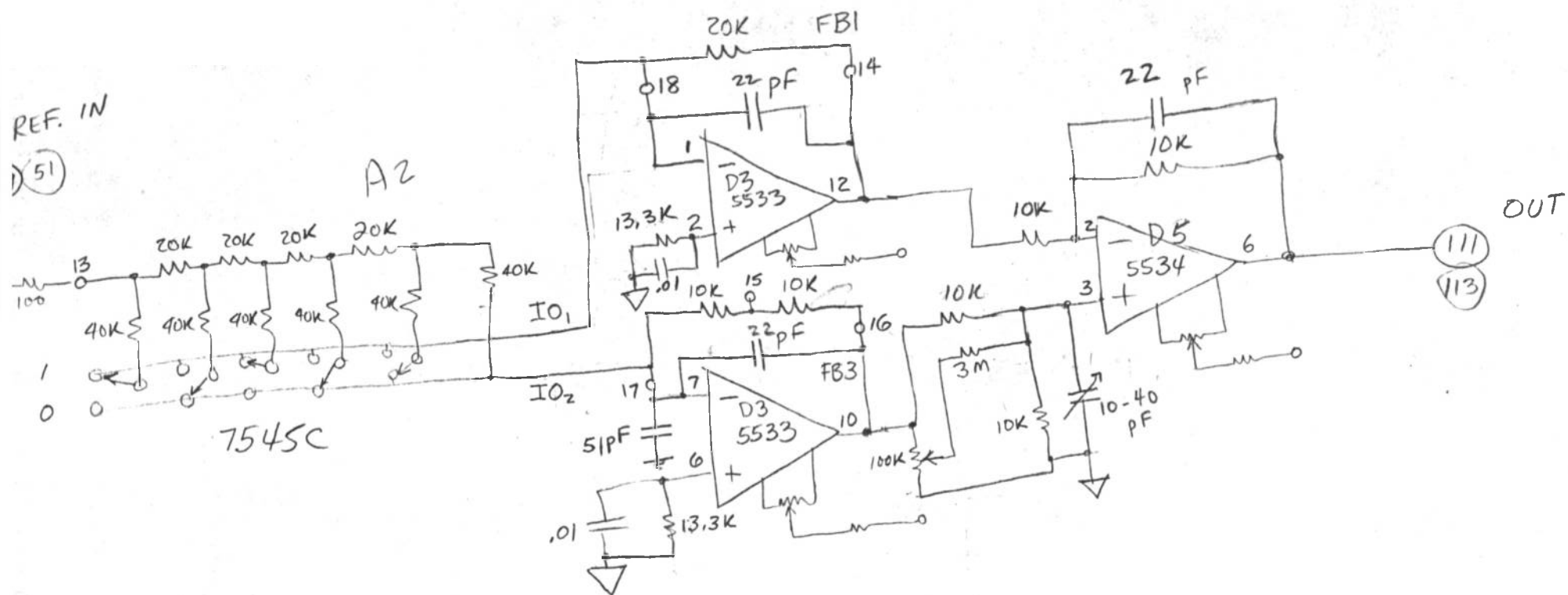
35k

CIC-1-1

	BEFORE CONV	AFTER CONV 1	CONV 1	NO CONV
+2047	011111	111111	+10	0
0	000000	100000	0	-10
-1	111111	011111	0	+10
-2048	100000	000000	-10	0
4095	111111	011111	0	+10
2048	100000	000000	-10	0
2047	011111	111111	+10	0
0	000000	100000	0	-10

CIC-1-2

	BEFORE CONV	AFTER CONV 2	CONV 2	NO CONV	CONV 1
+2047	011111	000000	+10	0	-10
0	000000	011111	0	+10	0
-1	111111	100000	0	-10	0
-2047	100000	111111	-10	0	+10
4095	111111	100000	0	-10	
2048	100000	111111	-10	0	
2047	011111	000000	+10	0	
0	000000	011111	0	+10	



CO₁ CO₂

100000 125 70
011111 200 150

Adj pot for ^{LF} null at 800

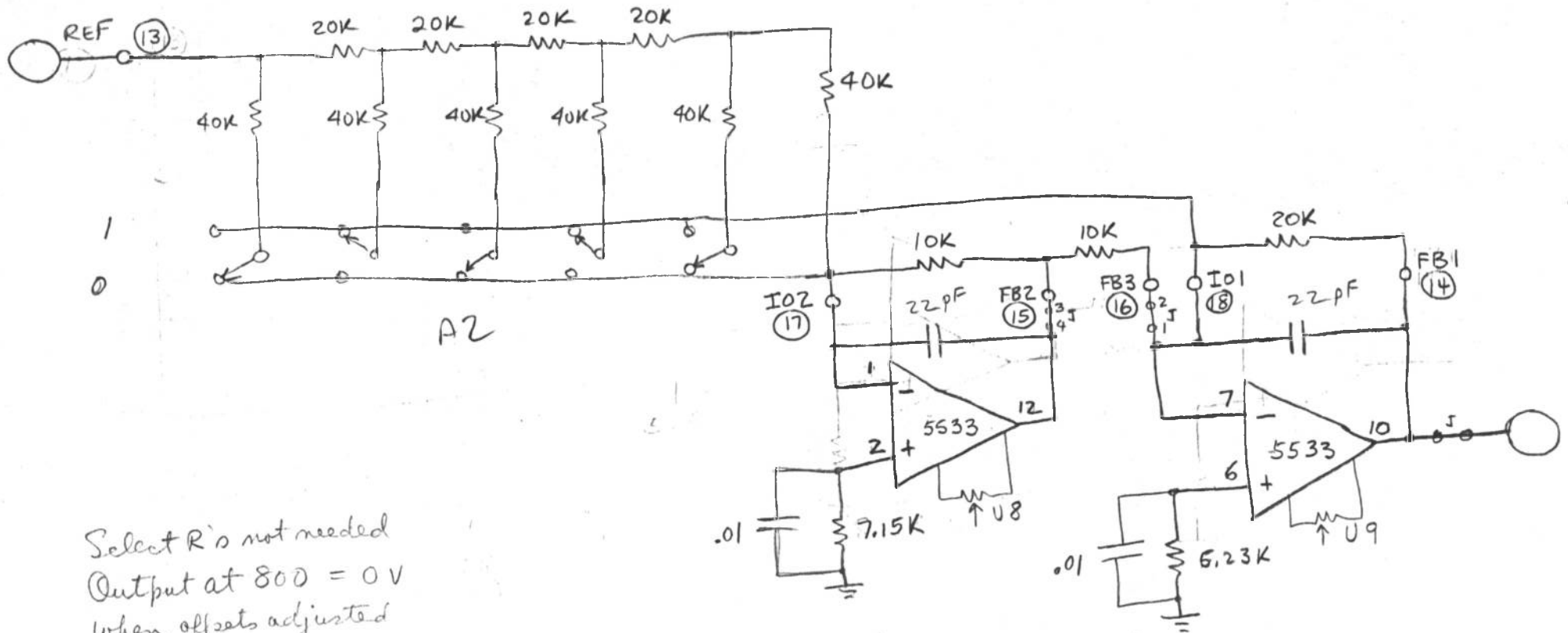
Adjust CVARI for HF null at 7FF and 800

11111 +V_{REF}
10000 0V
00000 -V_{REF}

TYPE 1 + 4
8 or 12 BIT MDAC (HF)
OFFSET BINARY

7FF/800 feed thru: 80 mV 100KHz S_g^{1/16}

BECKMAN 7545C



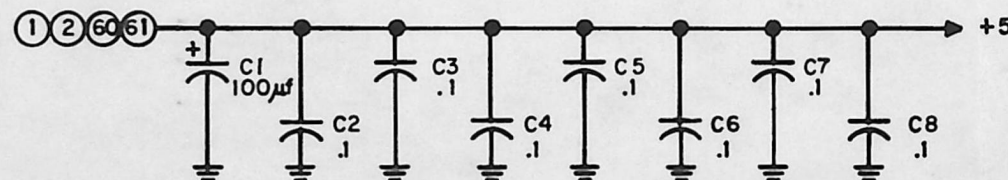
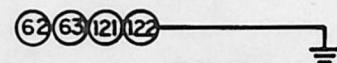
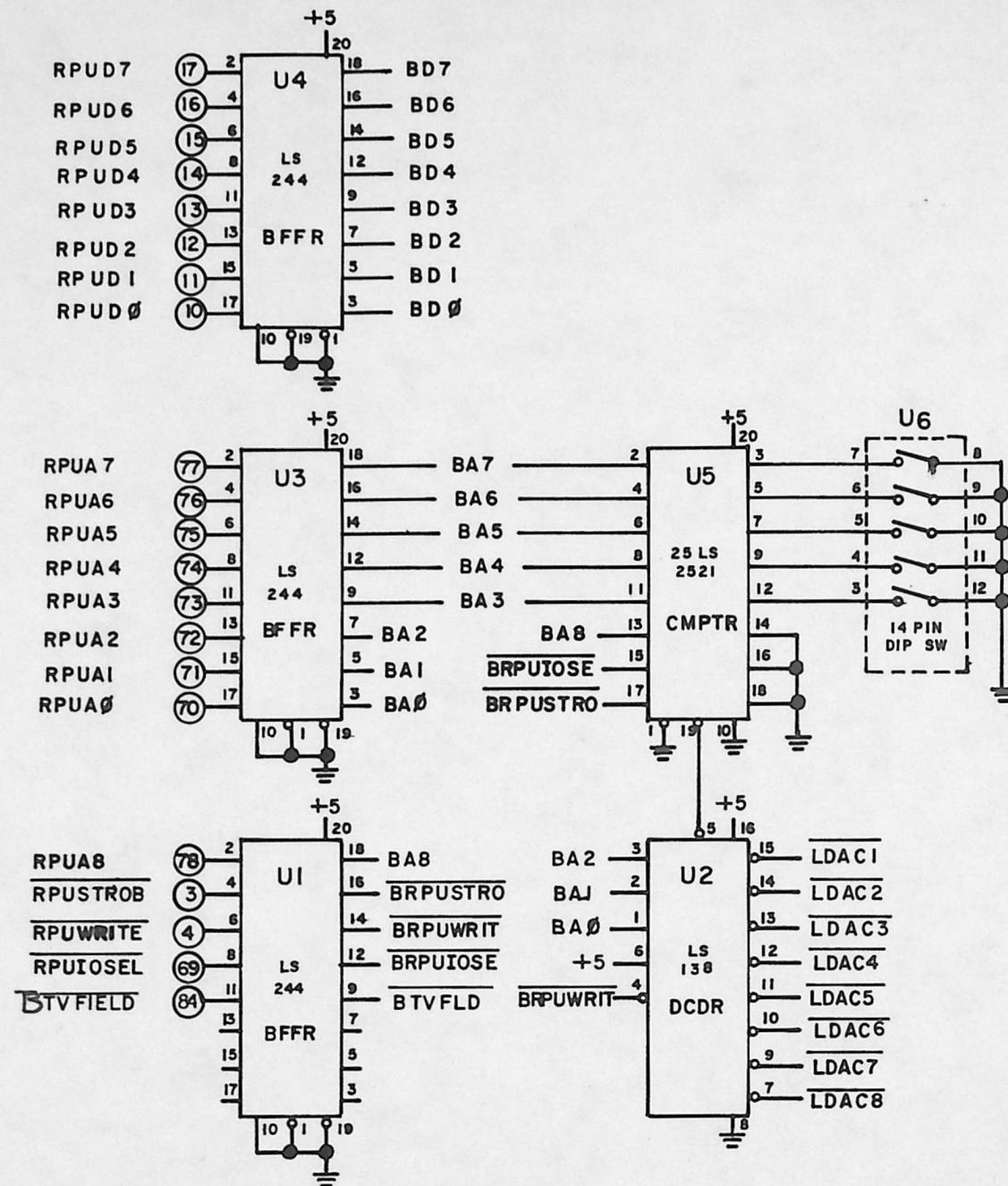
Select R's not needed
Output at 800 = 0V
When offsets adjusted

FFF	11111	$-V_{REF}$
800	10000	0V
7FF	09999	0V
0	00000	$+V_{REF}$

TYPE-2 +5

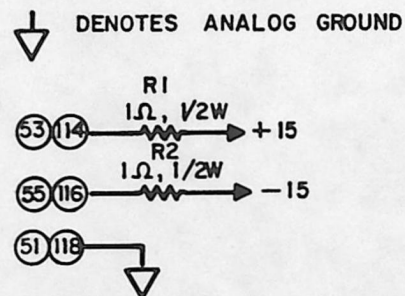
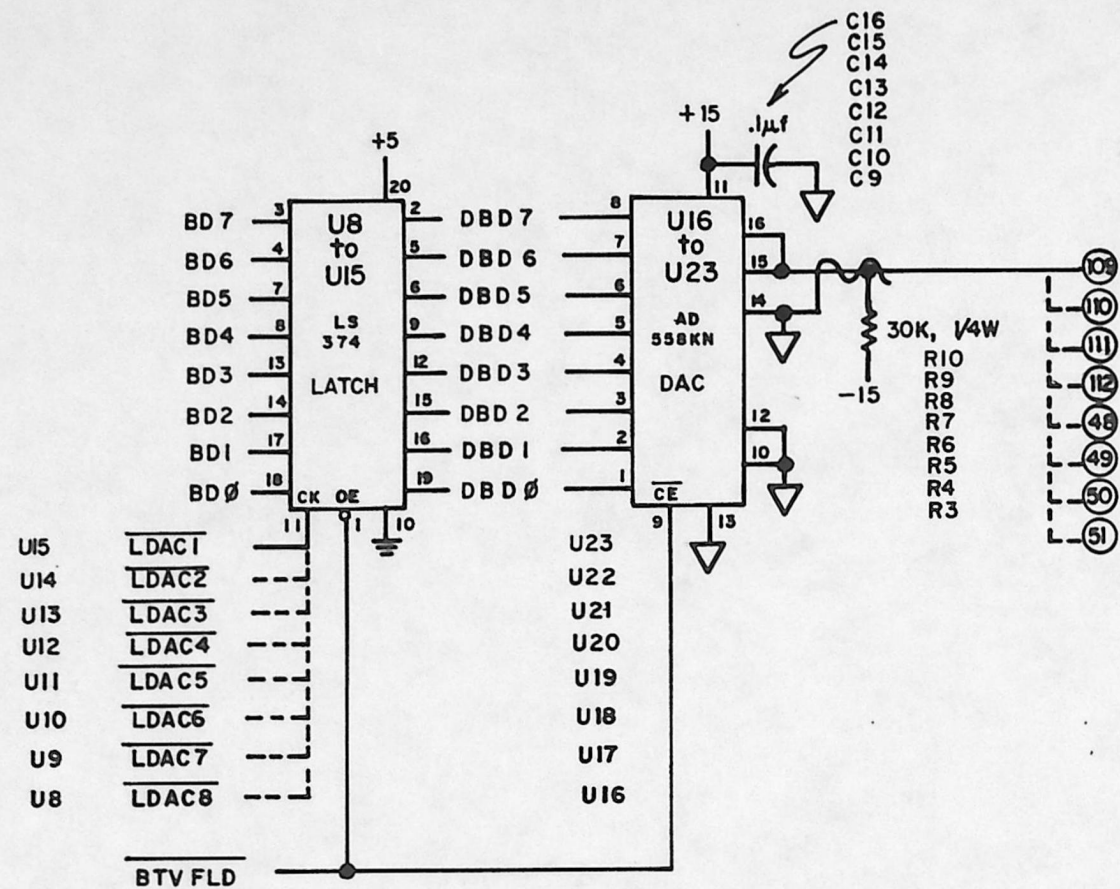
LF MDAC

Use with +10V REF. for 12 BIT DAC



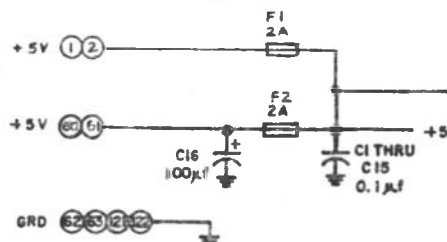
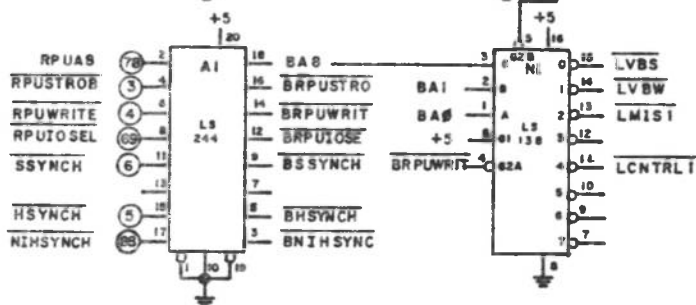
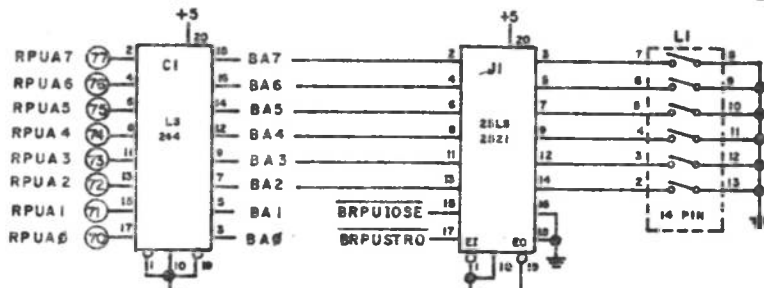
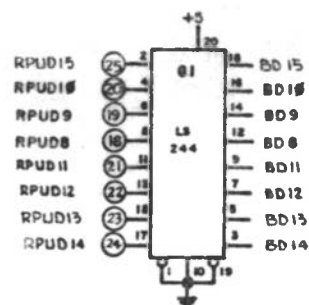
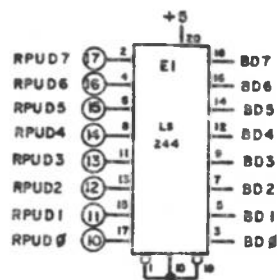
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COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 6-30-81	PER W.C.A. PRINT	E.R.	3-10
DRN E. R.	CRD		
APVD			
TITLE	DIGITAL INTERFACE 8 BIT DACS		NO CIC-5 SHEET 1 OF 2



REV: 1;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 6-30-81	PER W.C.A. PRINT	E.R.	3-10
DRW E.R.	CRD		
APVD			
TITLE 8 BIT DACS (ONE OF EIGHT)		NO CIC-5 SHEET 2 OF 2	

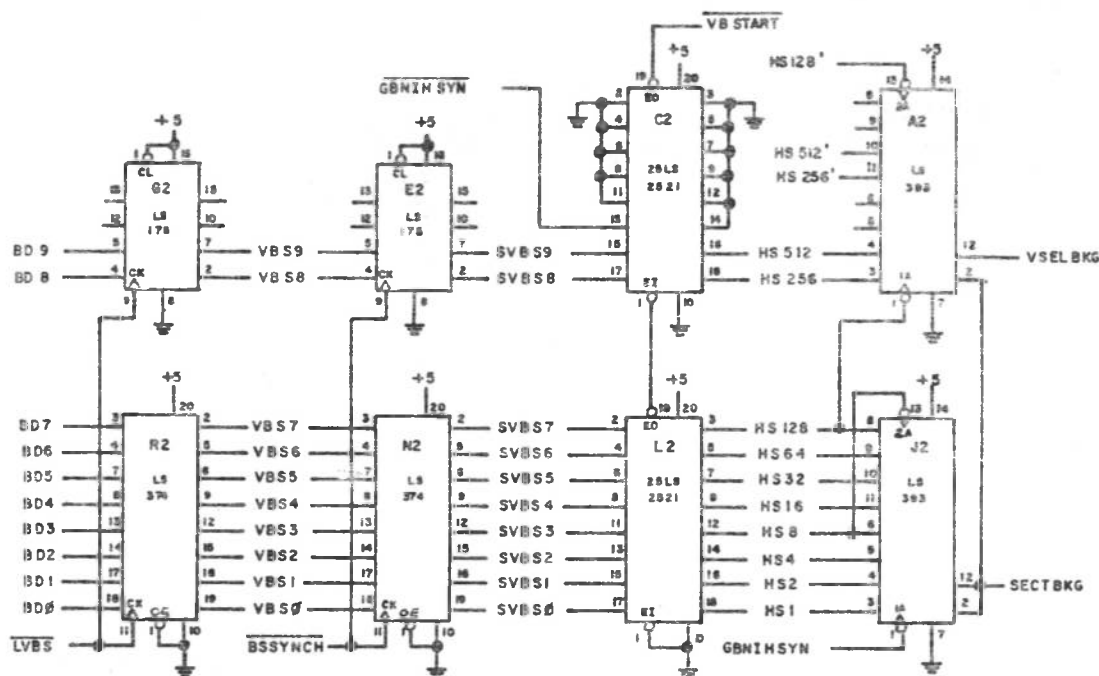


UNLESS OTHERWISE SPECIFIED
ALL LS NUMBERS ARE 74LSXXX

NO OVERLAP

REV: A;A-1;B;

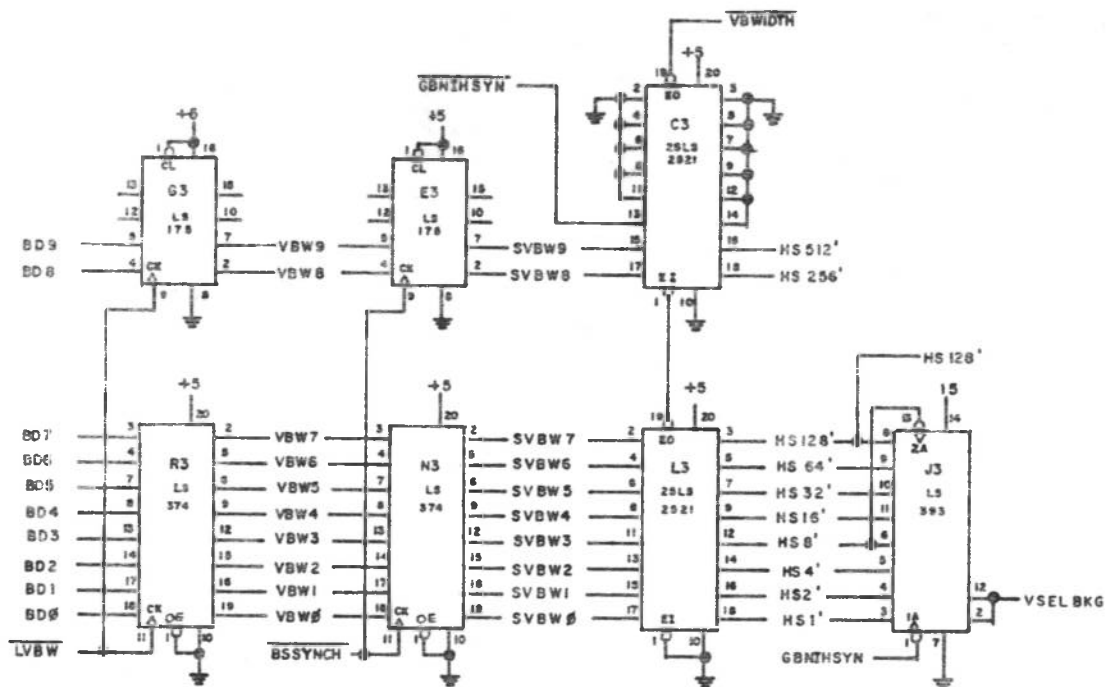
COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	REVISIONS	BY	DATE
NONE			
DATE 9-14-81	PER W.C.A. PRINT	E.R.	3-10
BY E.R. CRB	" " "	E.R.	5-10-82
APVD	"A" = ECN425	EVL	9-27-82
TITLE		CIC-W-13	
DIGITAL CONTROL FOR VERTICAL SELECTIVE BLANKING AND FUNCTION DECODER		SHEET 1 OF 4	



NO OVERLAP

REV: A, J, B;

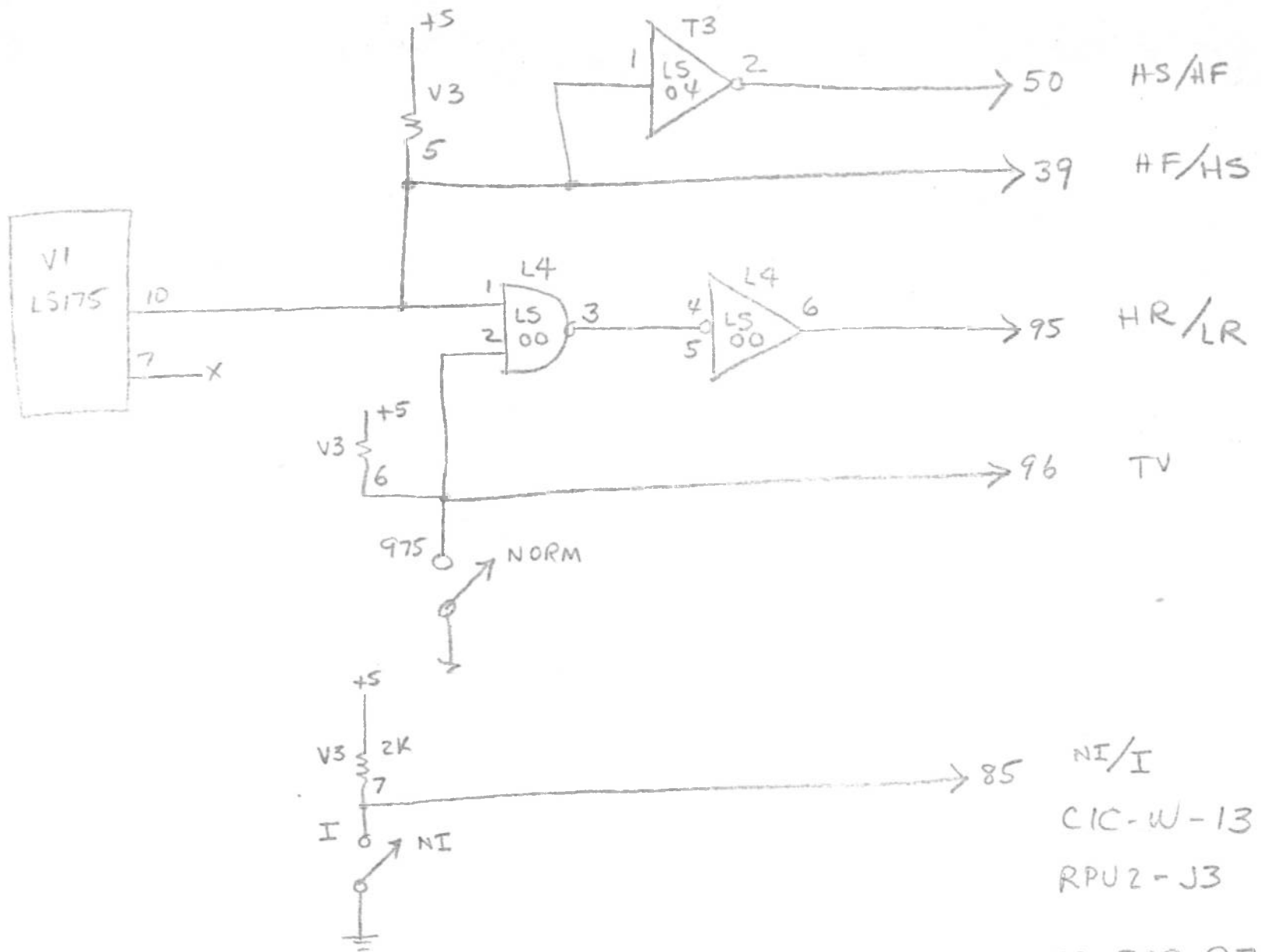
COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	NONE	REVISIONS	BY DATE
DATE	9-14-81	PER W.C.A. PRINT	E.R. 3-10
DRN	E.R.	CRS	
APV			
TITLE		VERTICAL SELECTIVE BLANKING START CONTROL FOR VERTICAL SELECTIVE BLANKING AND FUNCTION DECODER	
		CIC-W-13 SHEET 2 OF 4	



NO OVERLAP

REV: A1A-HB:

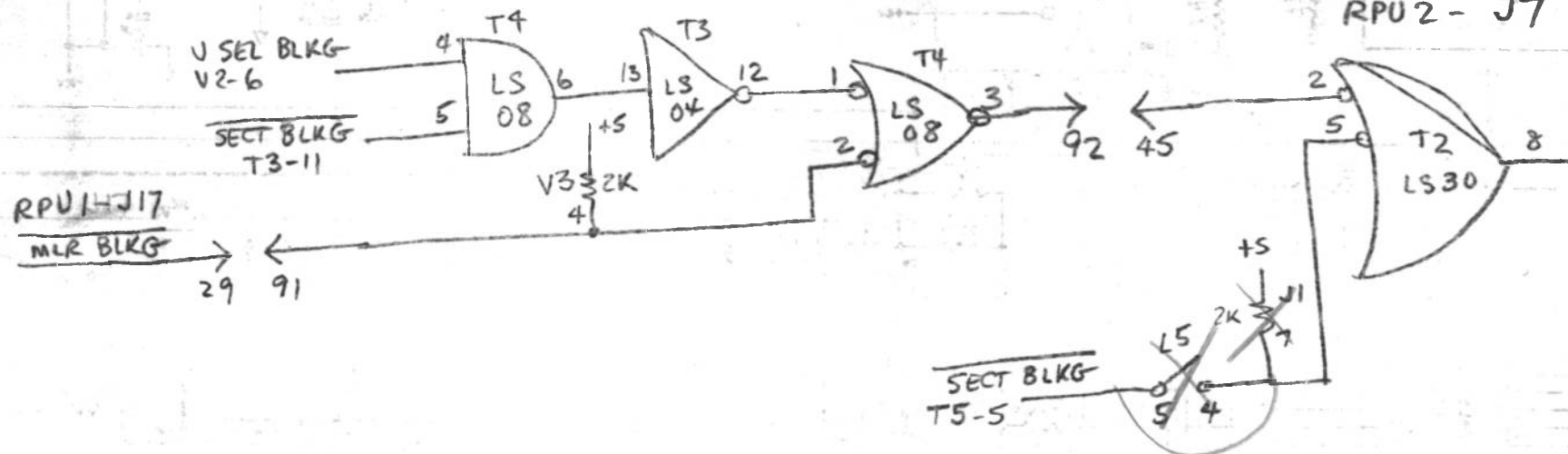
COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	NONE	REVISIONS	BY DATE
DATE	9-14-81	PER WCA PRINT	E.R. 3-10
DRN	E.R.	CRS	
APVD			
TITLE	VERTICAL SELECTIVE BLANKING WIDTH CONTROL FOR VERTICAL SELECTIVE BLANKING AND FUNCTION DECODER		NO
			CIC-W-13
			SHEET 3 OF 4



MOD FOR 975 L
& NI

CIC-W-13 Sh4
RPU2-J3

CIC-W-22
RPU2-J7



MLR
RPU

VERT SEL BLKG / FUNCT

DECODER
 CIC-W-13

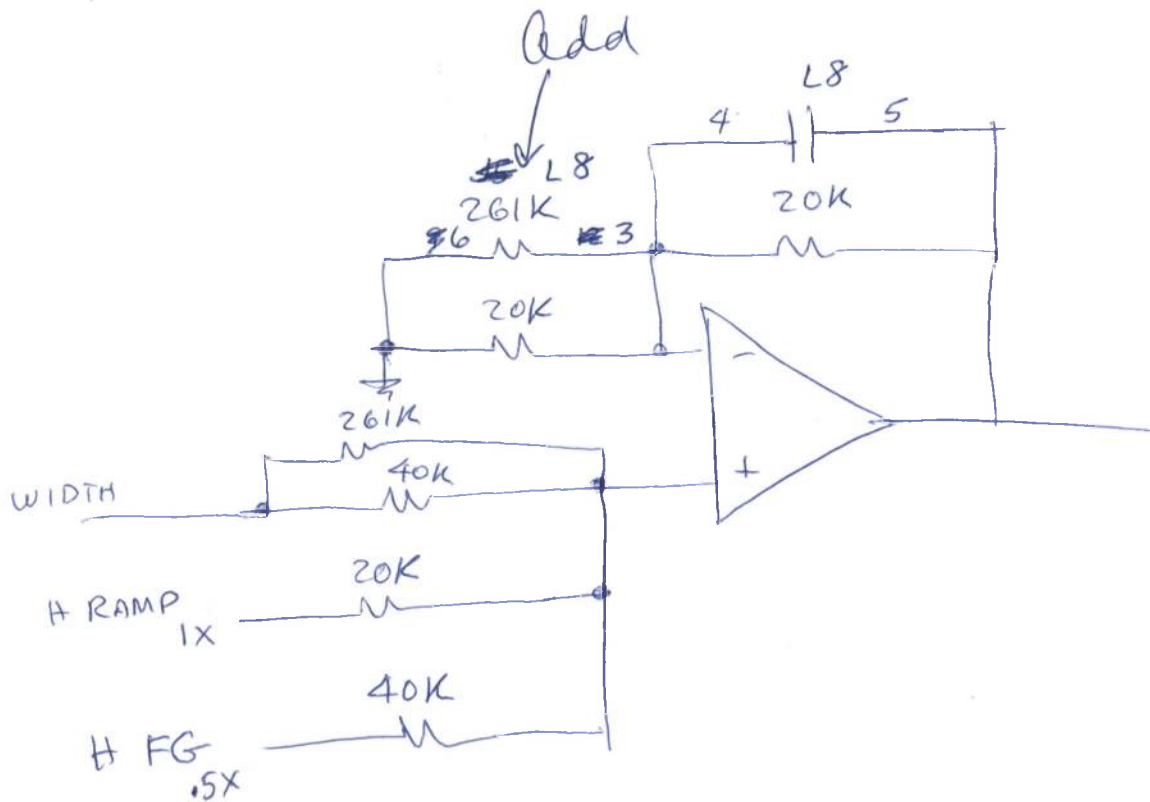
PARAMETER	EC ADR	PAR ADR (HEX)	PAR VALUE (HEX)
VERT BLKG START	6	40	0 to FFF
VERT BLKG WIDTH	7	41	0 to FFF
CAM A	52	42	X 0 0
CAM B			X 0 1
RED			X 0 2
GREEN			X 0 3
BLUE			X 0 4
LUM			X 0 5
AUX 1			X 0 6
AUX 2			X 0 7
SPEC BLKG			1 0 X
EXT BLKG			2 0 X
SECT OVERLAP	↓	↓	4 0 X
OVERLAP	F	43	1
H1 RES, TV	C=0	140	3 0 X
H1 RES, FILM			2 0 X
LO RES, TV			1 0 X
LO RES, FILM			0 0 X
3D			X 0 1
2D	↓	↓	X 0 0

NOTE: DECODE SW. 6 "ON"

X = DON'T CARE



VERT. SEL. BLKG.



Wire L8-3 to L8-4 needed
to complete mod.

$$\begin{array}{r} .05 \\ .025 \\ \hline .0038 \\ .0788 \end{array}$$

$$\frac{18.576}{38.576} \times \frac{38.5}{18.5}$$

$$\frac{12.69}{52.69} \times \frac{38.576}{18.576} = 1$$

Raster Gen
CIC-W-21

Edited 1-12-84

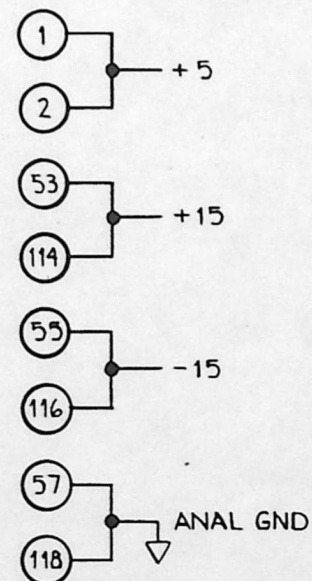
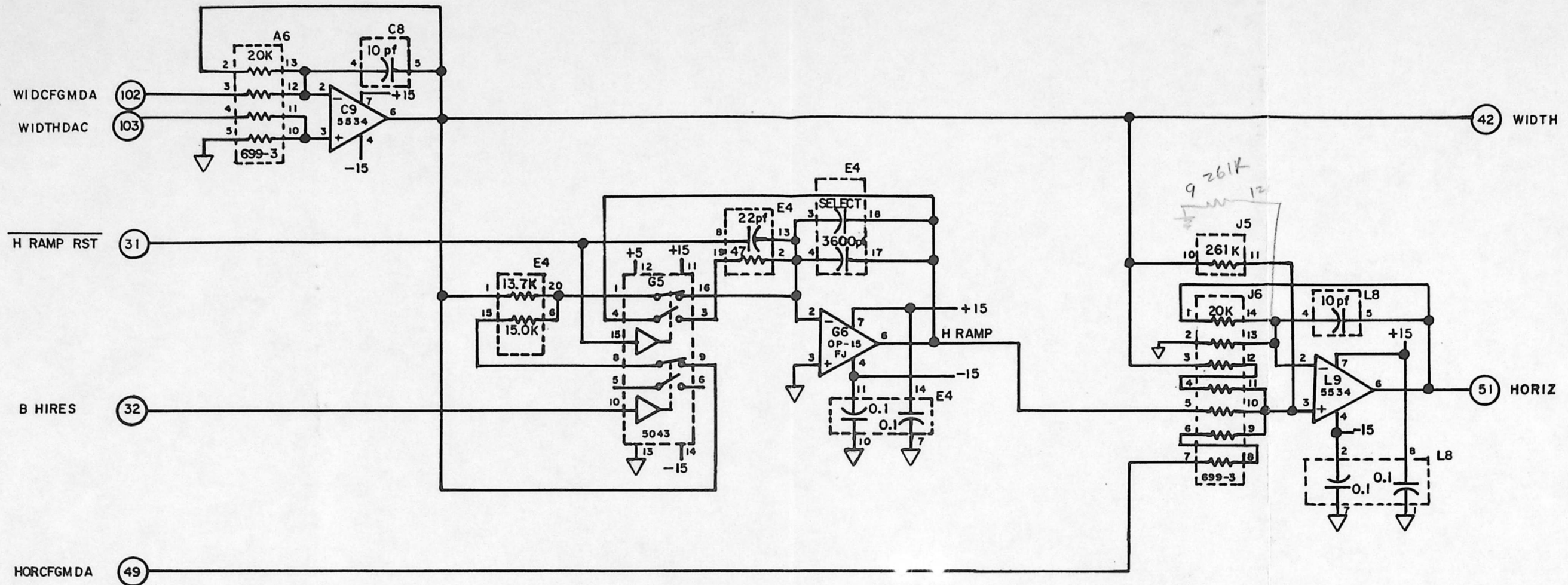
Found was done 9-14-84

Absolute Value

Look at AV output (pin 112)
Start with 2 ^{equal} sections
Both sect Set SIZE = 2047
" WIDTH = 0
" SIC = 2
Sect 1 Set HEIGHT = 2047
Sect 2 " " -2047
Adjust ABS. VAL offset for same DC level
in both sections

Square Root

Look at SQ ROOT output (pin 104)
Start with default raster
Set SIZE = 2047
" WIDTH = 0
WFG Amp = 10, ^{FG}AMP = 2047
Turn on WIDTH FG, LF TRIANGLE
✓ Adjust SQ RT offset for sharpest negative



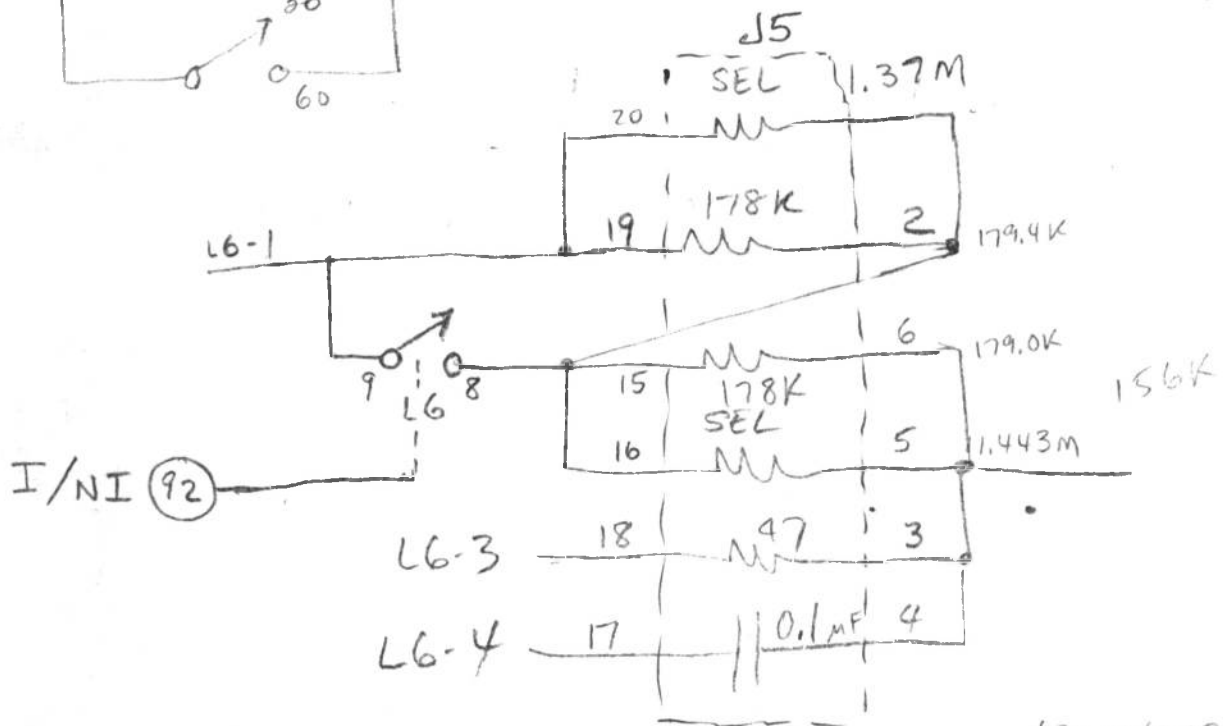
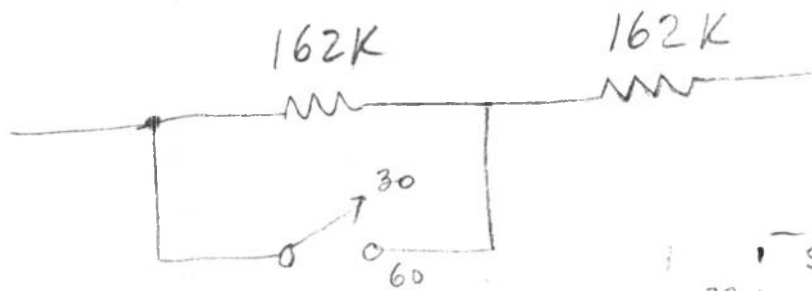
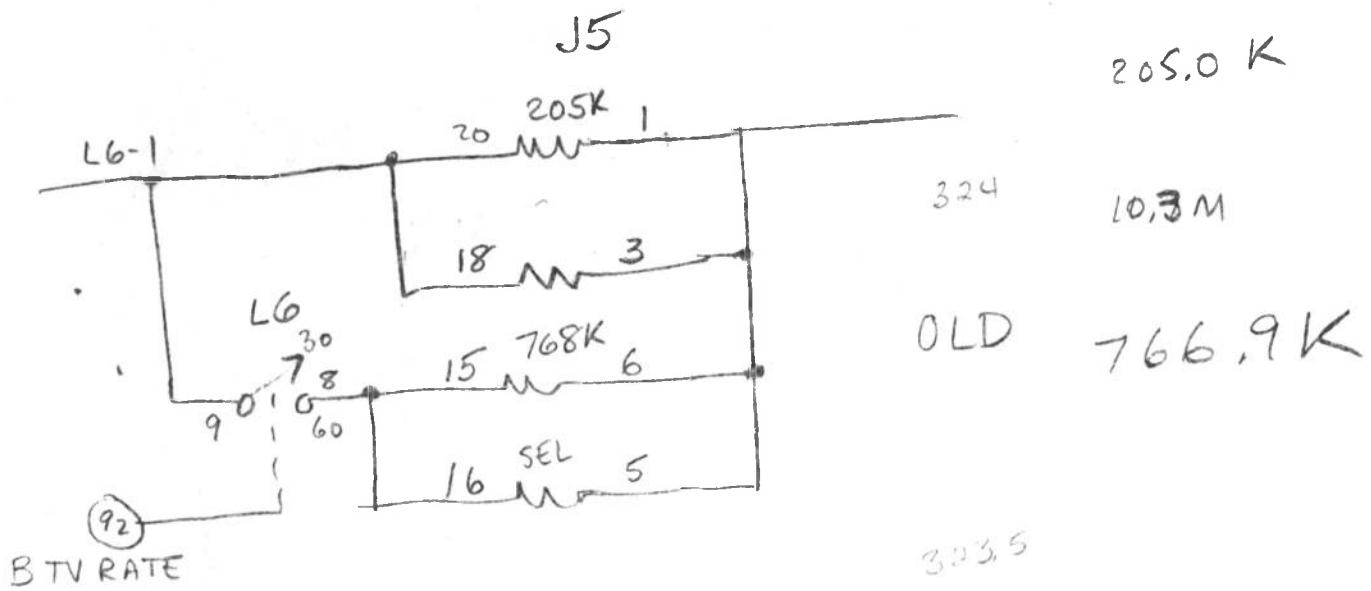
NOTES:

5534 IS NE 5534 AN OP AMP.
 5043 IS H11-5043-5 ANALOG SWITCH.
 699-3 IS A 14 PIN DIP WITH 7 IDENTICAL RESISTORS.
 694-3 IS A 8 PIN DIP WITH 4 IDENTICAL RESISTORS.
 ALL DIODES ARE IN3064 UNLESS OTHERWISE NOTED.
 ALL THREE PLACE VALUE RESISTORS ARE 1%, 1/8 W

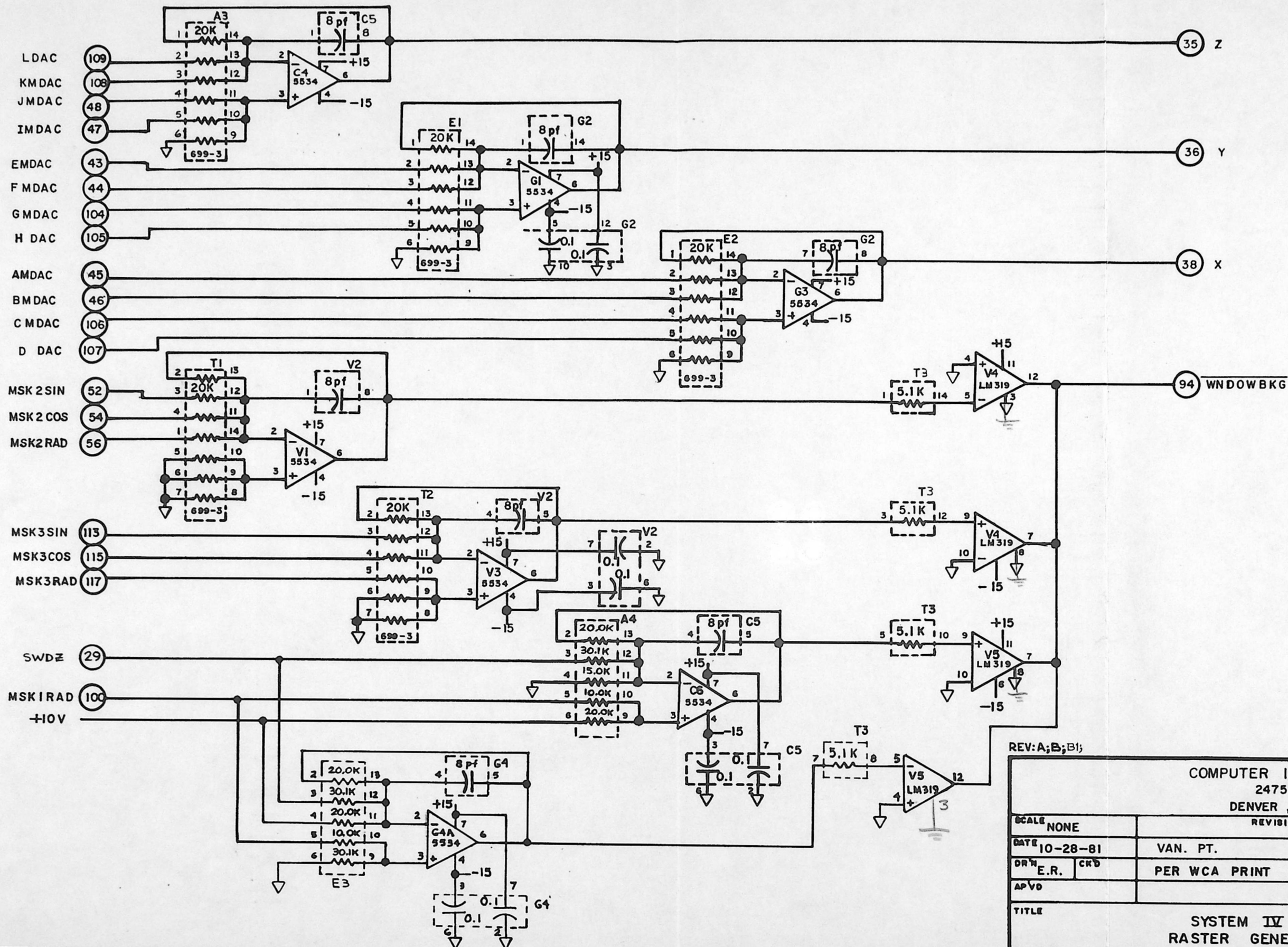
REV: A;B;B1;

COMPUTER IMAGE CORPORATION			
2475W. 2nd AVE DENVER, CO. 80223			
SCALE	REVISIONS	BY	DATE
NONE			
DATE 10-28-81	PER WCA. MARKED PRINT	E.R.	3-11
DRN E.R. CRD	VAN. PT.	E.R.	4-15
APVD	PER WCA PRINT (ECN #10)	E.R.	5-10-82
TITLE		SYSTEM IV RASTER GENERATOR	
		NO CIC-W-21 SHEET 1 OF 5	

WRONG REV



RASTER GEN
CIC-W-21 Sh2
116



REV: A; B; B1;

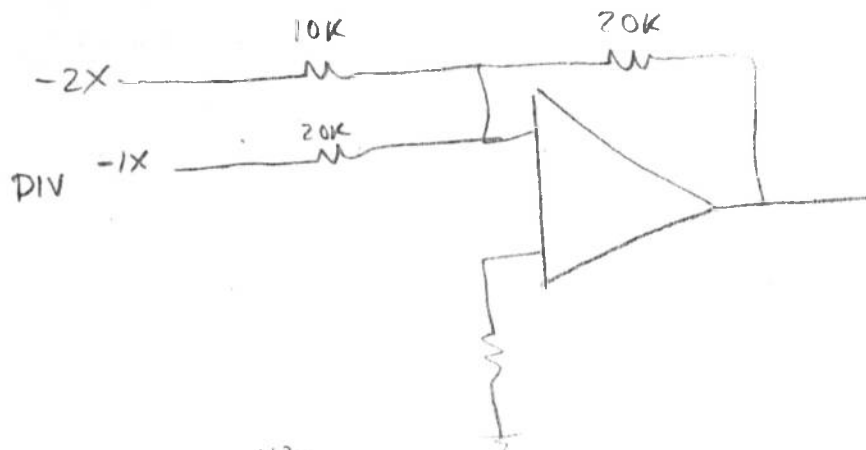
COMPUTER IMAGE CORPORATION
2475 W. 2nd AVE
DENVER, CO. 80223

SCALE	NONE	REVISIONS	BY	DATE
DATE	10-28-81	VAN. PT.	E.R.	4-15
DRN	E.R.	CKD	PER WCA PRINT	E.R.
APVD				

TITLE	SYSTEM IV RASTER GENERATOR	NO CIC-W-21 SHEET 3 OF 5
-------	-------------------------------	--------------------------------

WRONG REV

VG, V8



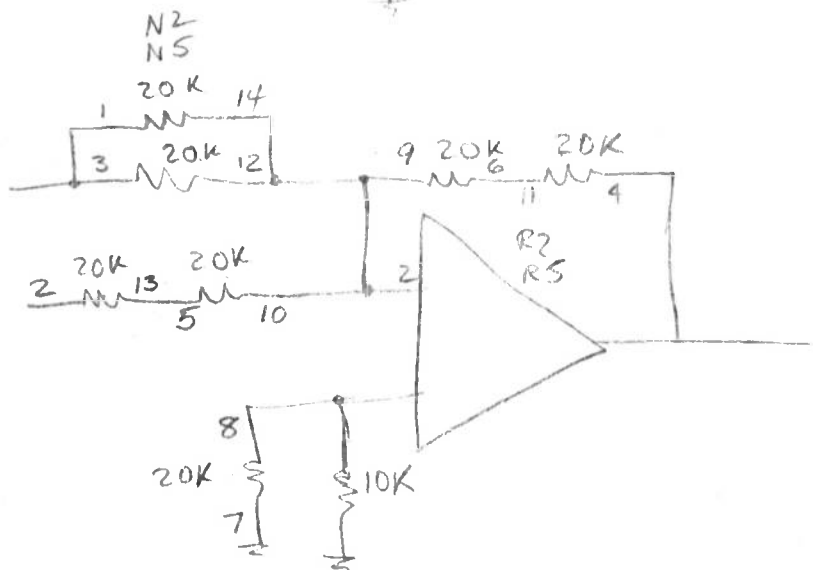
$\frac{1}{4}$

VG, V8

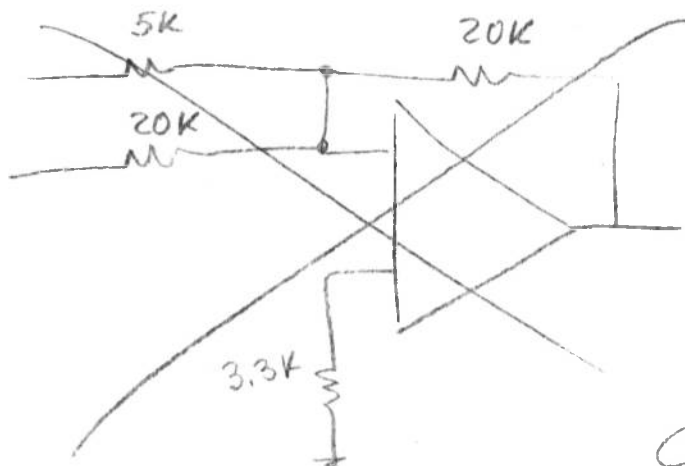
-4X

DIV.

-1X



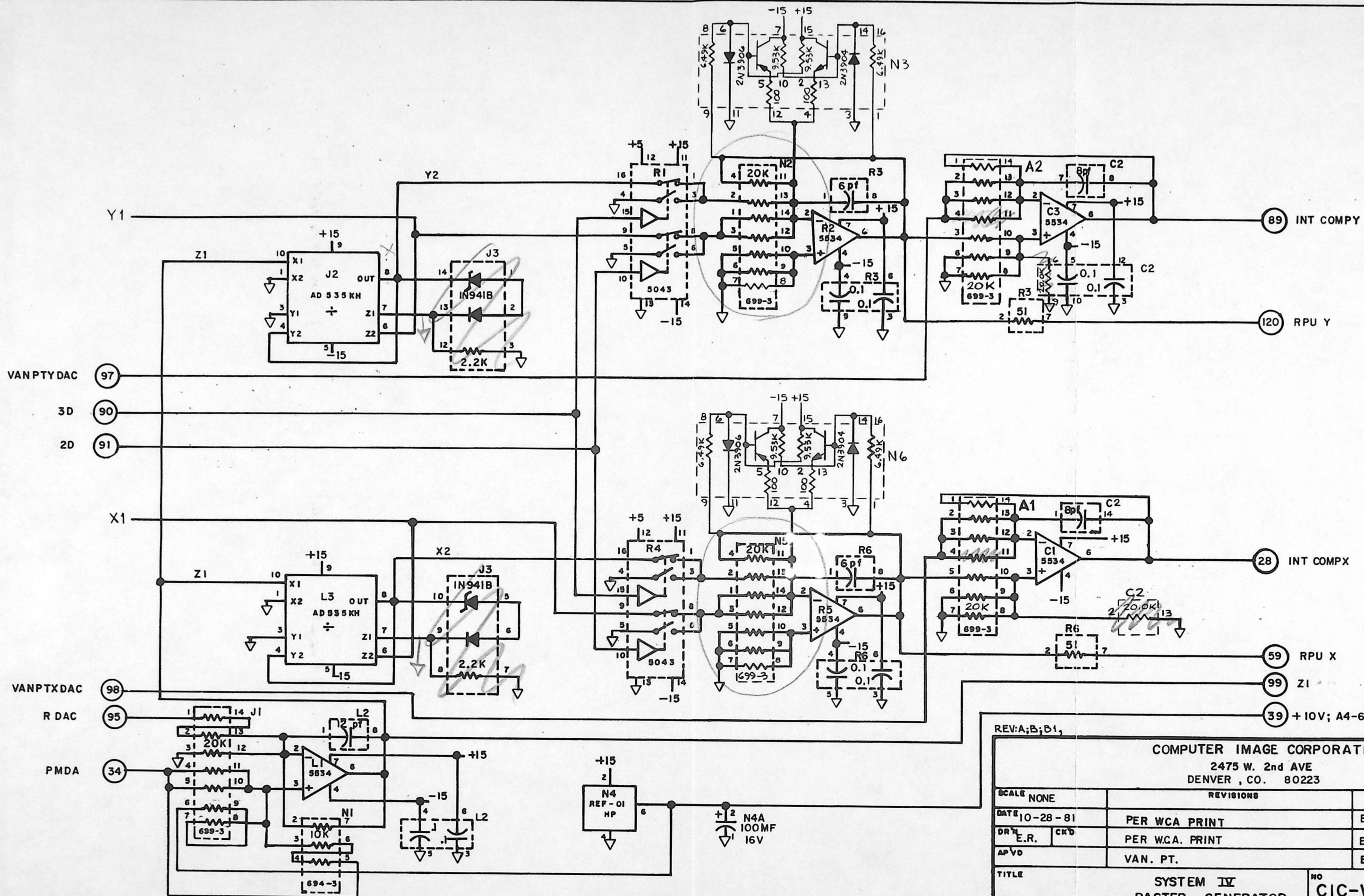
$\frac{48}{8} = 6$



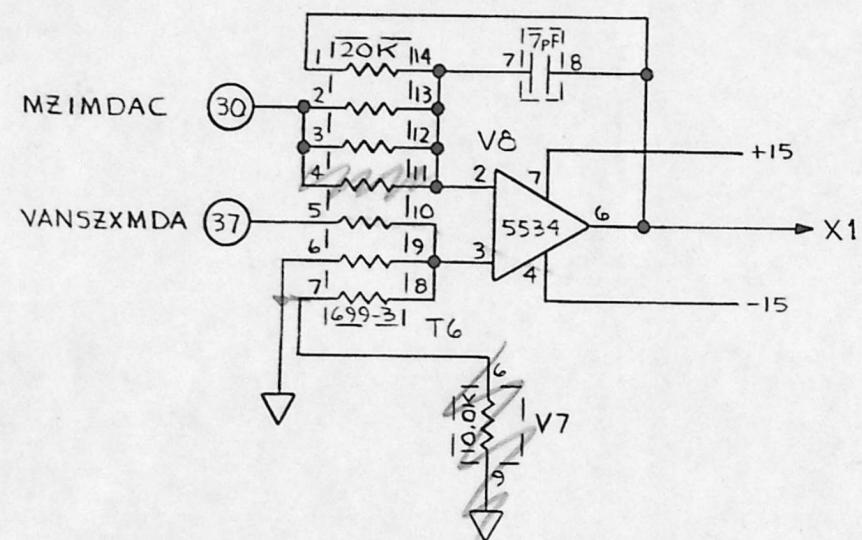
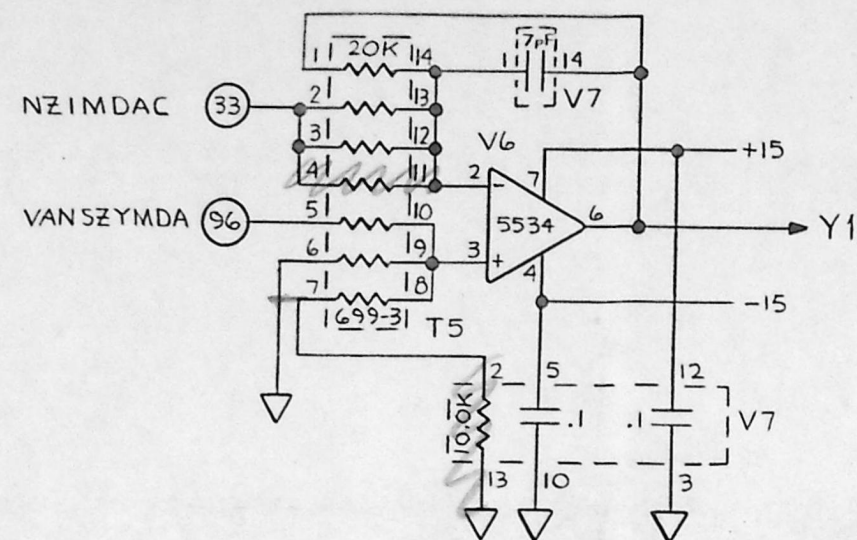
Raster Gen
CIC-W-21

Sh. 4

Mod to make raster size 2X
normal in 2D mode



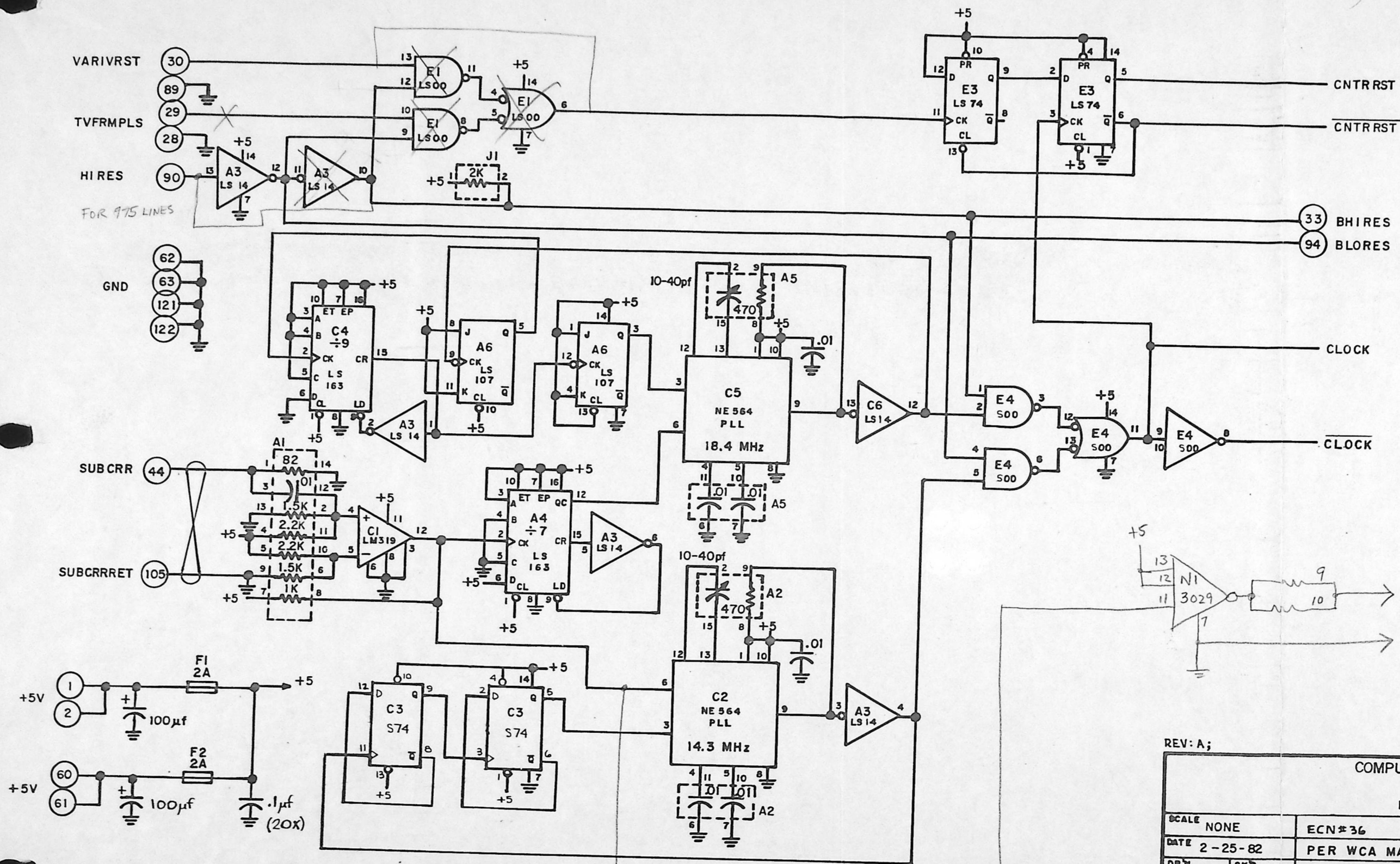
WRONG REV



REV: B1;

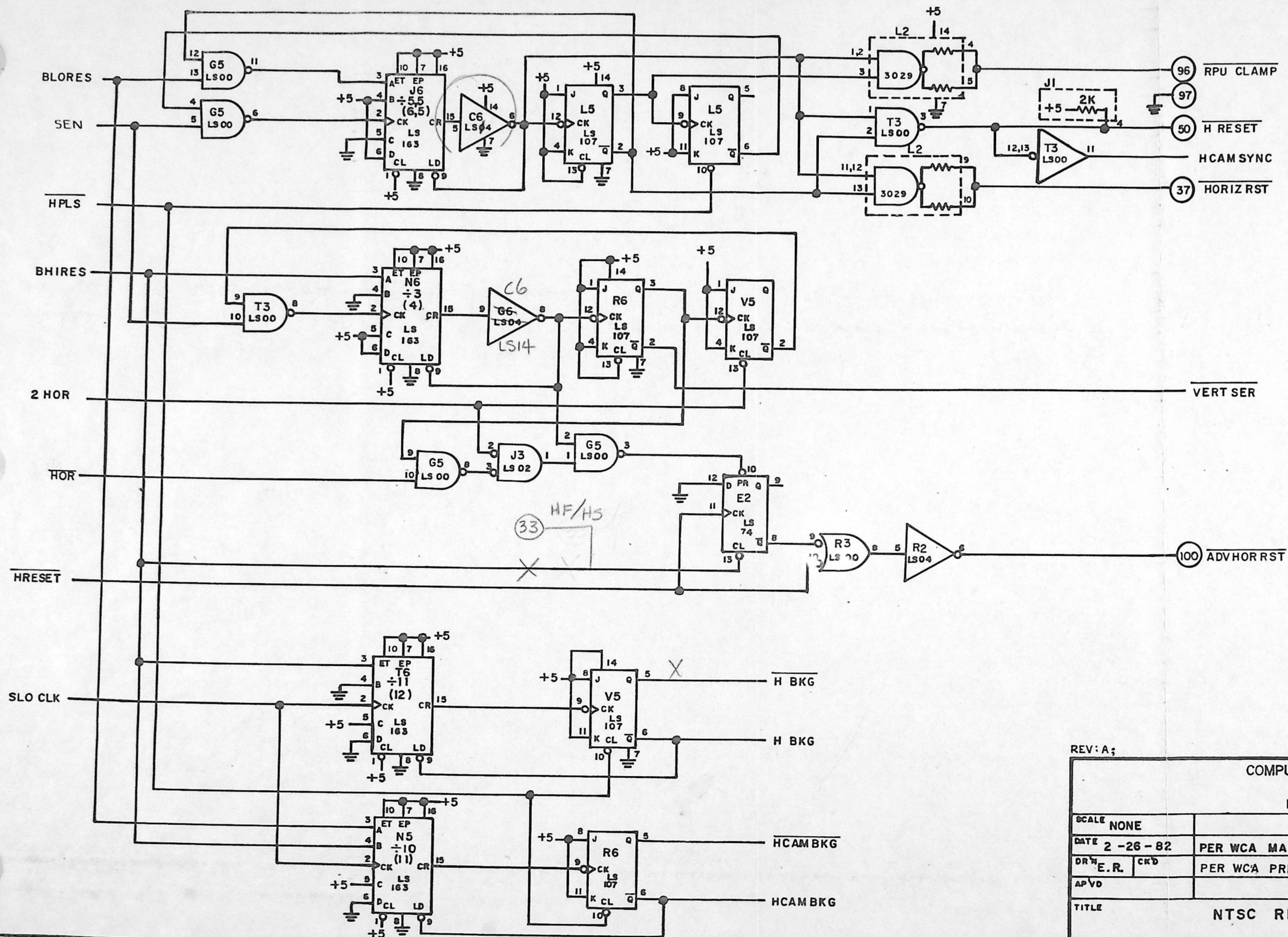
COMPUTER IMAGE CORPORATION 2475 W 2ND AVE DENVER, CO 80223			
SCALE	REVISIONS	BY	DATE
DATE 4/21/03			
DRN EVL CKD			
APVD			
TITLE SYSTEM IV RASTER GENERATOR		NO CIC-W-21 SHEET 5 OF 5	

WRONG REV



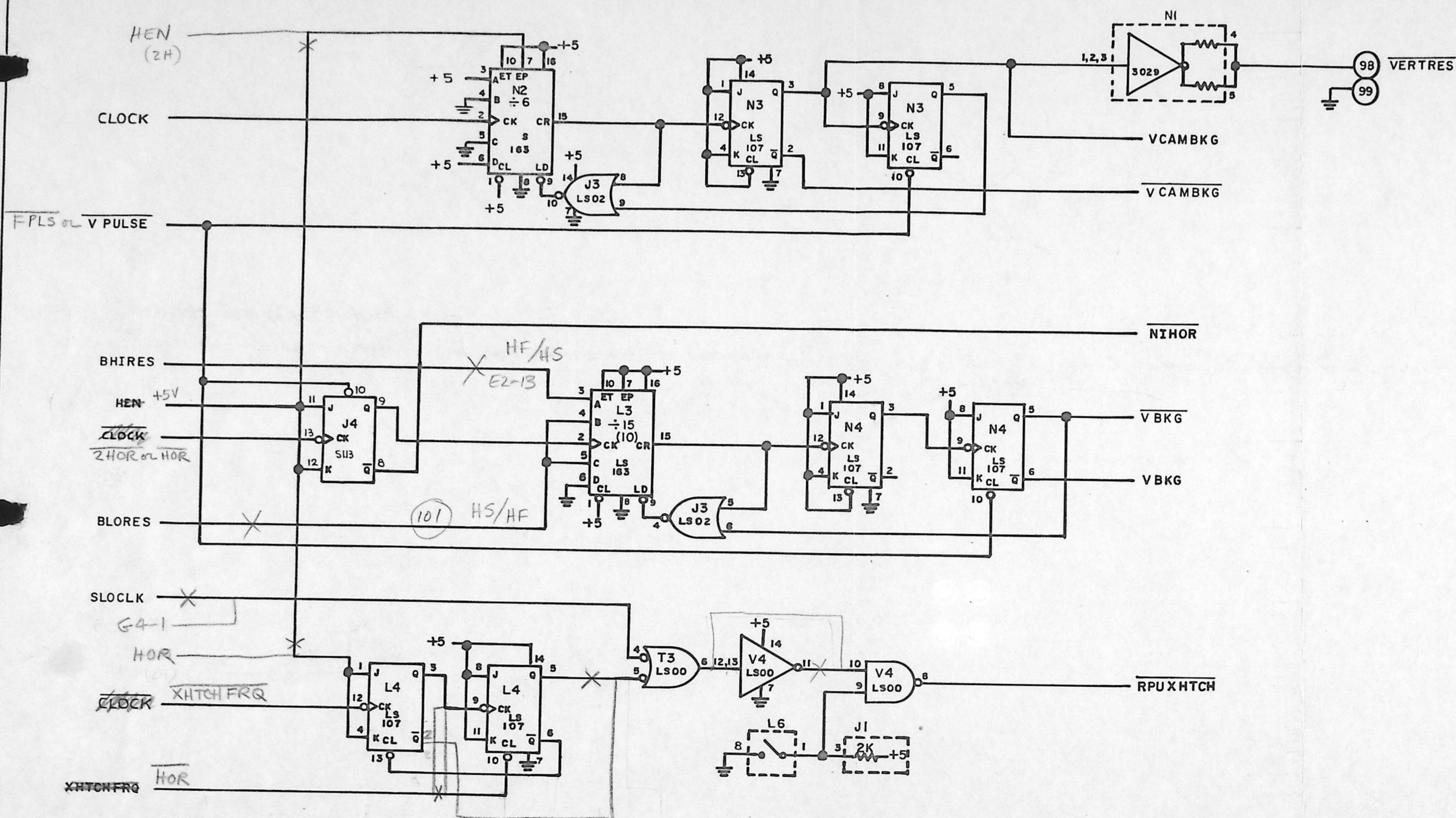
REV: A;

COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE NONE	ECN# 36	REVISIONS	BY EVL DATE 10/11/82
DATE 2-25-82	PER WCA MARKED PRINT		E.R. 4-27-82
DRN E.R. CKD	" " " "		E.R. 5-10-82
APVD	ECN# 39		EVL 10-26-82
TITLE NTSC RPU SYNC			NO
			CIC-W-22
			SHEET 1 OF 6



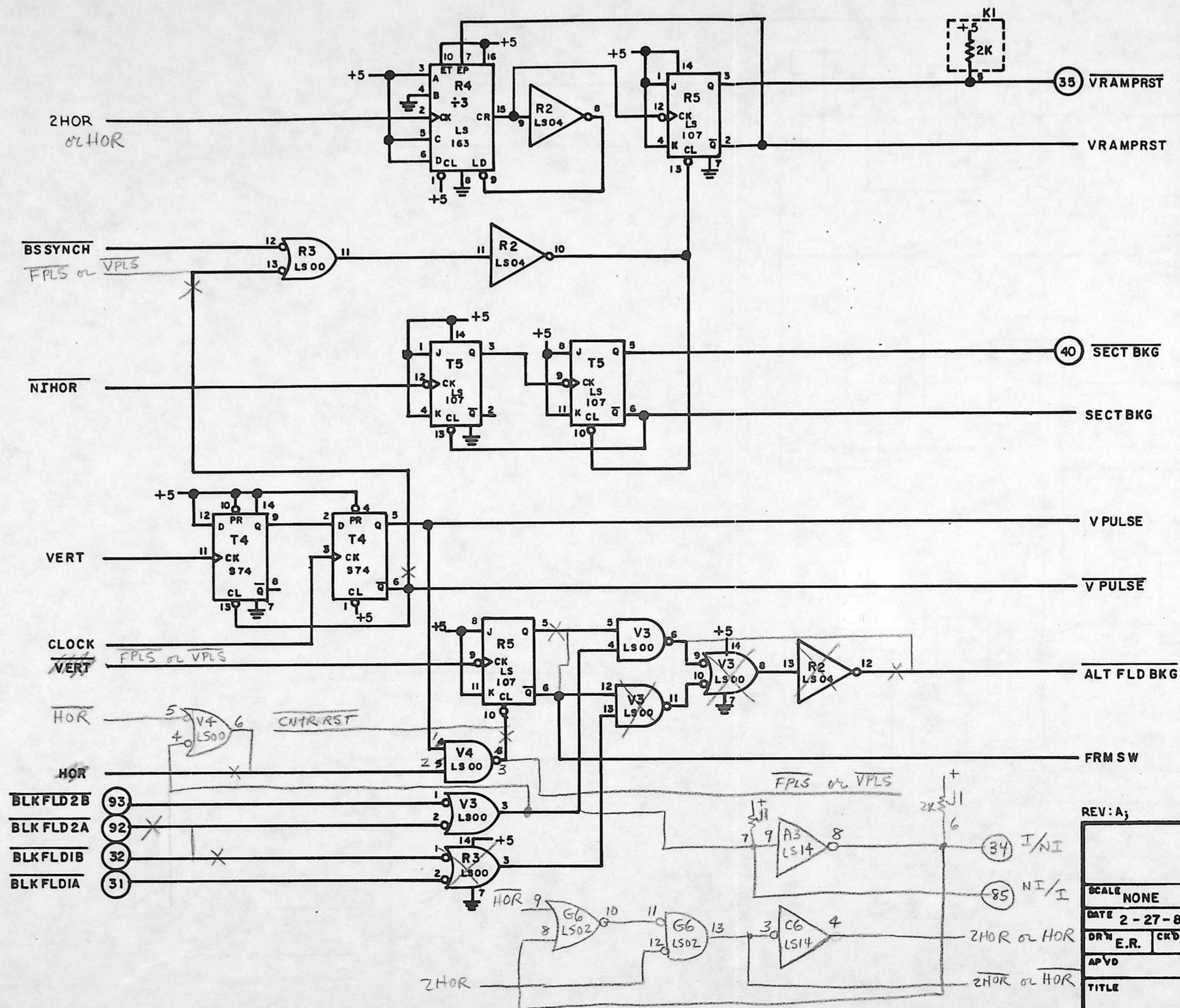
REV: A;

COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	REVISIONS	BY	DATE
NONE			
DATE 2-26-82	PER WCA MARKED PRINT	E.R.	4-27-82
DRW E.R. CRD	PER WCA PRINT	E.R.	5-10-82
APVD			
TITLE	NTSC RPU SYNC		
	CIC-W-22		
	SHEET 3 OF 6		



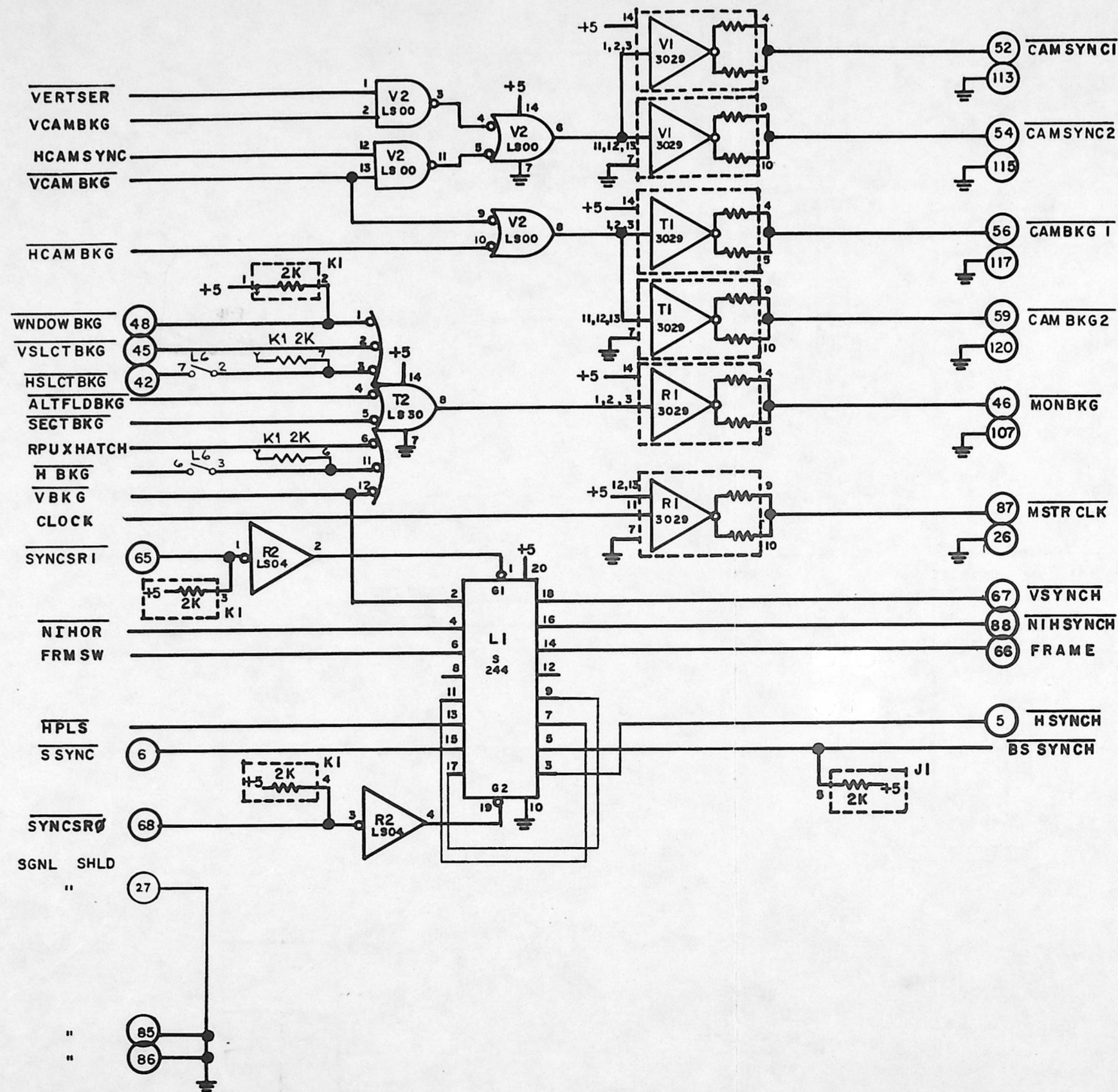
REV: A;

COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 2-26-82			
DRW E.R. CKD			
APVD			
TITLE		NTSC RPU SYNC	
		NO	
		CIC-W-22	
		SHEET 4 OF 6	



REV: A;

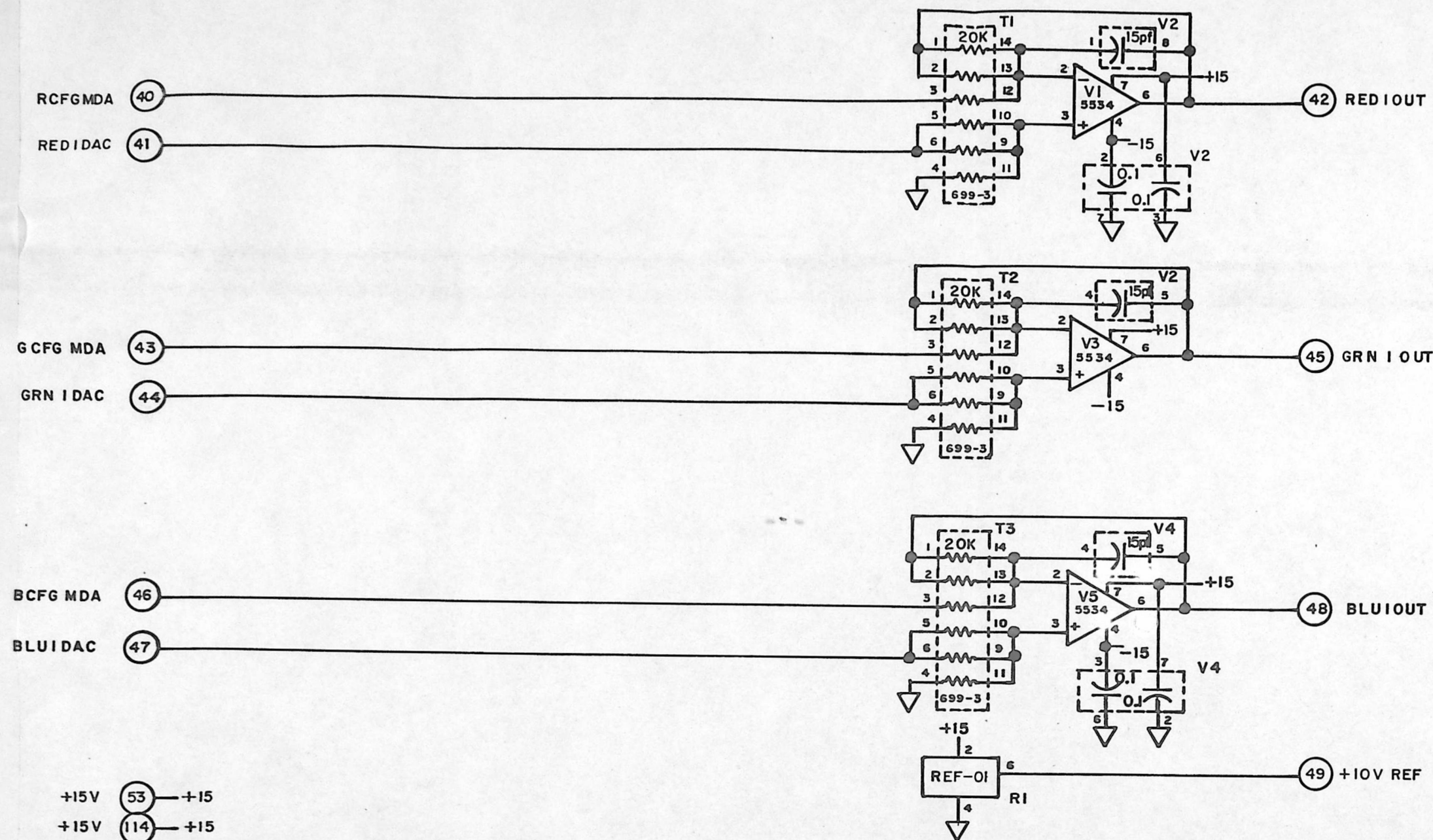
COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	NONE	REVISIONS	BY DATE
DATE	2-27-82	PER WCA MARKED PRINT	E.R. 4-27-82
DRN	E.R.	CRD	
APVD			
TITLE	NTSC RPU SYNC		NO
			CIC-W-22
			SHEET 5 OF 6



REV: A;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 2-27-82	PER WCA MARKED	E.R.	4-27-82
DRW E.R. CRD			
APVD			
TITLE	NTSC RPU SYNC		
	NO CIC-W-22 SHEET 6 OF 6		

026-4



NOTES:

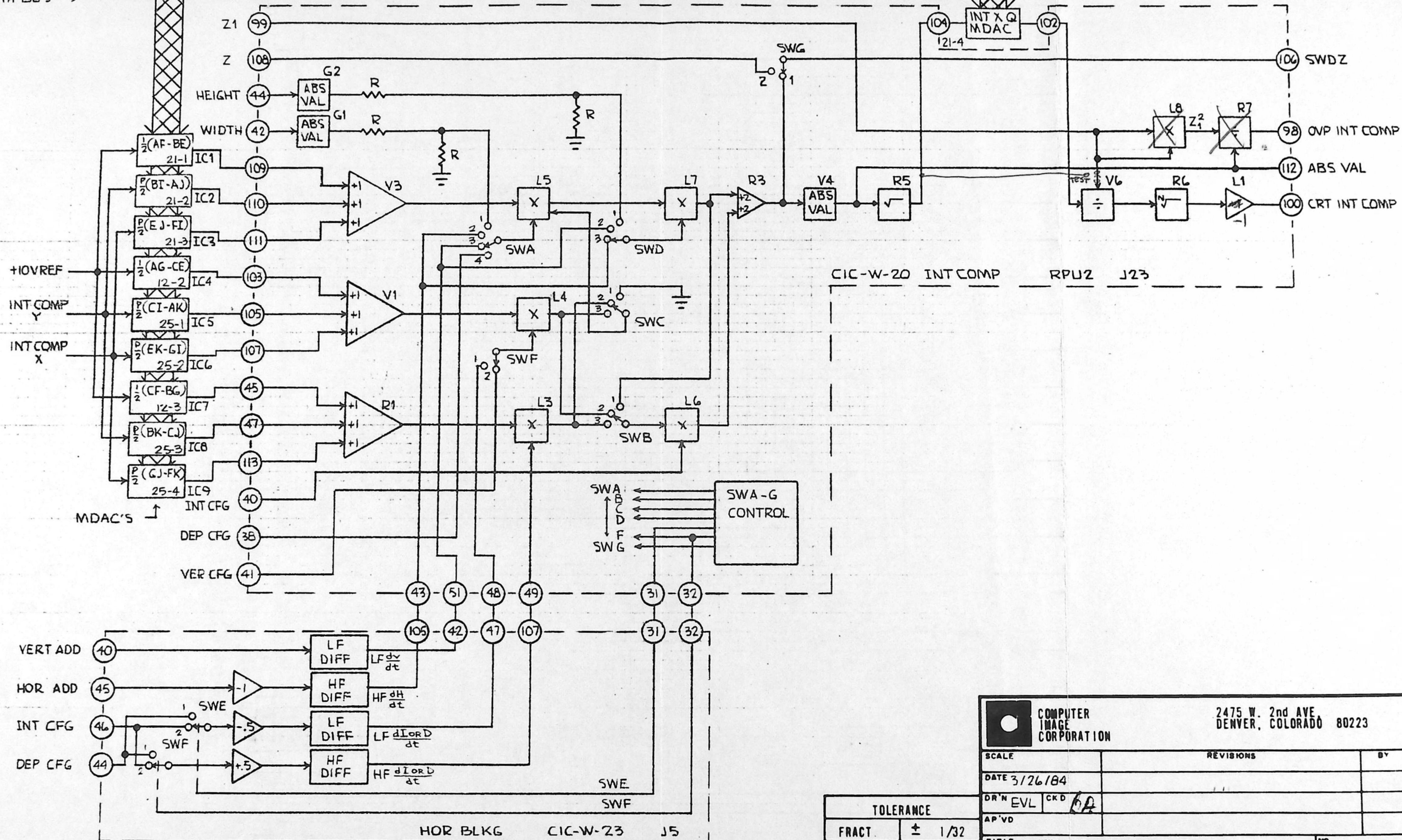
5534 IS NE5534AN OP AMP.

699-3 IS A 14 PIN DIP WITH 7 IDENTICAL RESISTORS.

REV:

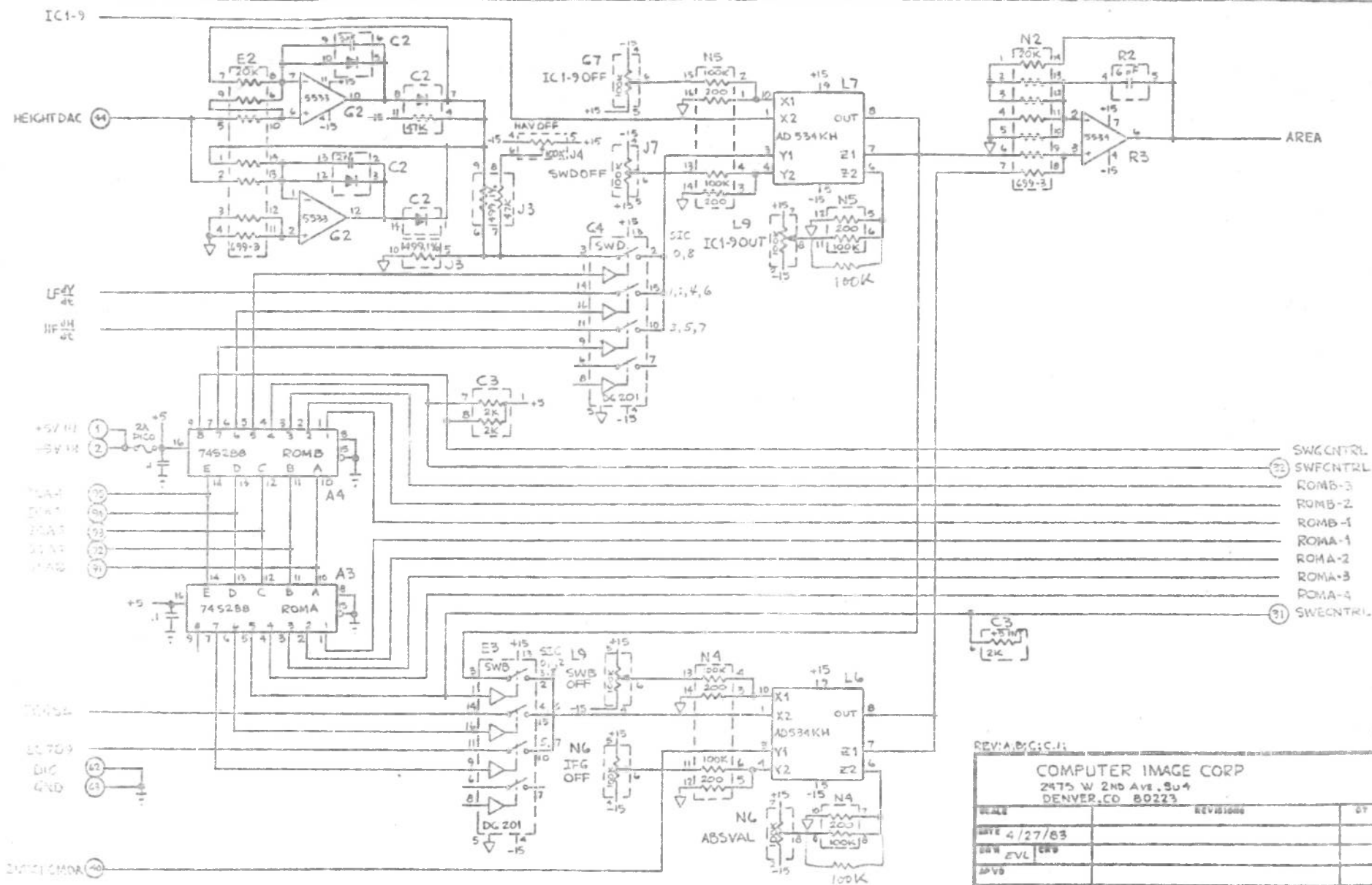
COMPUTER IMAGE CORPORATION 2475W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 3-17-82	PER WCA MARKED PRINT	E.R.	4-27-82
DRN ER.	CKD		
APVD			
TITLE SYSTEM IV COLOR ADDERS		NO CIC-W-42	

SYS IV
RPU
DATA BUS



		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	REVISIONS		BY
DATE 3/26/84			DATE
DR'N EVL	CKD		
AP'VD			
TITLE INTENSITY COMPENSATION BLOCK DIAGRAM			NO BCIC-10

TOLERANCE	
FRACT	± 1/32
.XX	± .015
.XXX	± .005

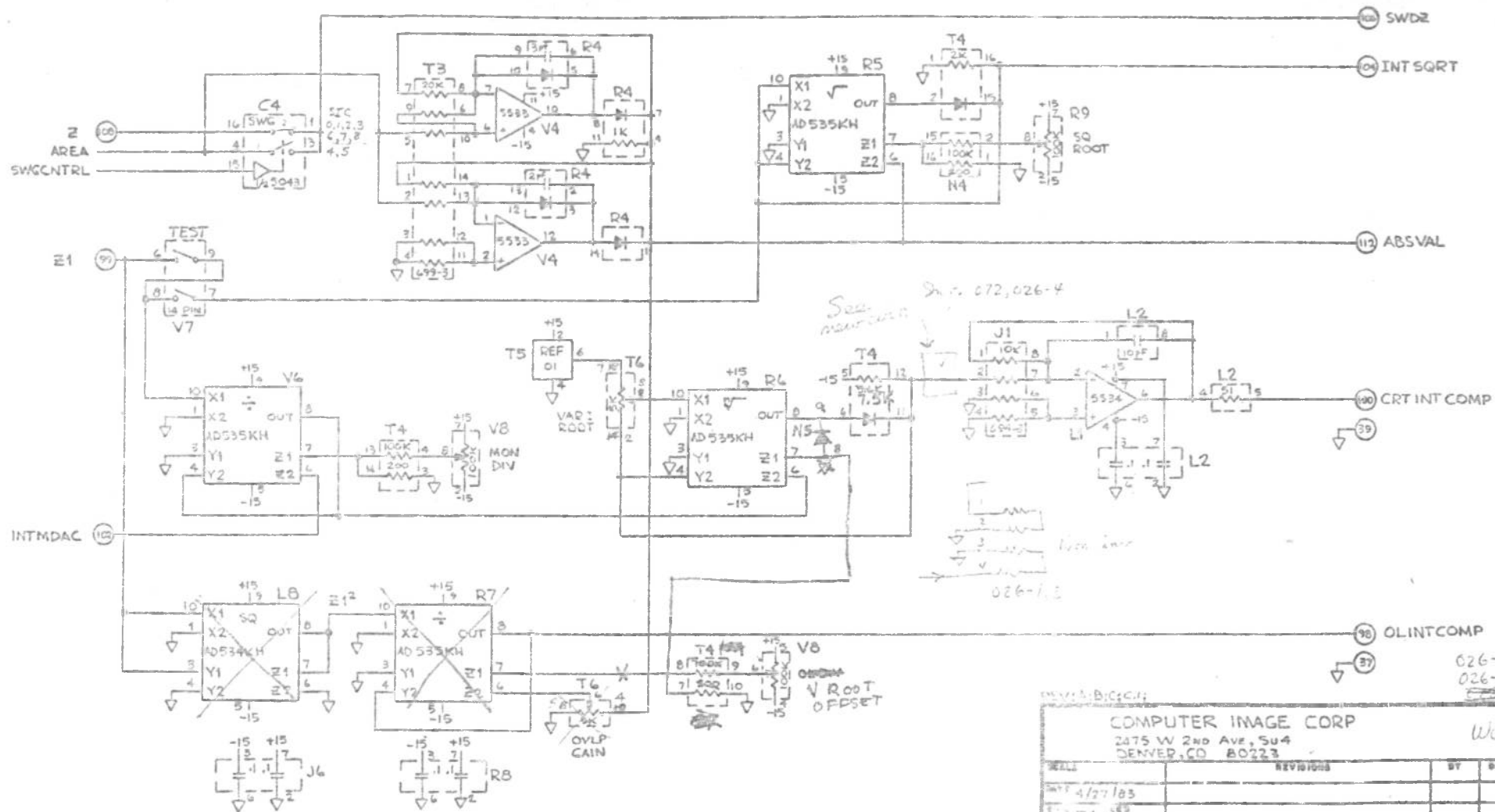


REV: A, B, C, D, E

COMPUTER IMAGE CORP
2475 W 2ND AVE, SU 4
DENVER, CO 80223

DATE	REVISION	BY	CHK
4/27/83			
BY	EVL	CRB	
APPRO			
TITLE INTENSITY COMPENSATION SYSTEM IV		CIC-W-20 SH 2 OF 3	

026-4



026-4
026-1

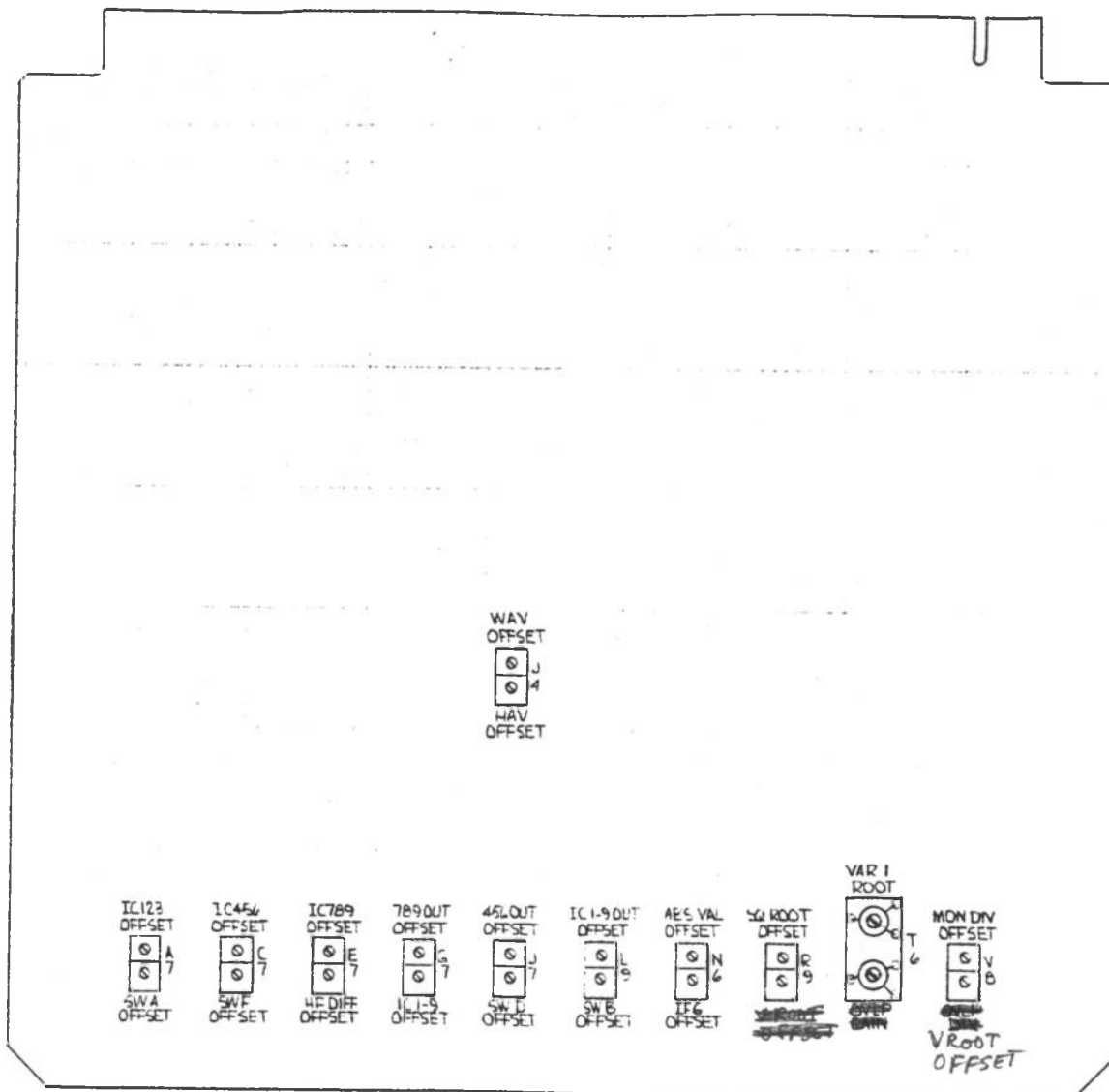
COMPUTER IMAGE CORP
2475 W 2ND AVE, SU4
DENVER, CO 80223

DATE	BY	DATE
4/27/83		
EVL		
APV		

TITLE INTENSITY COMPENSATION SYSTEM IV

NO. C1C-W-20
SH 3 OF 3

026-4

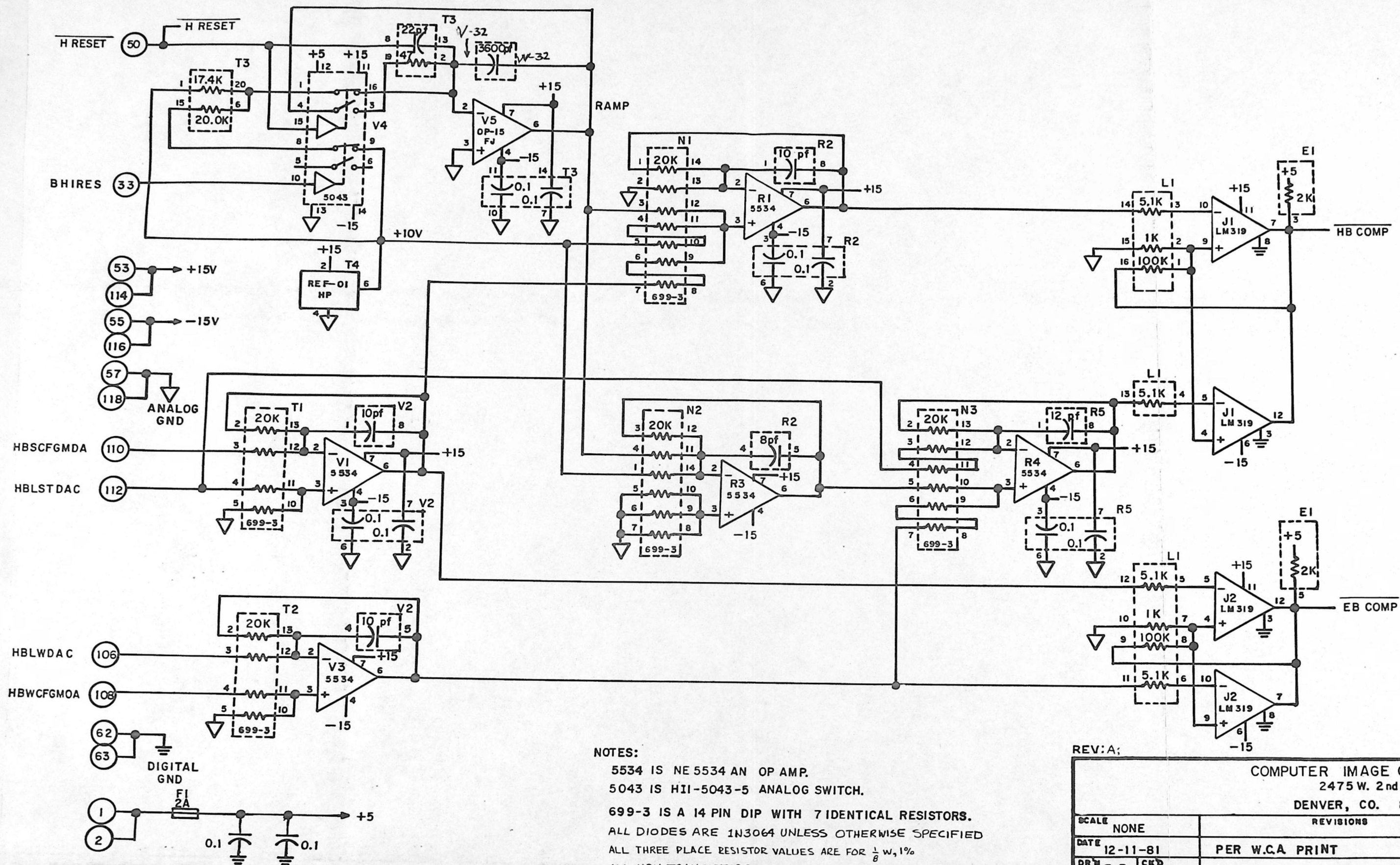


REV:

 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	REVISIONS	BY	DATE
DATE 7/24/64			
DR & EVL			
AP VC			
TITLE INTENSITY COMP		SH 1 OF 1	
CIC-W-20		FCIC-69	

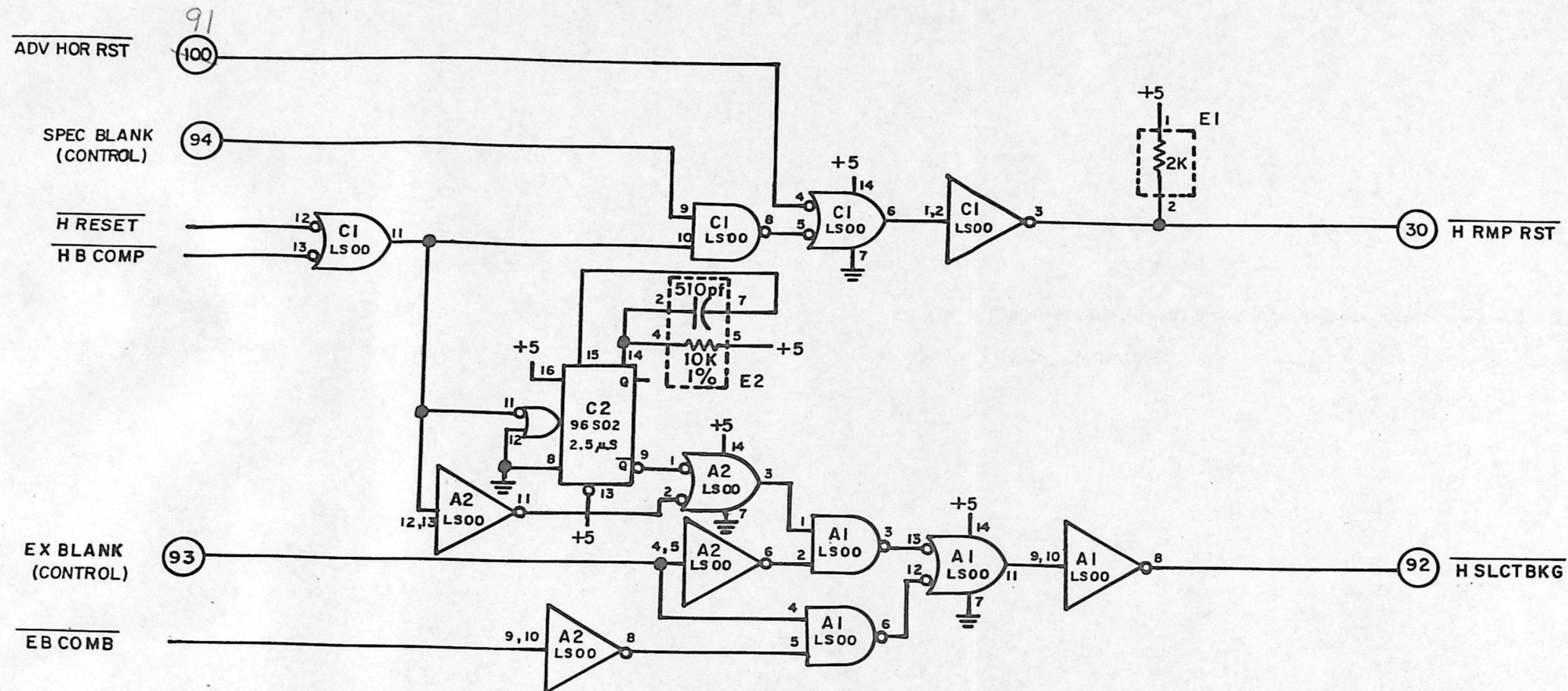
TOLERANCE	
FRAC	± 1/3:
XX	± 0.1:
XXX	± 0.01:

026-4



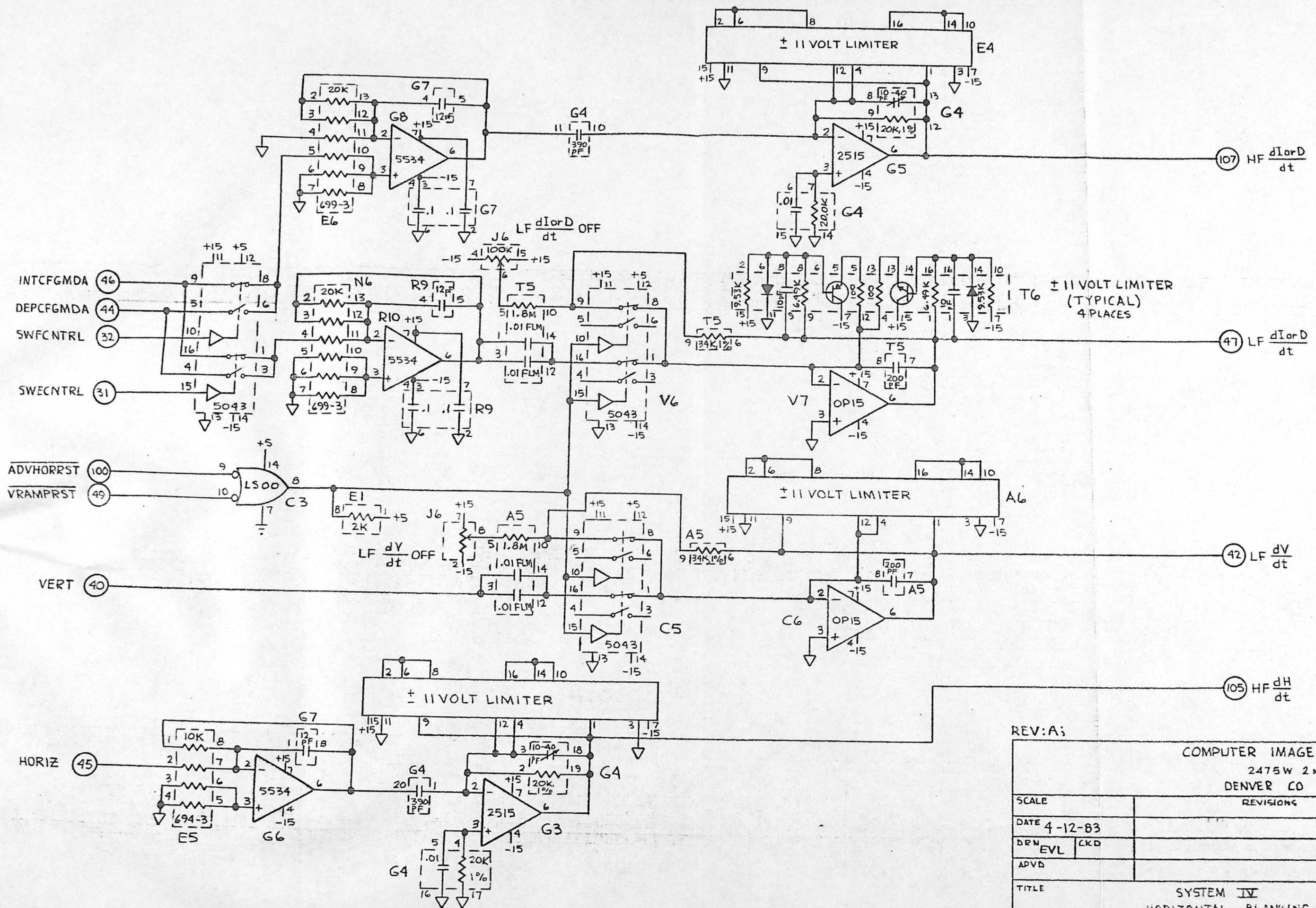
REV: A;

COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	REVISIONS	BY	DATE
NONE			
DATE 12-11-81	PER W.C.A. PRINT	E.R.	3-11
DRN E.R. CKD	" " "	E.R.	5-10-82
APVD			
TITLE			NO
SYSTEM IV HORIZONTAL BLANKING			CIC-W-23
			SHEET 1 OF 4



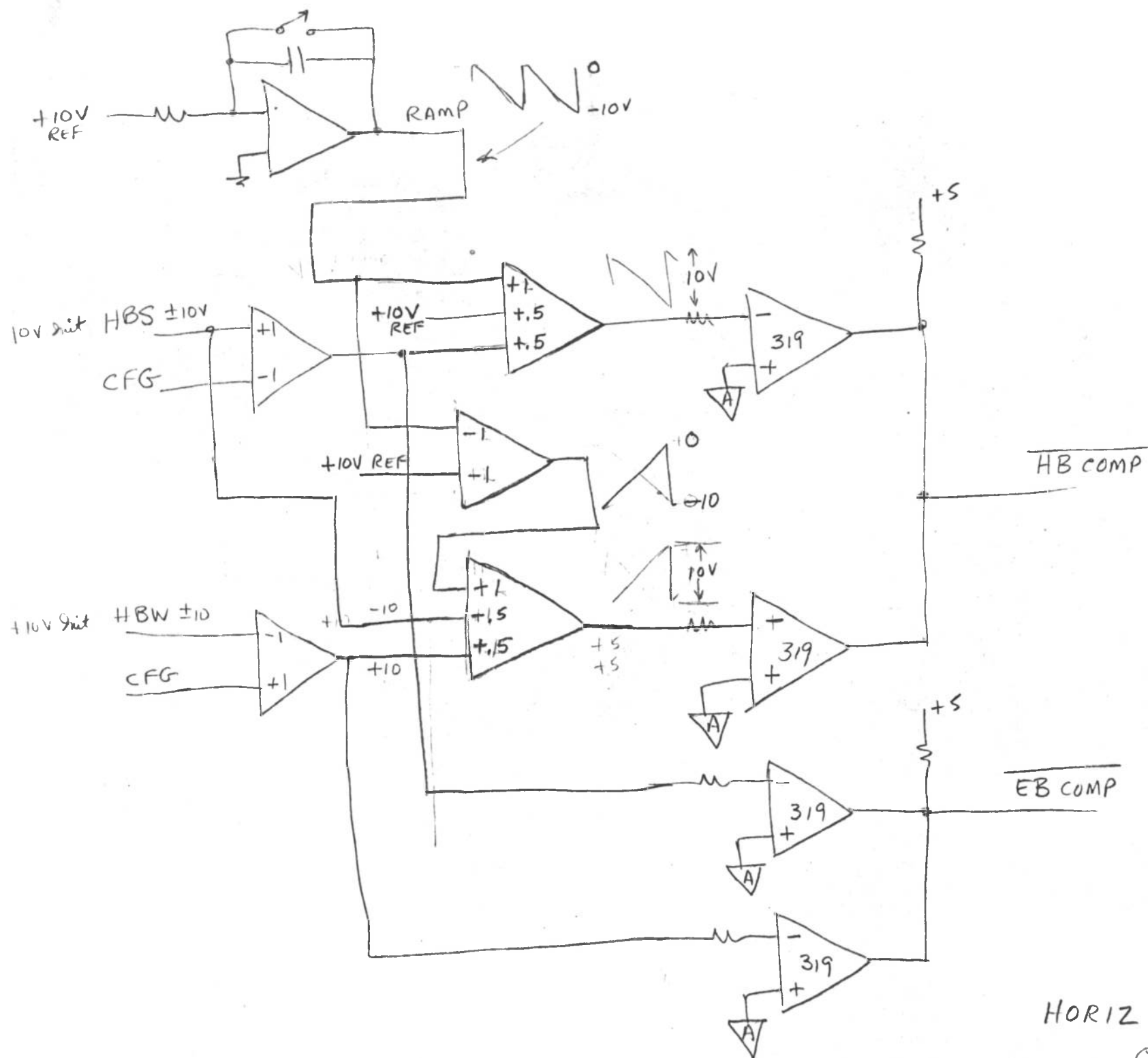
REV: A;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 12-11-81	PER WCA MARKED PRINT	E.R.	4-27-82
DRN E.R. CKD			
APVD			
TITLE	SYSTEM IV HORIZONTAL BLANKING		NO CIC-W-23 SHEET 2 OF 4



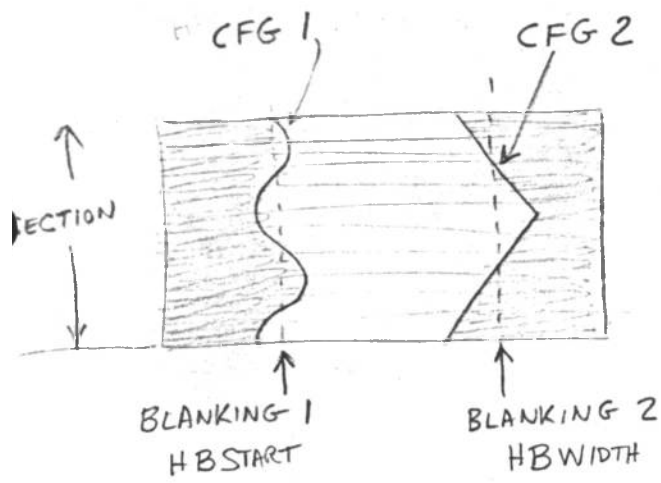
REV: A1

COMPUTER IMAGE CORPORATION			
2475 W 2ND AVE			
DENVER CO 80223			
SCALE	REVISIONS	BY	DATE
DATE 4-12-83			
DRN EVL CKD			
APVD			
TITLE		NO	
SYSTEM IV		CIC-W-23	
HORIZONTAL BLANKING		SHEET 4 OF 4	

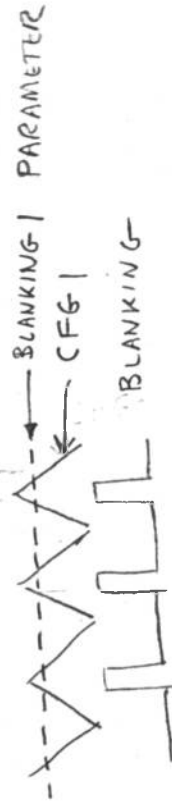


HORIZ BLANKING

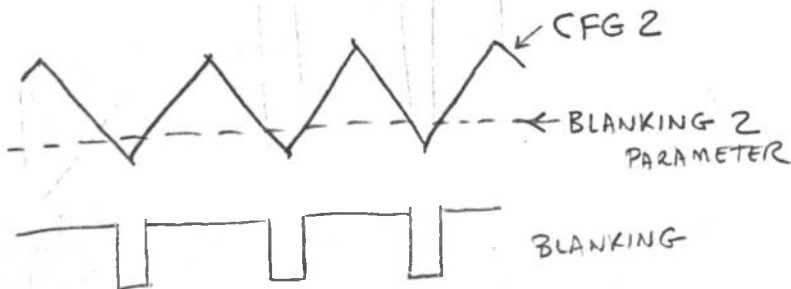
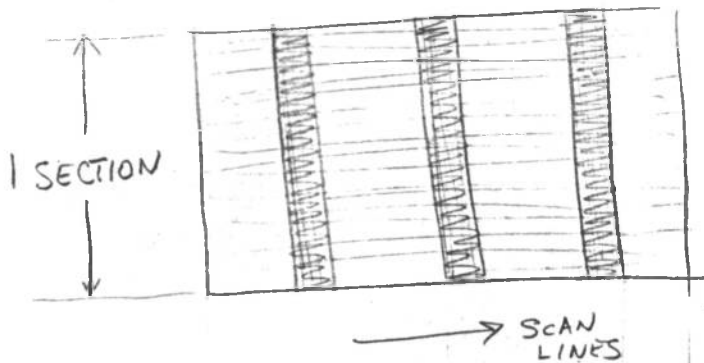
CIC-W-23



HBS/HBW



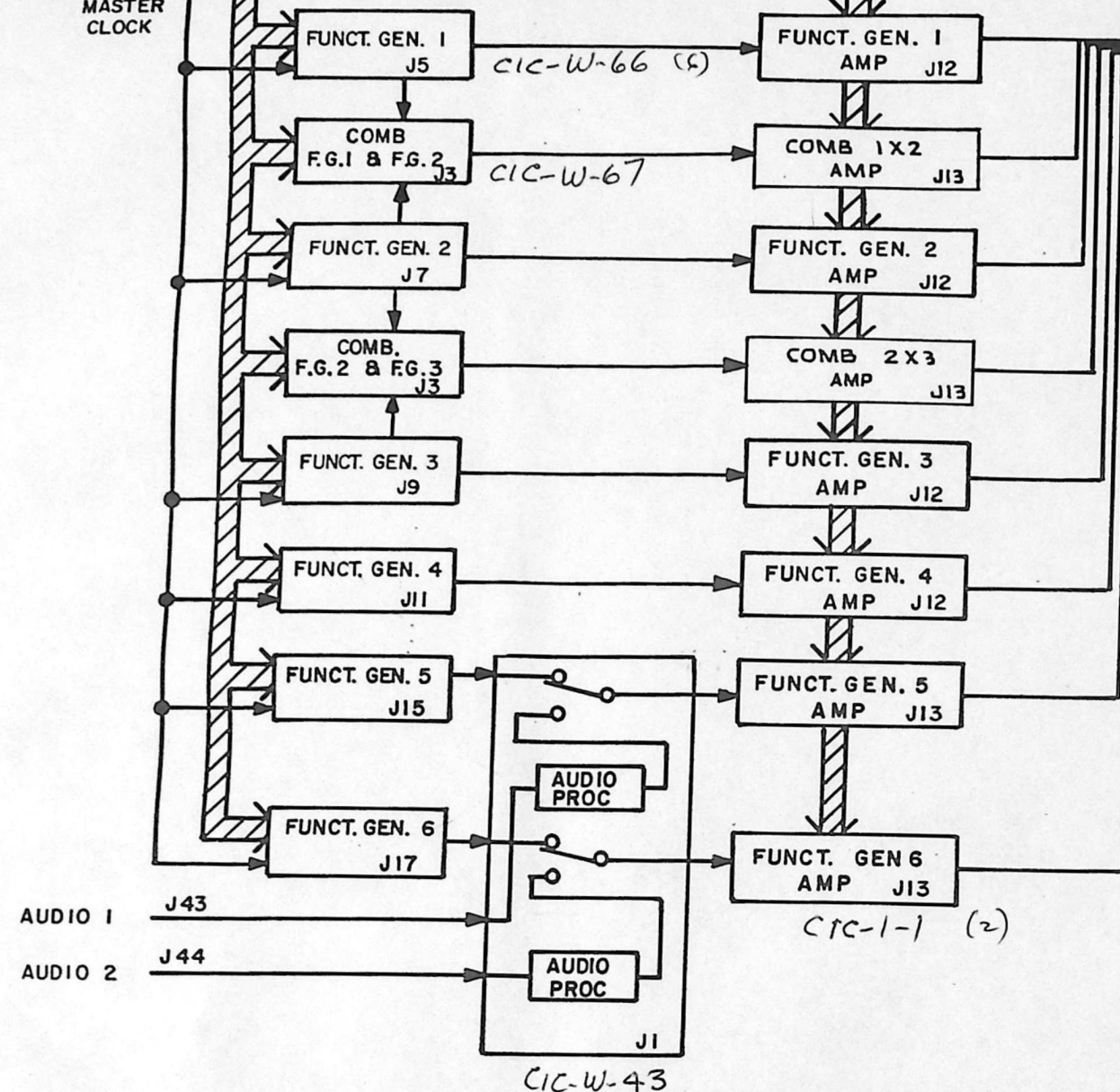
EX BLANK



HORIZ. BLANKING
CIC-W-23

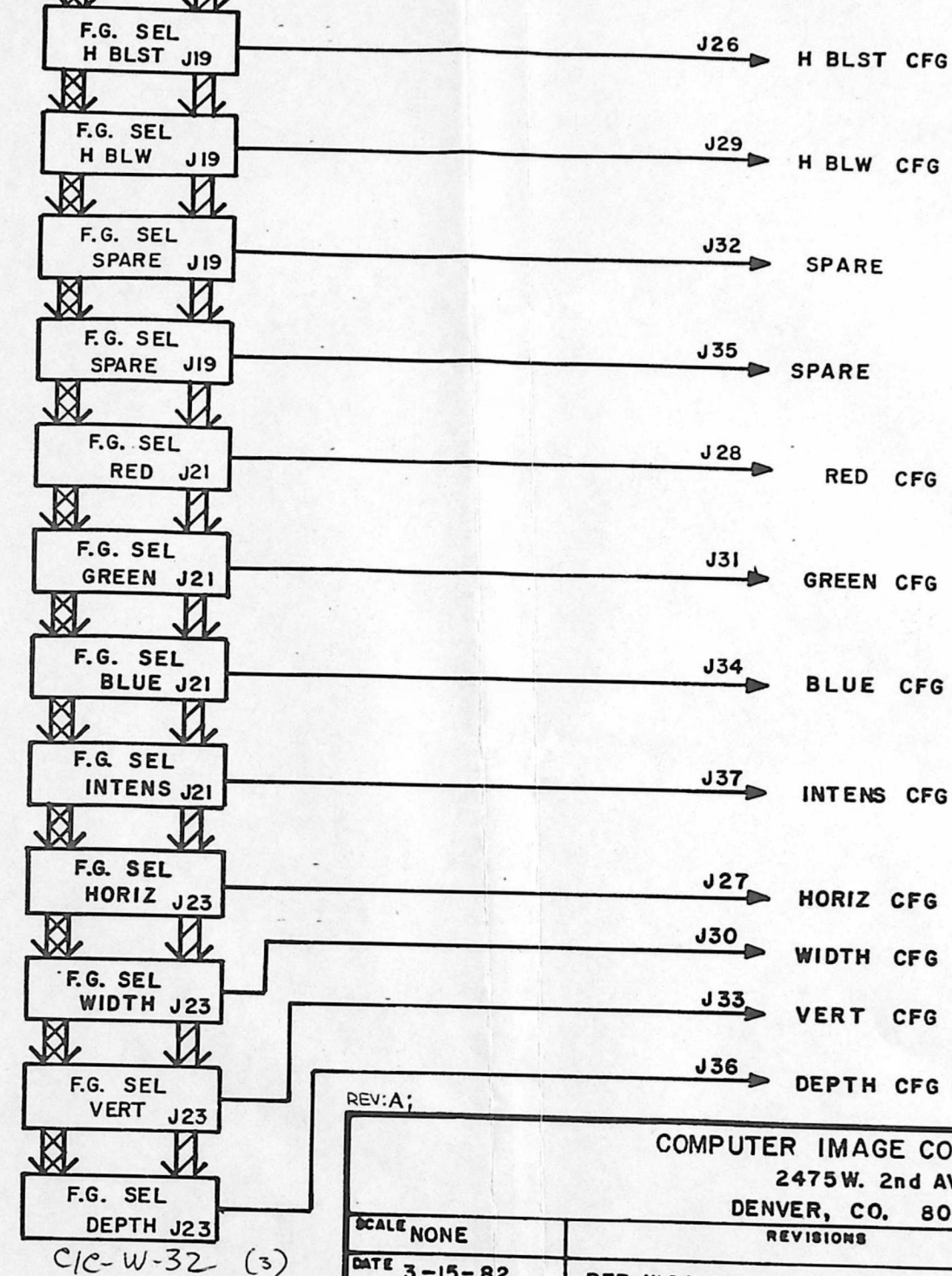
MASTER CLOCK J40
SYSTEM IV
DIGITAL BUS

BUFFERED
MASTER
CLOCK



J25
TERMINATOR
&
DIGITAL DATA
DISPLAY

CIC-W-33



REV: A;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS		BY DATE
DATE 3-15-82	PER WCA MARKED PRINT		E. R. 4-27-82
DRN E. R. CKD	ECN #30		EVL 10/11/82
APVD			
TITLE FUNCTION GENERATOR NEST BLOCK DIAGRAM			NO BCIC-3

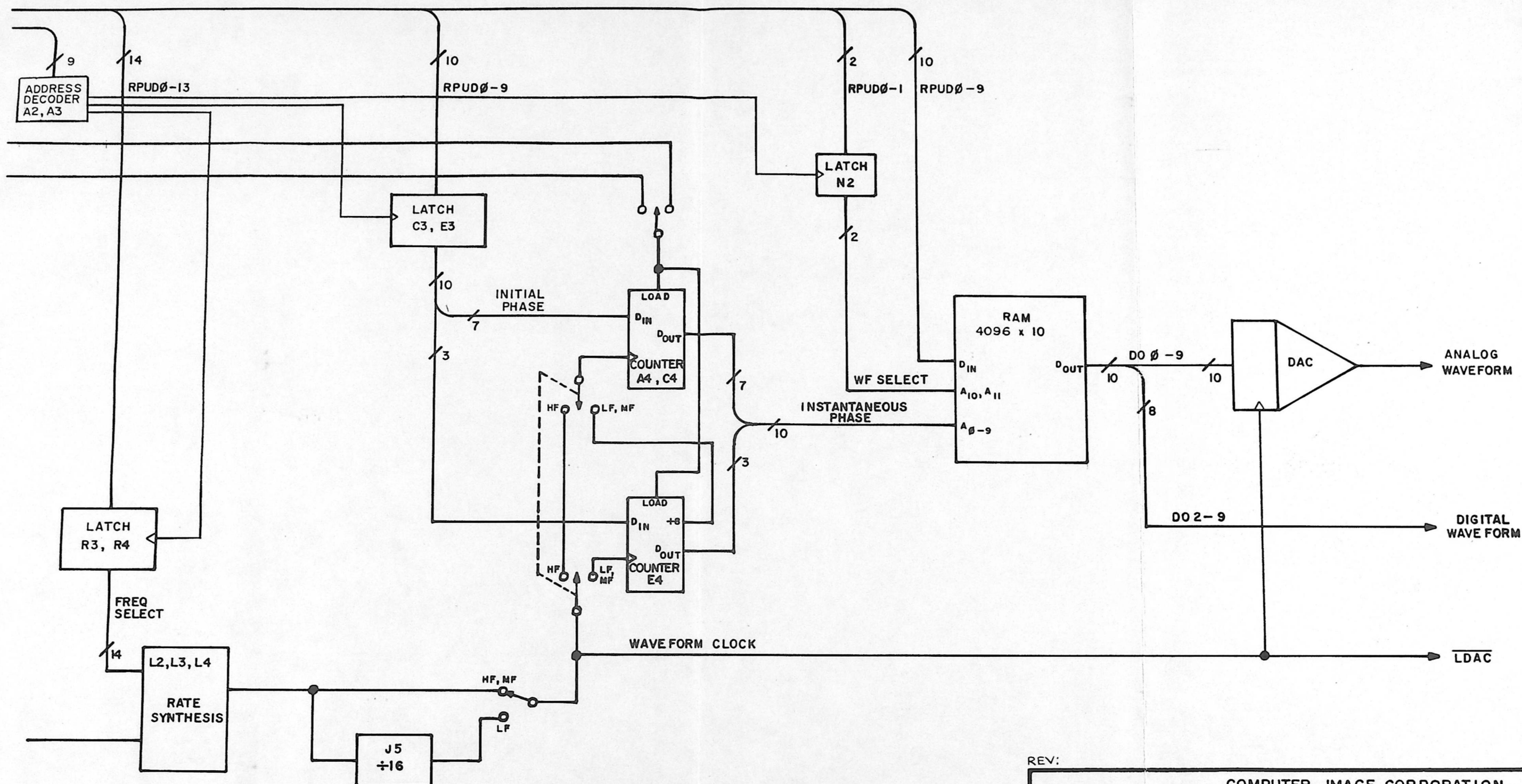
SIX DATA BUS

RPUDØ-13
RPUAØ-8

HSYNC

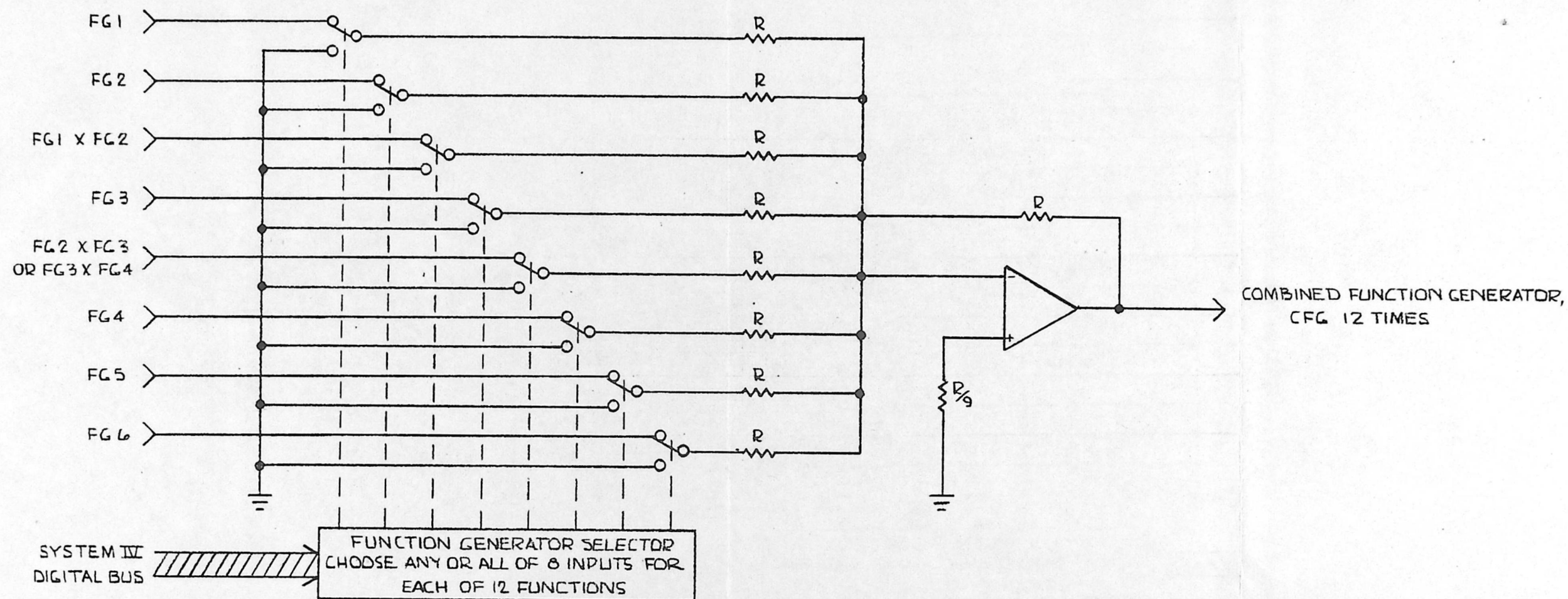
SSYNC

MCLK



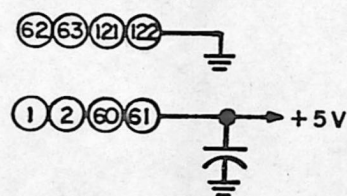
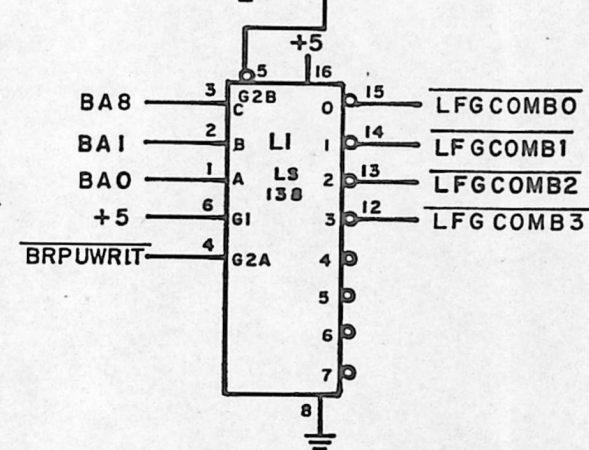
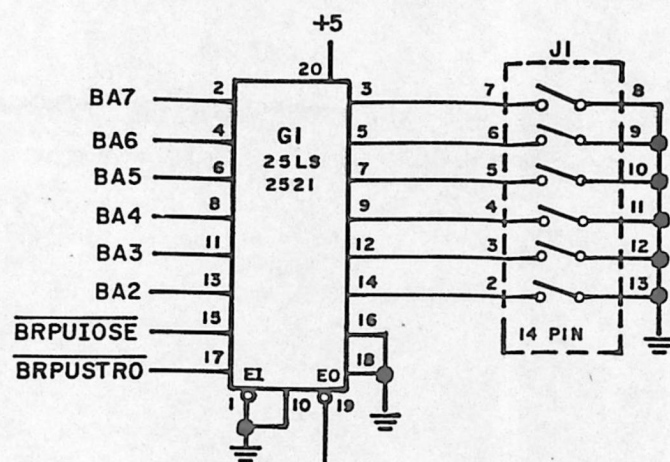
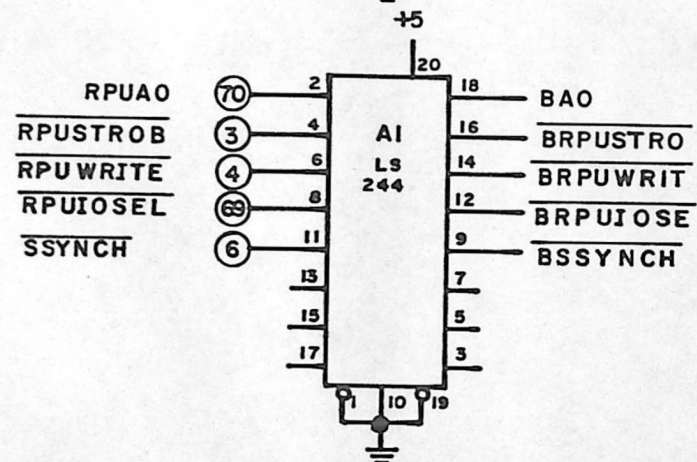
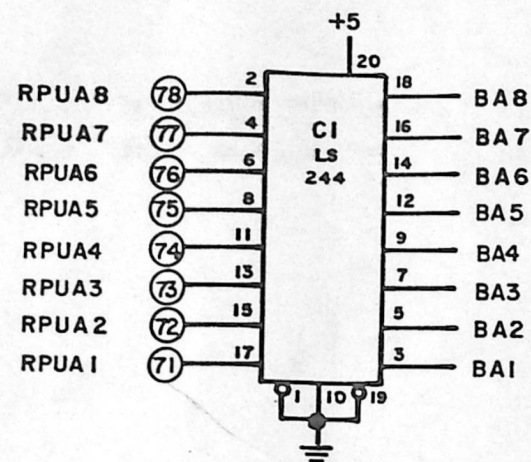
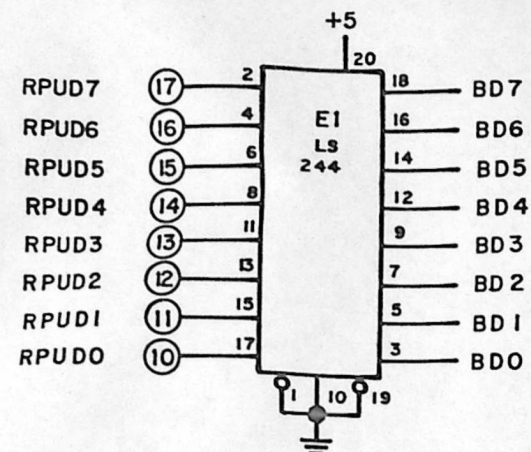
REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 5-31-82			
DRN E.R. CKD			
APVD			
TITLE	FUNCTION GENERATOR BLOCK DIAGRAM		NO BCIC-8



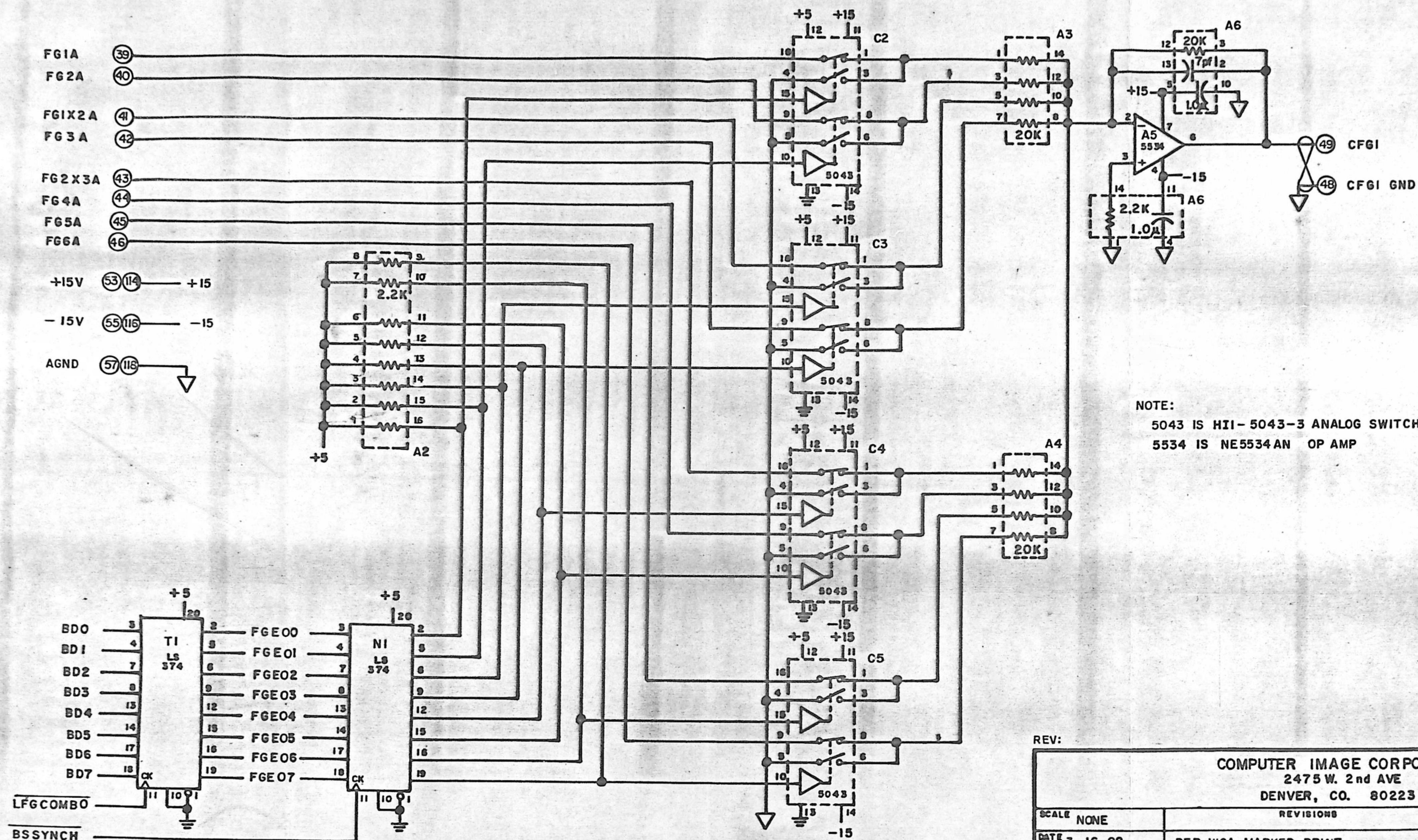
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE. DENVER, COLORADO 80223	
SCALE	REVISIONS		BY
DATE 3/23/84			DATE
DR'N EVL	CK'D NCA		
AP'VD			
TITLE FUNCTION GENERATOR SELECT CIC-W-32 BLOCK DIAGRAM			NO BCIC-9

TOLERANCE		
FRACT.	±	1/32
.XX	±	.015
.XXX	±	.005



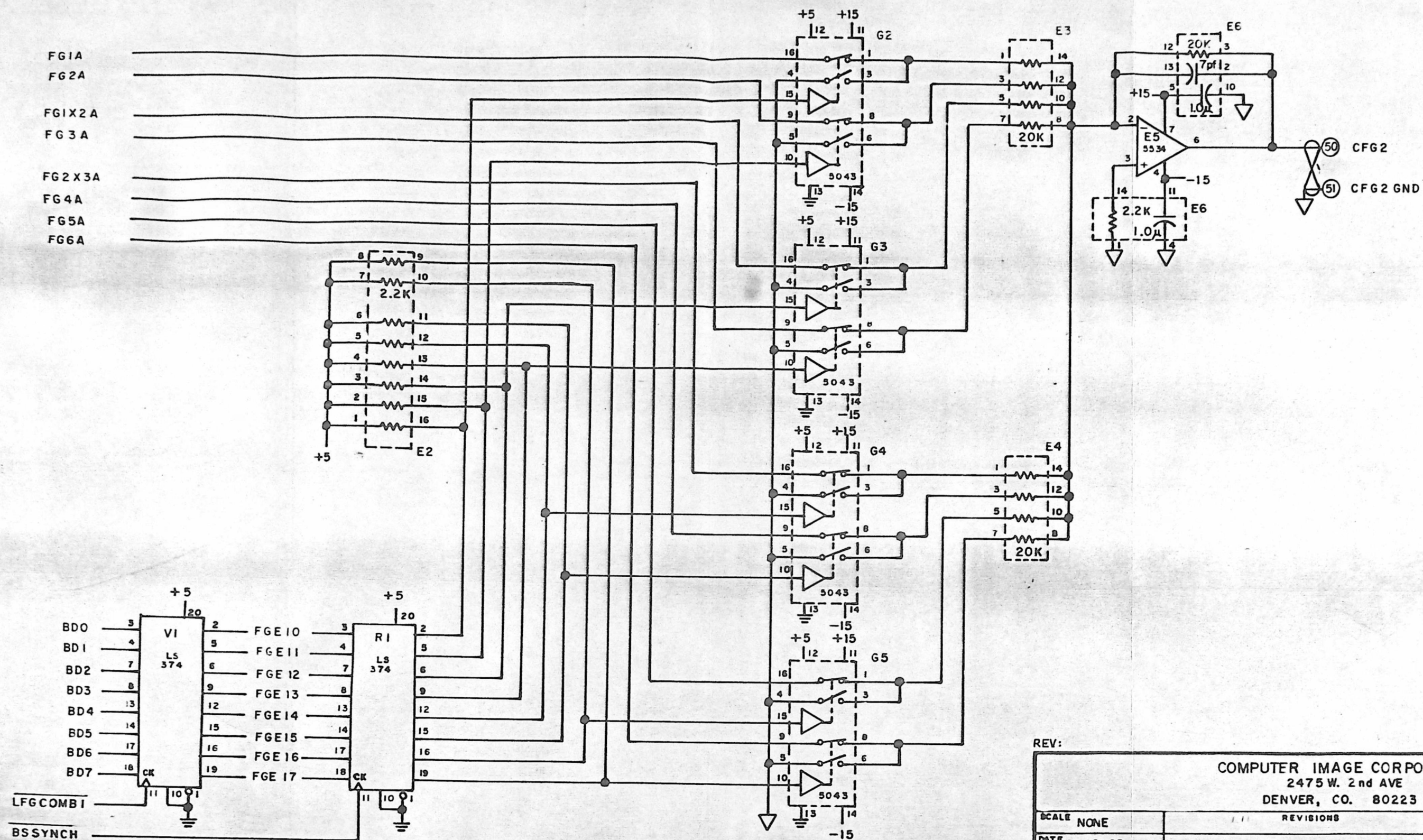
REV:

COMPUTER IMAGE CORPORATION 2475W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 3-16-82			
DRN E.R. CR'D			
APVD			
TITLE	FUNCTION GENERATOR SELECT		NO CIC-W-32 SHEET 1 OF 5



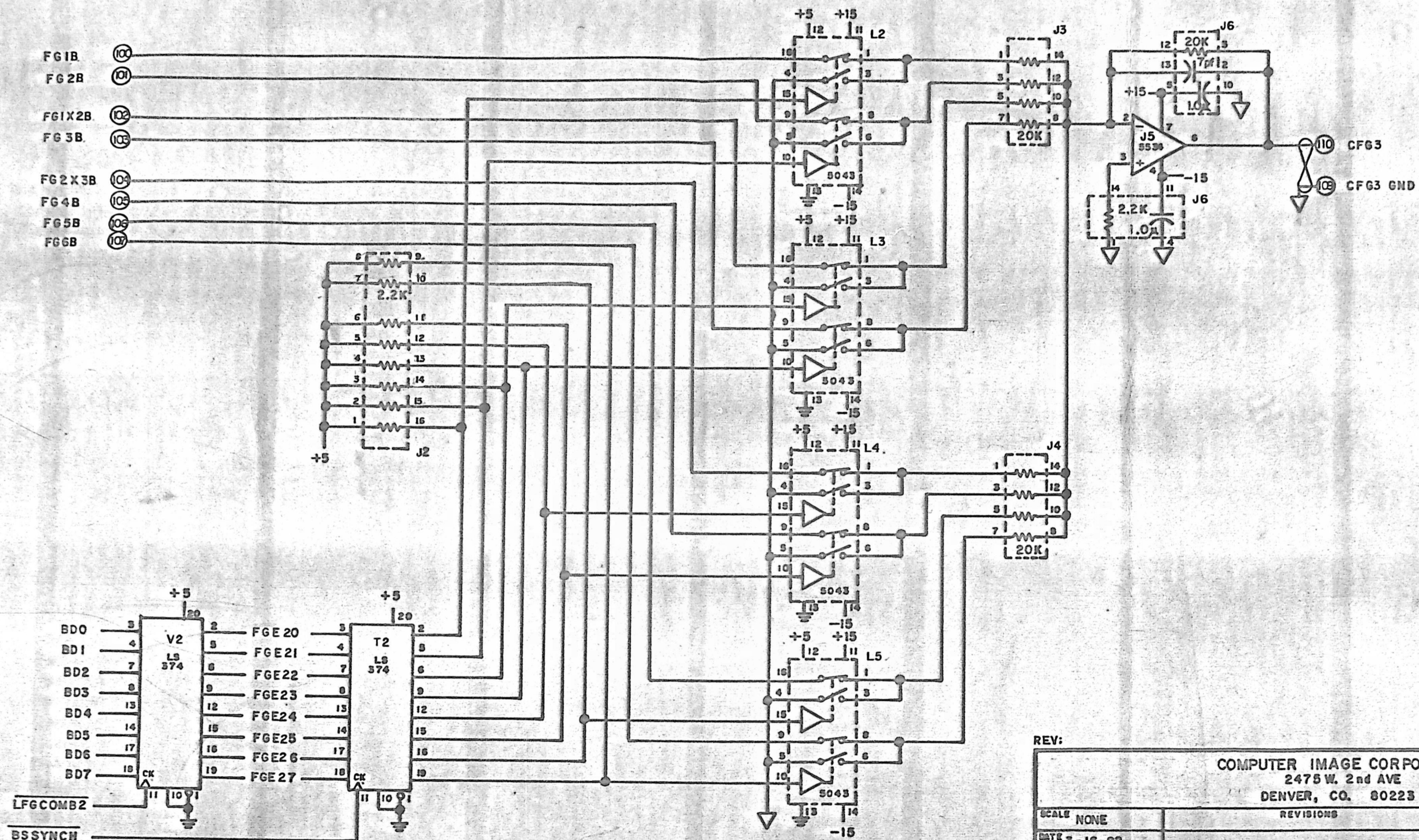
REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 3-16-82	PER WCA MARKED PRINT	E.R.	4-27-82
DRW E.R.	CRD		
APVD			
TITLE FUNCTION GENERATOR SELECT		NO CIC-W-32 SHEET 2 OF 5	



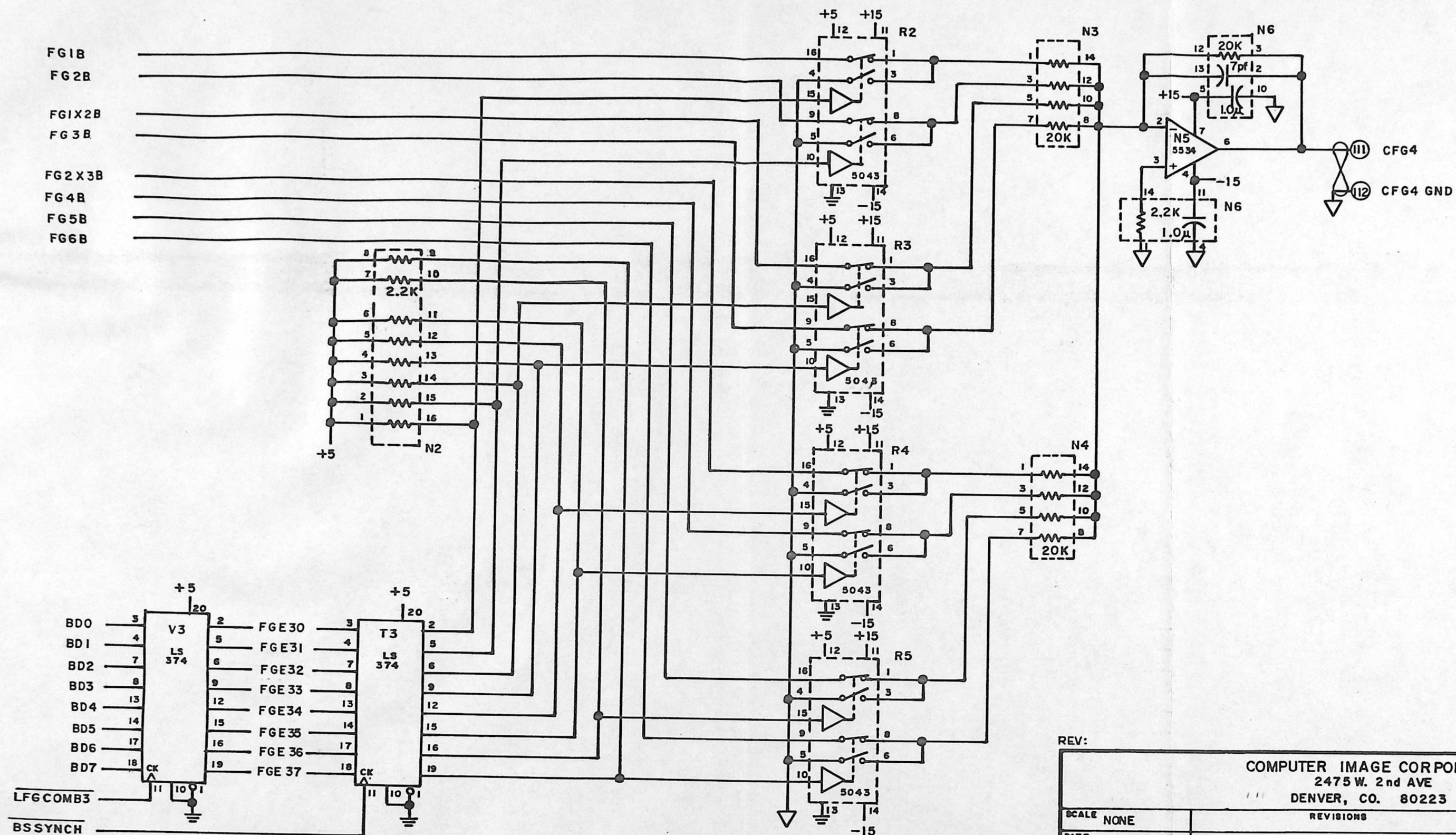
REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 3-16-82			
DRN E.R. CKD			
APVD			
TITLE	FUNCTION GENERATOR SELECT		NO CIC-W-32 SHEET 3 OF 5



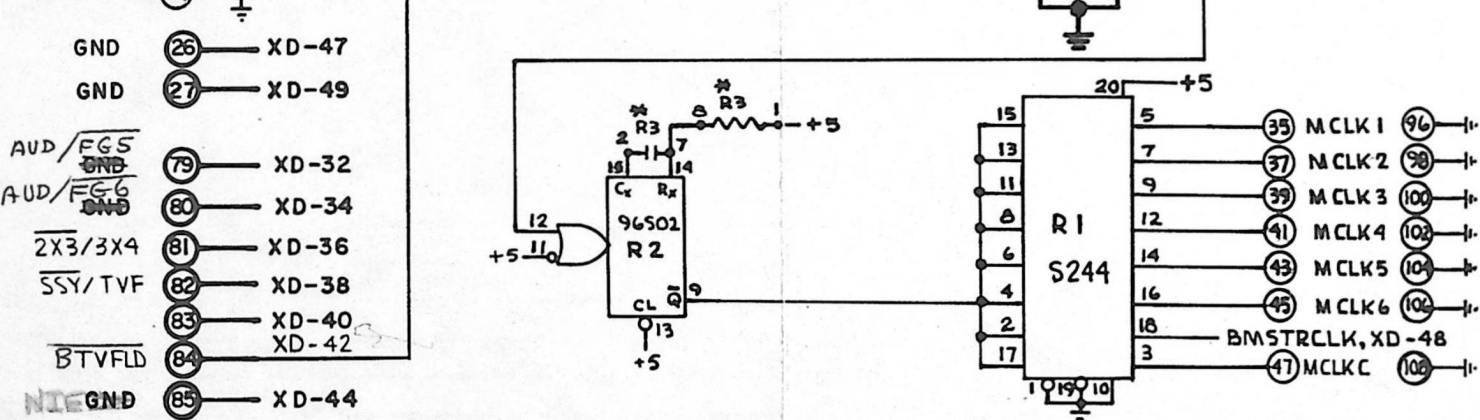
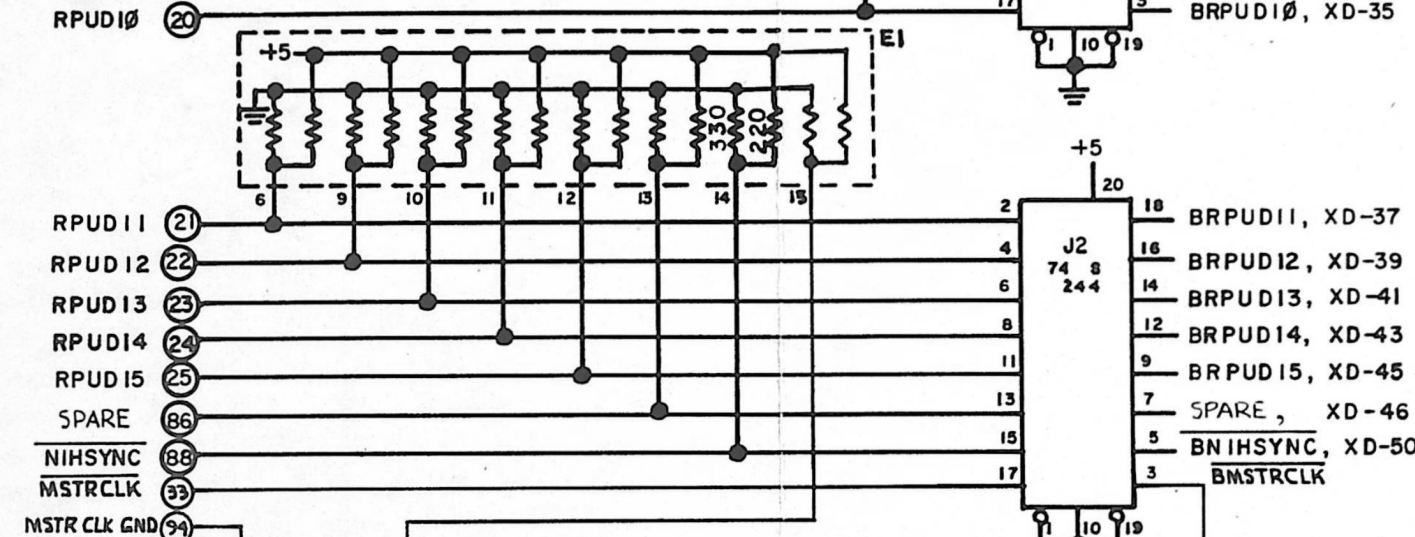
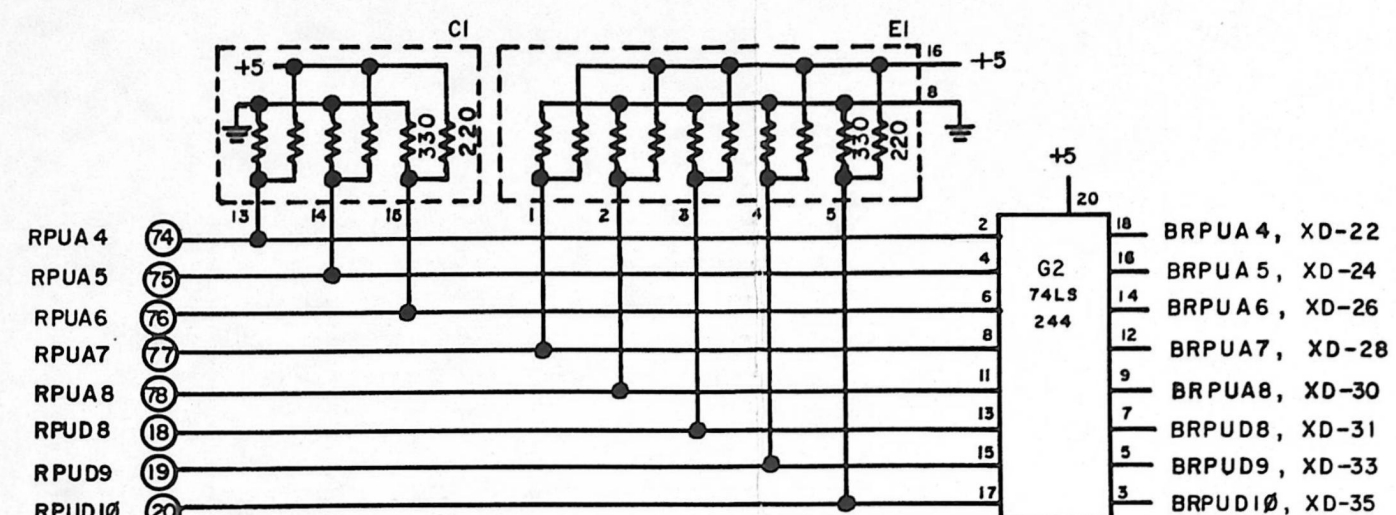
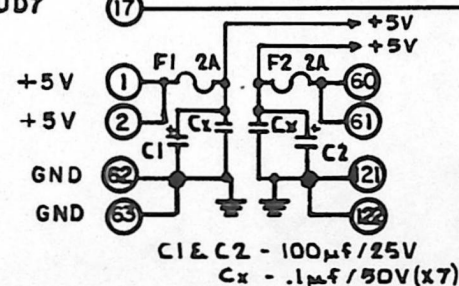
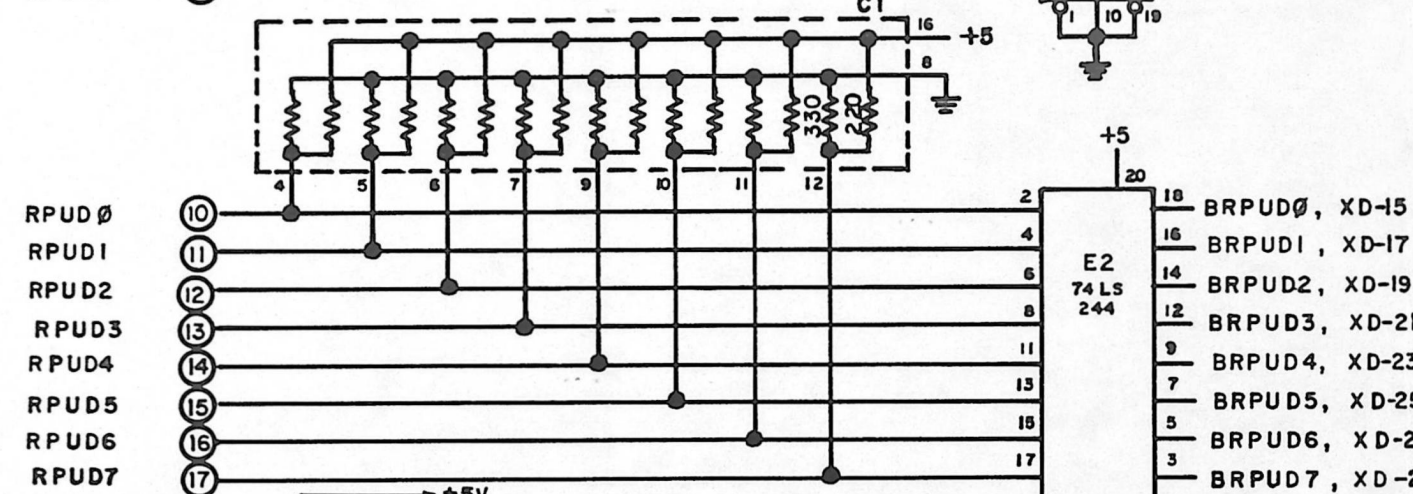
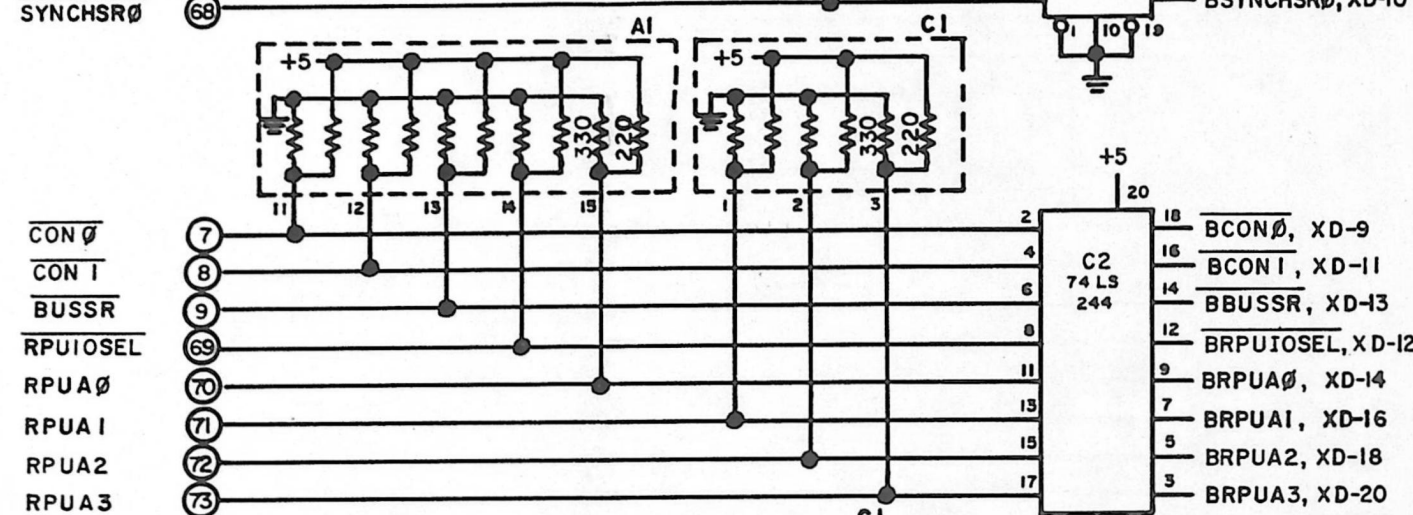
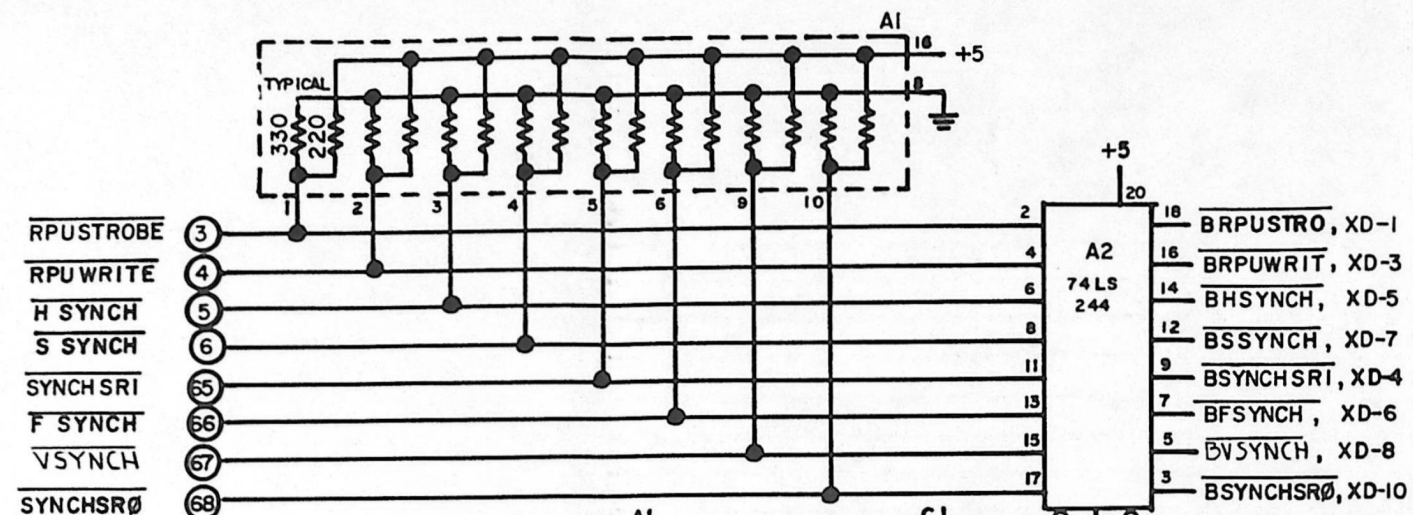
REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 3-16-82			
DRW E.R. CRD			
APVD			
TITLE FUNCTION GENERATOR SELECT		NO CIC-W-32 SHEET 4 OF 5	



REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 3-16-82			
DRW E.R.	CRD.		
APVD			
TITLE FUNCTION GENERATOR SELECT		NO CIC-W-32 SHEET 5 OF 5	



NOTES:

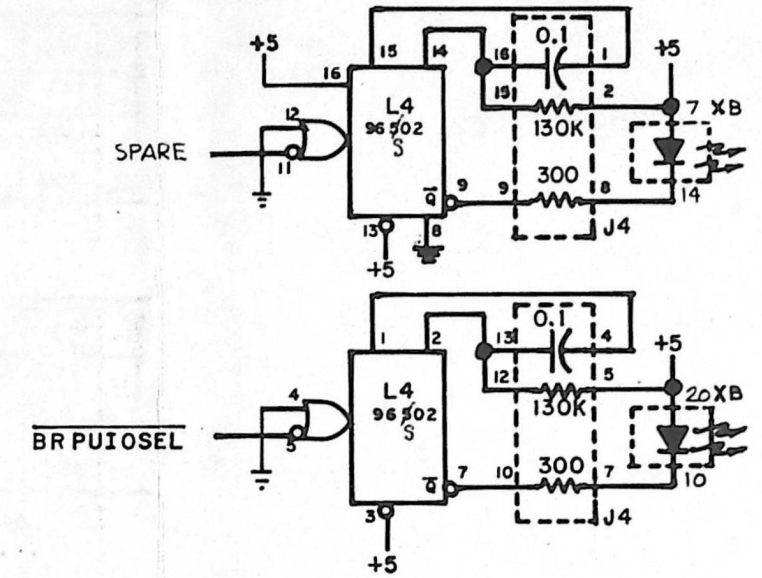
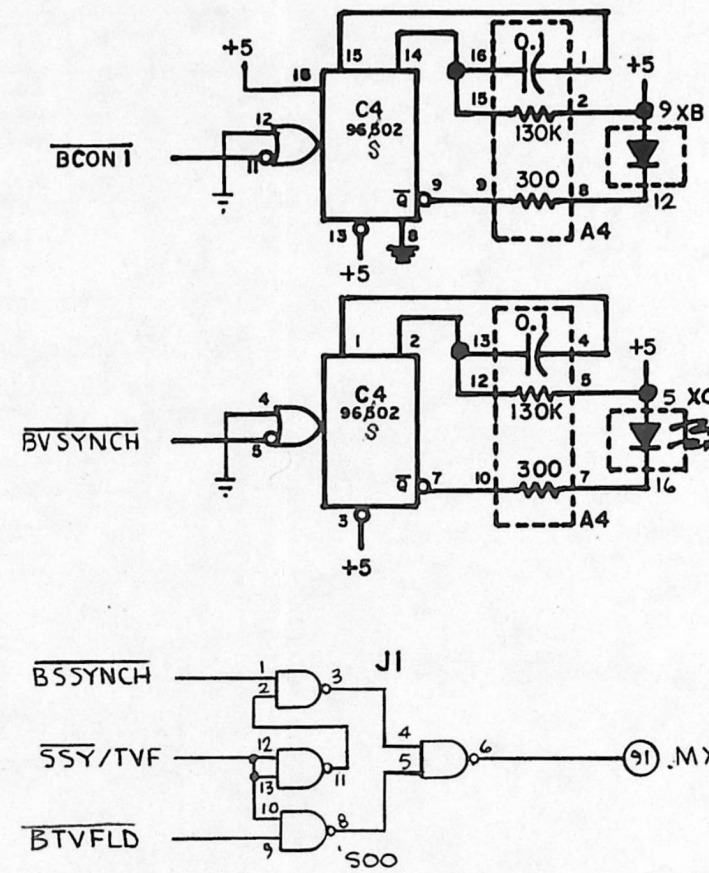
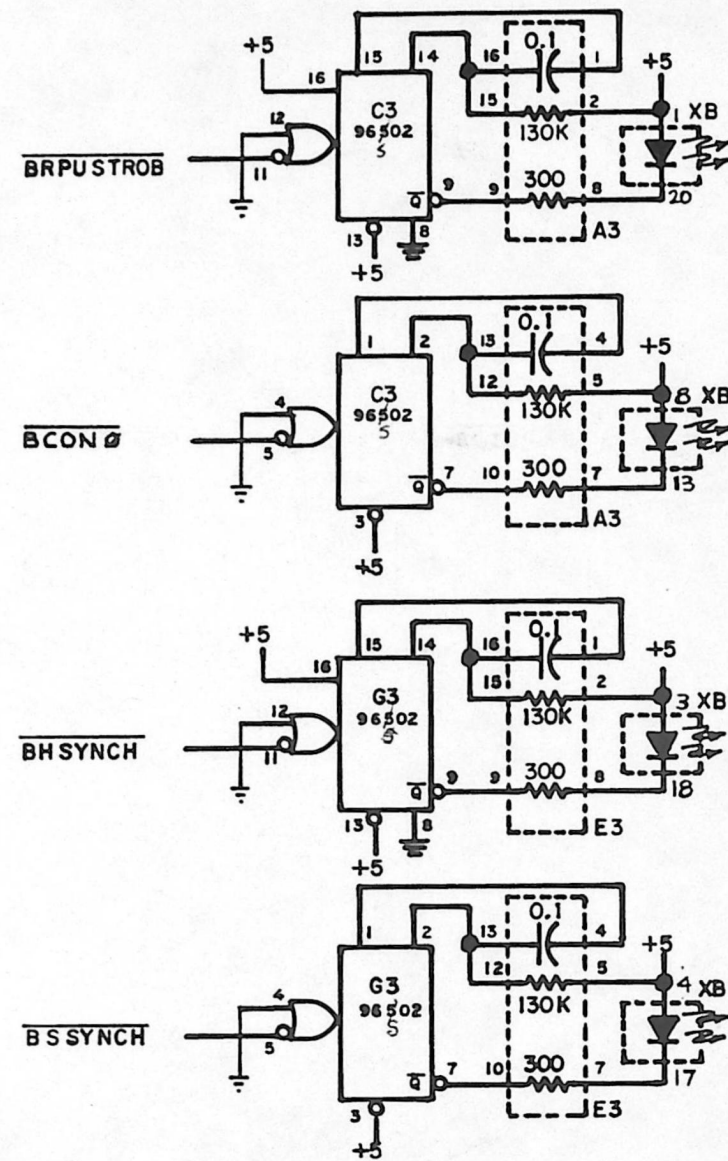
AI, CI, EI ARE 220Ω/330Ω RESISTOR DIPs
CTS #761-5-R220/330 OR EQUIVALENT

XC & XB ARE MV57164

* SELECT FOR t_{PWLOW} = 35-37NS
INITIAL VALUES - R_x = 1K, C_x = 0

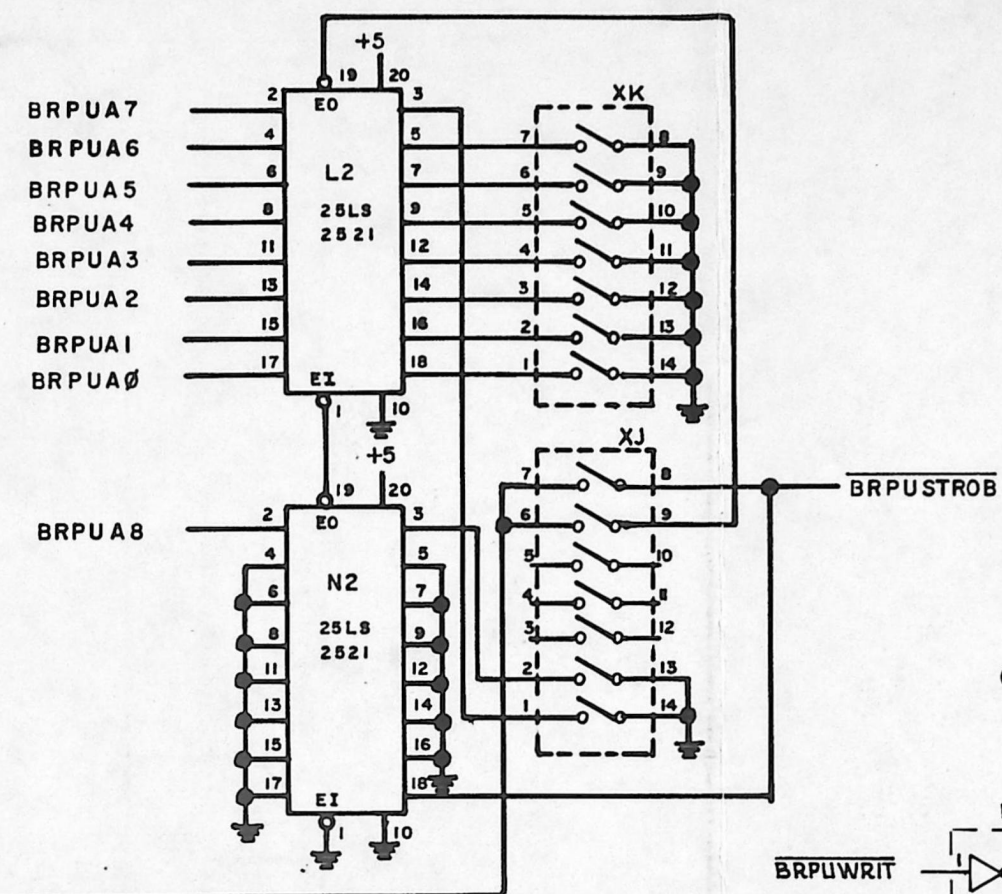
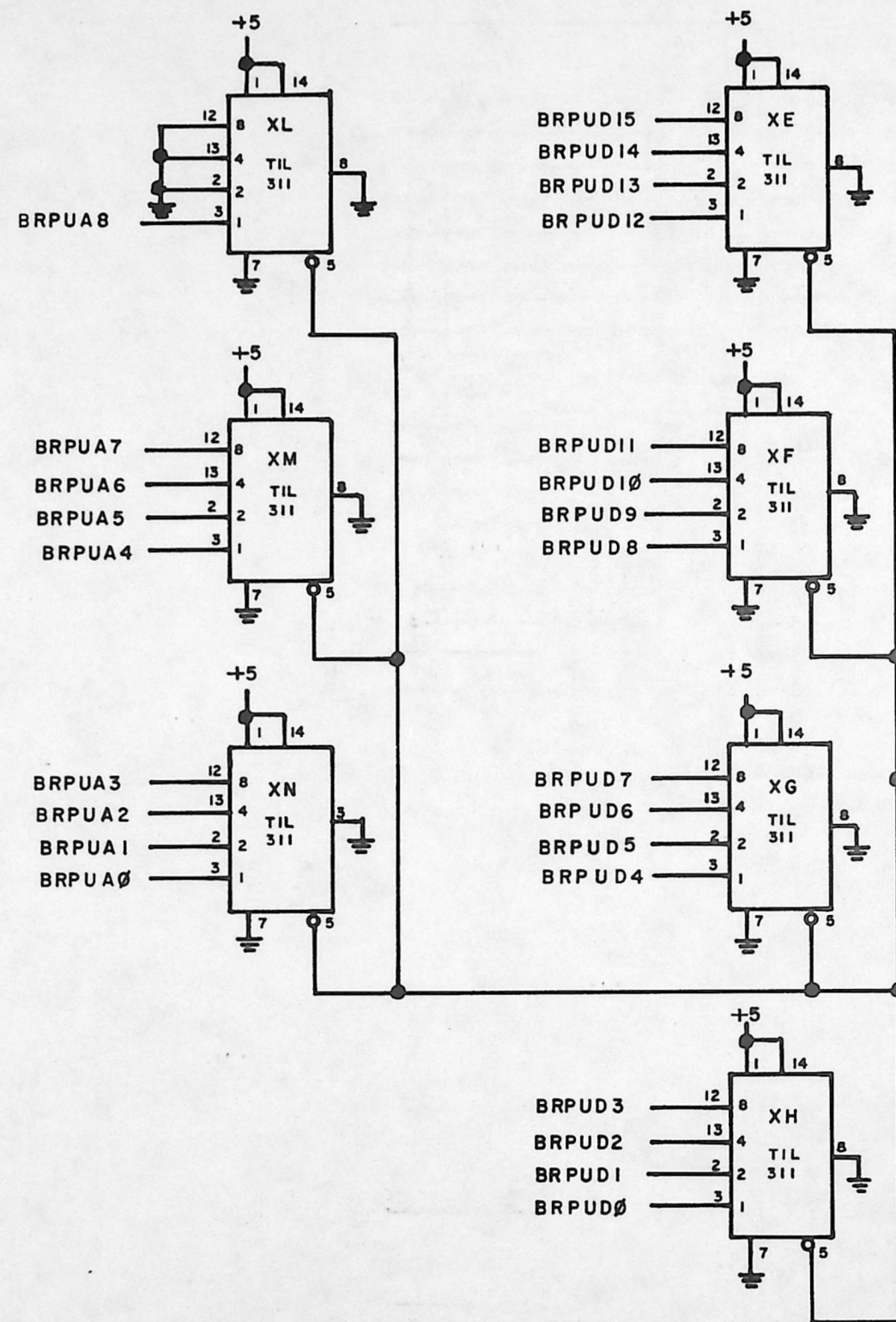
REV: A;B;C;1;C2;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 11-6-81			
DR E.R.	CRD		
APVD			
TITLE SYSTEM IV DIGITAL BUS TERMINATOR		NO CIC-W-33 SHEET 1 OF 4	

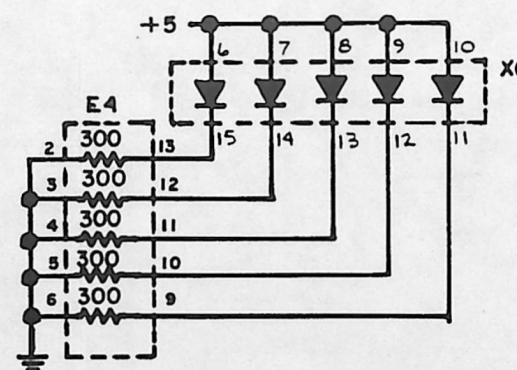
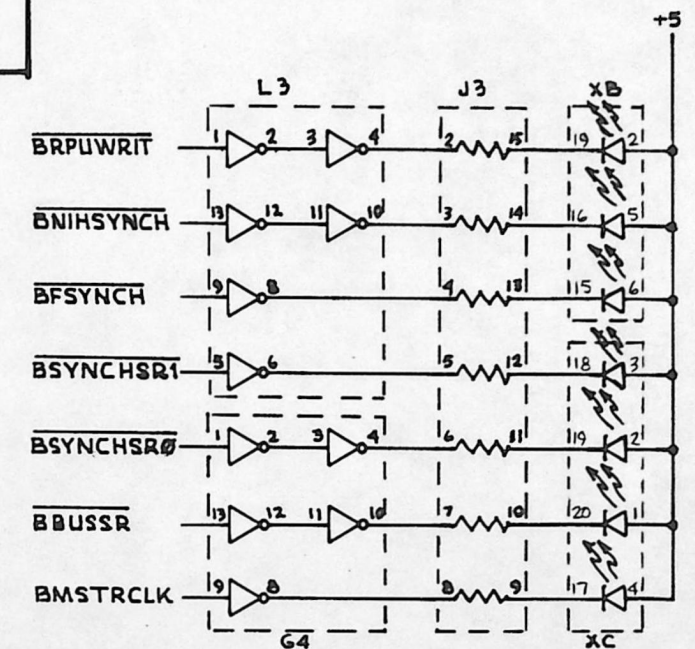


REV: A;B;C;C.1;C.2;

COMPUTER IMAGE CORPORATION 2475W. 2nd AVE DENVER, CO. 80223			
SCALE	NONE	REVISIONS	BY DATE
DATE	11-6-81		
DR	E.R.	CRD	
APVD			
TITLE	SYSTEM IV DIGITAL BUS TERMINATOR		NO CIC-W-33 SHEET 2 OF 4



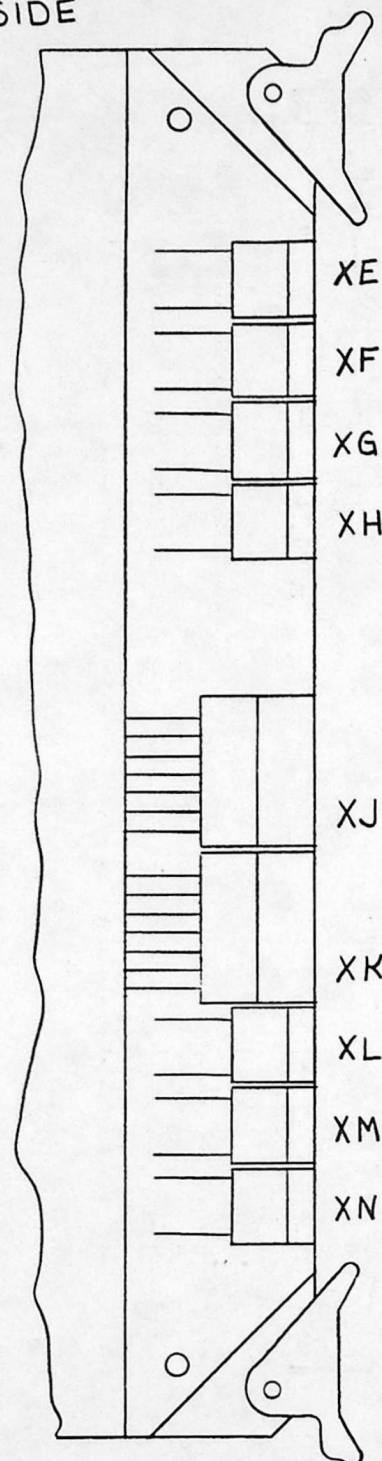
G4 & L3 ARE 7406



REV: A; B; C; C.1; C.2;

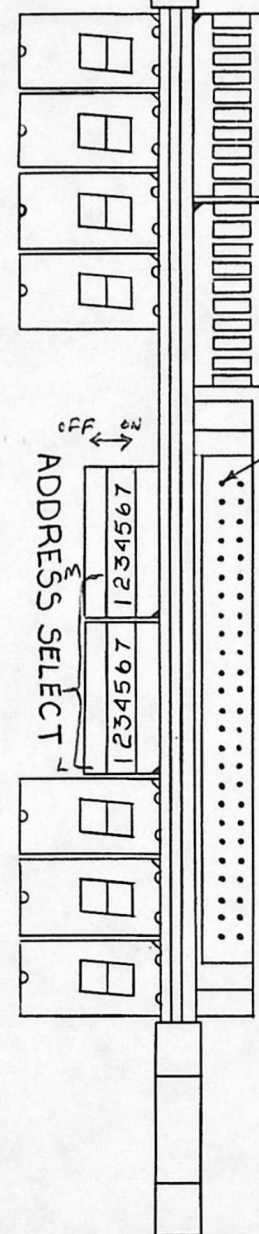
COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	REVISIONS	BY	DATE
NONE			
DATE 11-10-81			
DR E.R. CRD			
APVD			
TITLE	SYSTEM IV		NO
DIGITAL BUS TERMINATOR	DIGITAL BUS TERMINATOR		CIC-W-33
			SHEET 3 OF 4

WIRING SIDE



HEX DATA

HEX ADDRESS

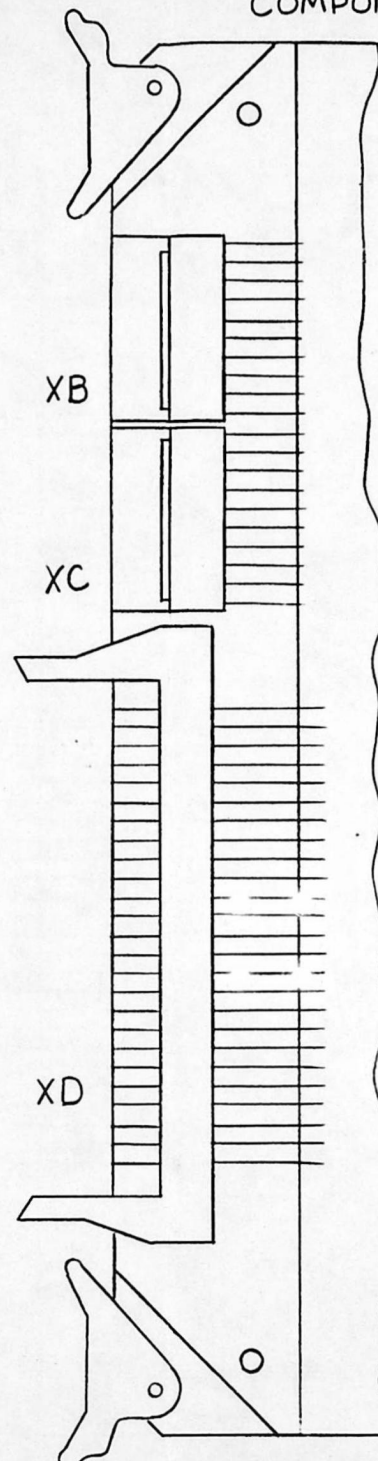


TIMING SIGNALS

DIGITAL BUS

PIN 1

COMPONENT SIDE



XB-1	BRPUSTROEE
2	BRPUWRIT
3	BHSYNCH
4	BSSYNCH
5	BNIHSYNCH
6	BFSYNCH
7	BVBKG
8	BCON 0
9	BCON 1
10	BRPUIOSEL
XC-1	BBUSSR
2	BSYNCHSR 0
3	BSYNCHSR 1
4	BMSTRCLK
5	SPARE
6	N/A
7	N/A
8	N/A
9	N/A
10	N/A

REV: A,B,C,C.1,C.2;

SCALE		REVISIONS		BY	DATE
DATE 12/30/81					
DRY	GSH	CRD			
APVD					
TITLE SYSTEM IV DIGITAL BUS TERMINATOR LAYOUT				NO CIC-W-33 SH 4 OF 4	

SCHEMATIC

WIRE LIST

[illegible]

TOLERANCES UNLESS OTHERWISE SPECIFIED		COMPUTER IMAGE	
FRACTIONS	DEC ANGLES		
±	±	AUDIO FILTER	
±	±		
APPROVALS	DATE	CONTROL SHEET	
DRAWN			
CHECKED		SCALE	SIZE
			B
		DRAWING NO.	
		CIC-W-43	
		DO NOT SCALE DRAWING	
		SHEET 8 - 1	

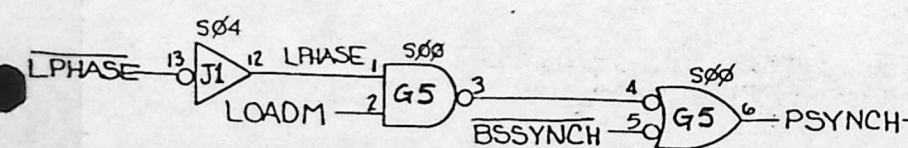
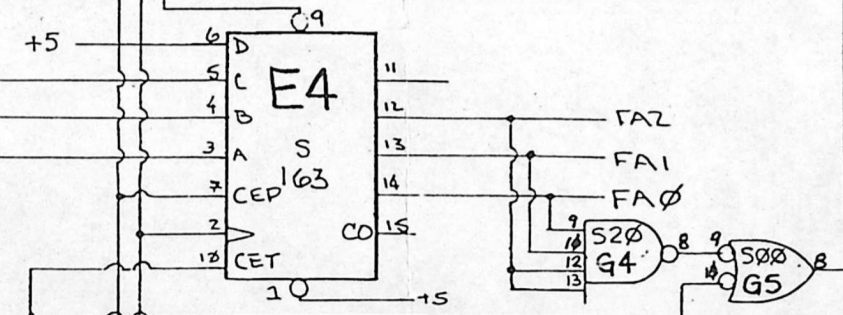
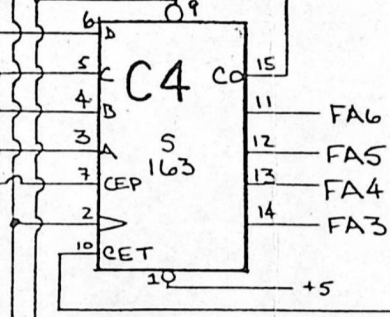
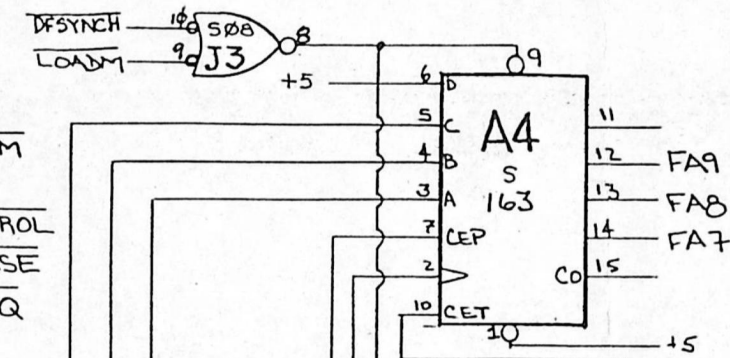
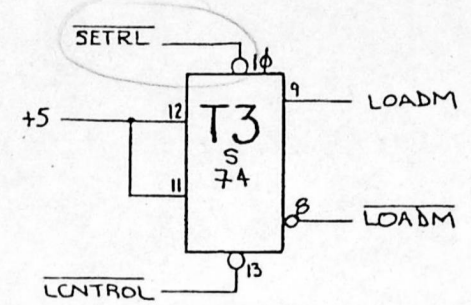
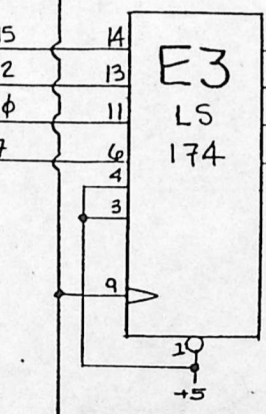
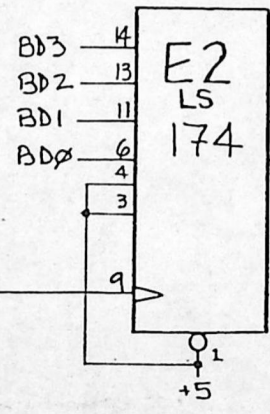
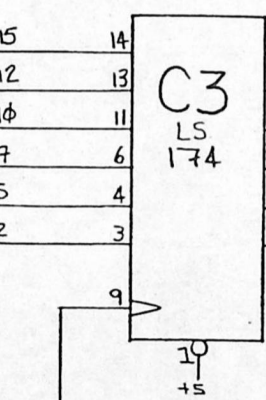
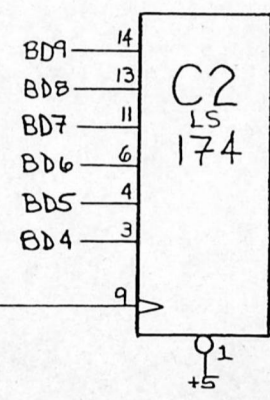
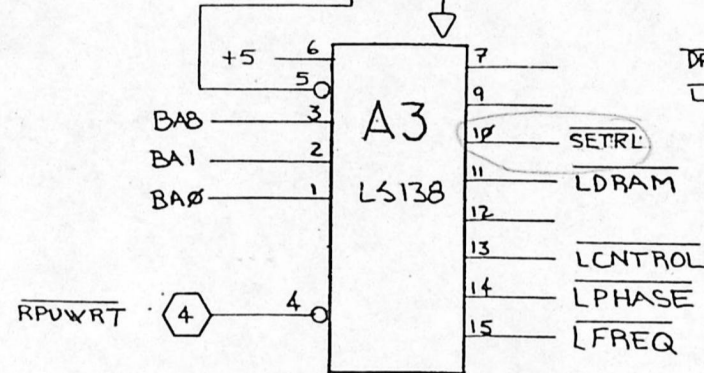
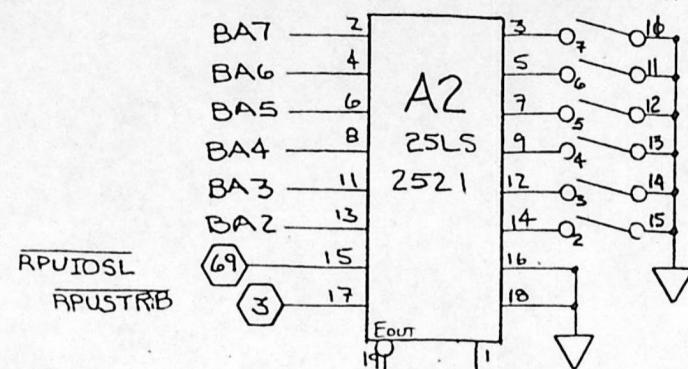
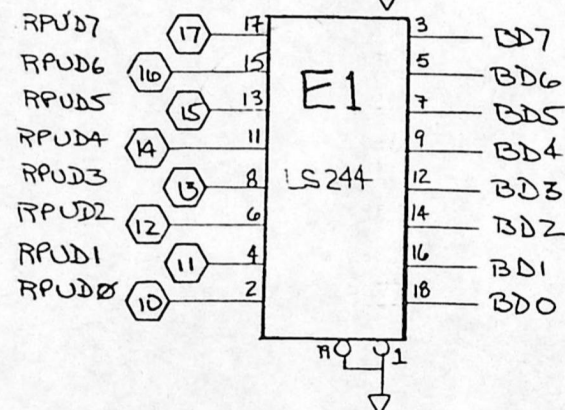
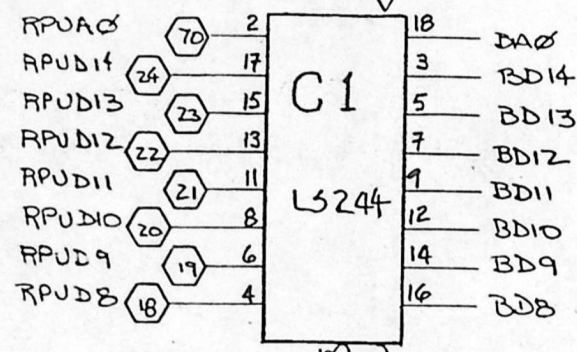
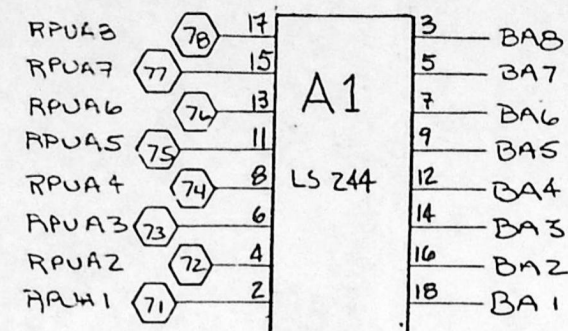


NOT USED
S37 R2-C,D
S04 J1-D

Non-Interlaced/Interlaced modes.

CIC-W-66

FUNCT. GEN.



REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

TOLERANCES UNLESS OTHERWISE SPECIFIED
FRACTIONS DEC ANGLES
± ± ±

REV:
A;B;C;D;E;
F;I

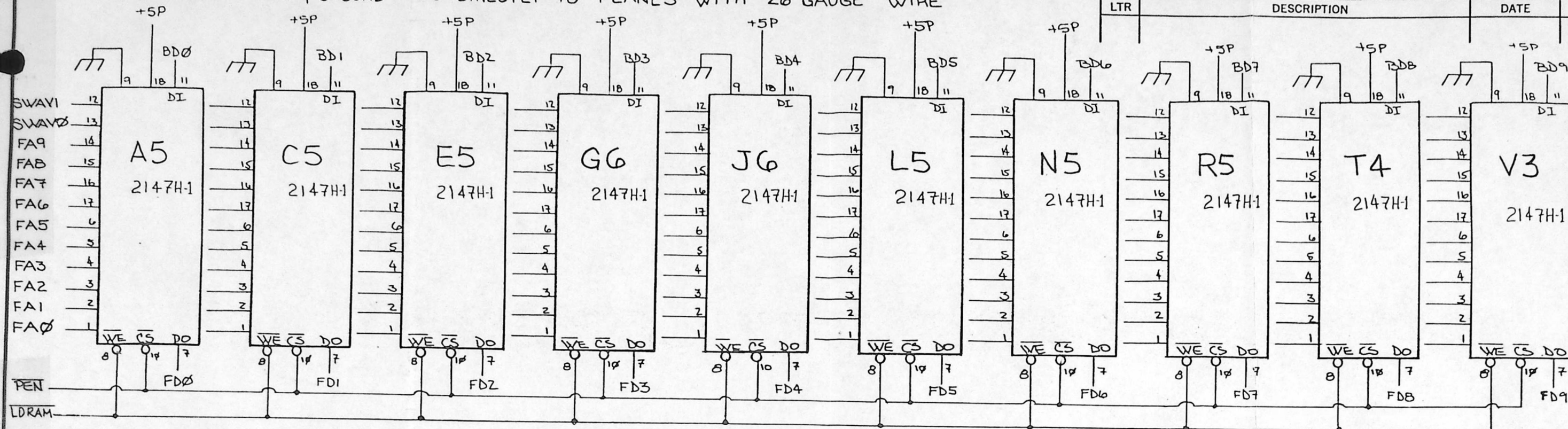
APPROVALS	DATE
DRAWN SP/LD	4-21-82
CHECKED REV D	

ATA Corp.
 COMPUTER IMAGE
FUNCTION GENERATOR
 Buffers & Address Generator

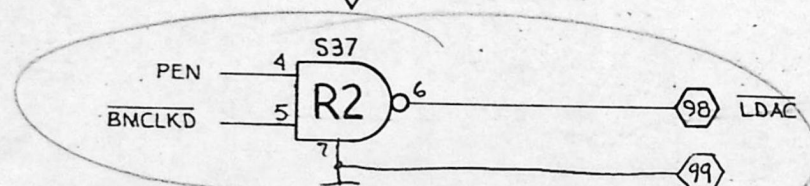
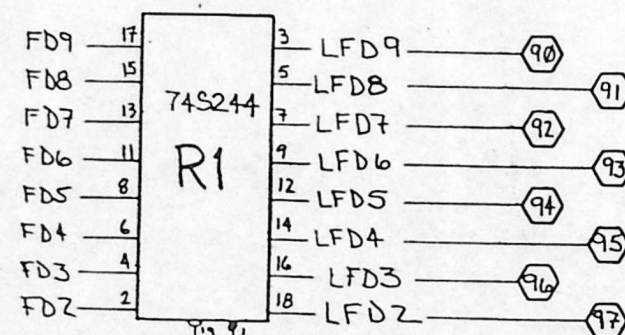
SCALE	SIZE B	DRAWING NO. CIC-W-66
-------	------------------	--------------------------------

DO NOT SCALE DRAWING
 SHEET 1 OF 3

NOTE: CONNECT RAM POWER & GROUND PINS DIRECTLY TO PLANES WITH 26 GAUGE WIRE

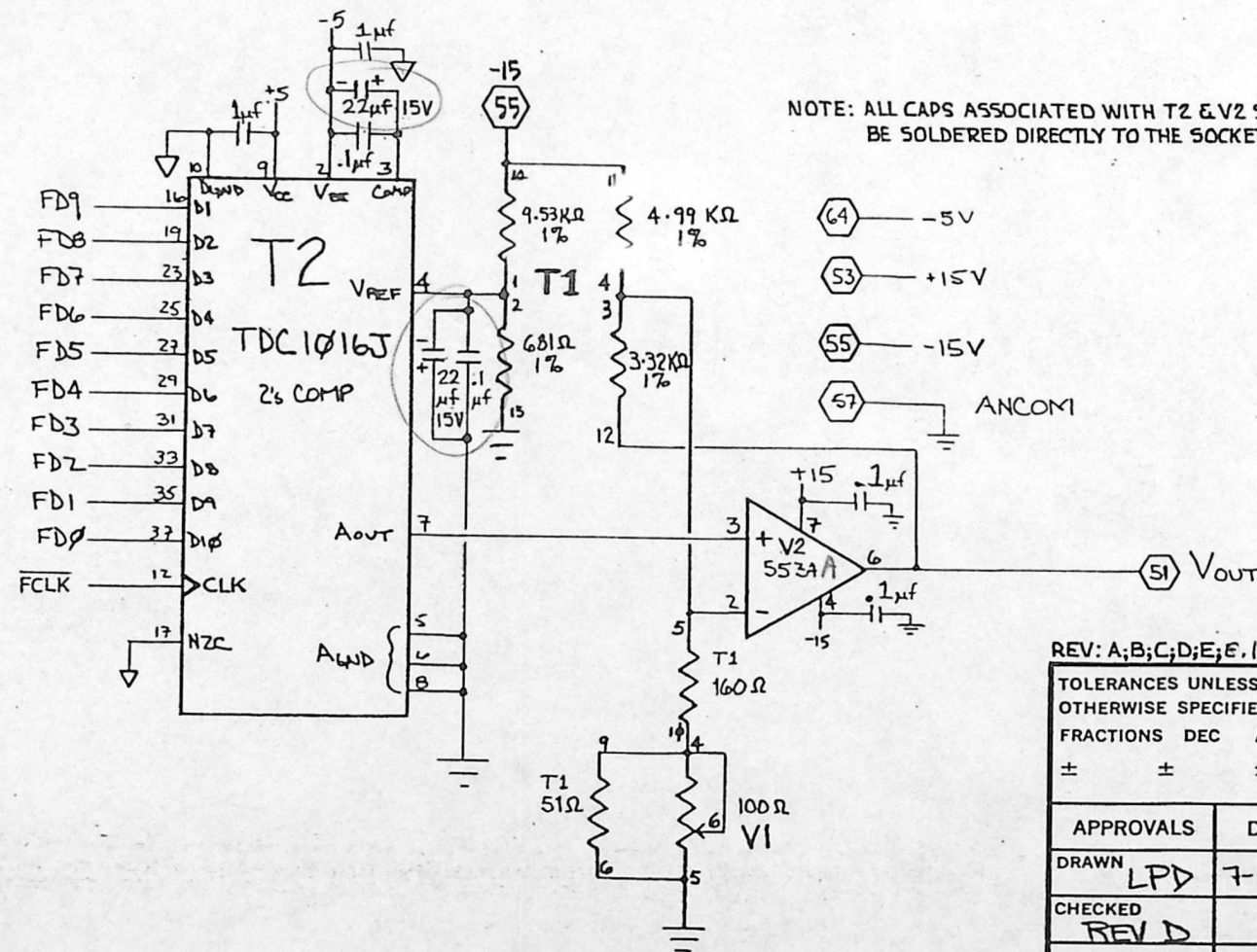


REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



NOTE: $\frac{1}{2}$ DENOTES ISOLATED ANALOG GND BETWEEN ROWS U & V.

UNUSED: AND J3-11
NAND G5-11, J4-3, R2-8, R2-11
INV J1-8, G1-8



NOTE: ALL CAPS ASSOCIATED WITH T2 & V2 SHALL BE SOLDERED DIRECTLY TO THE SOCKET PINS.

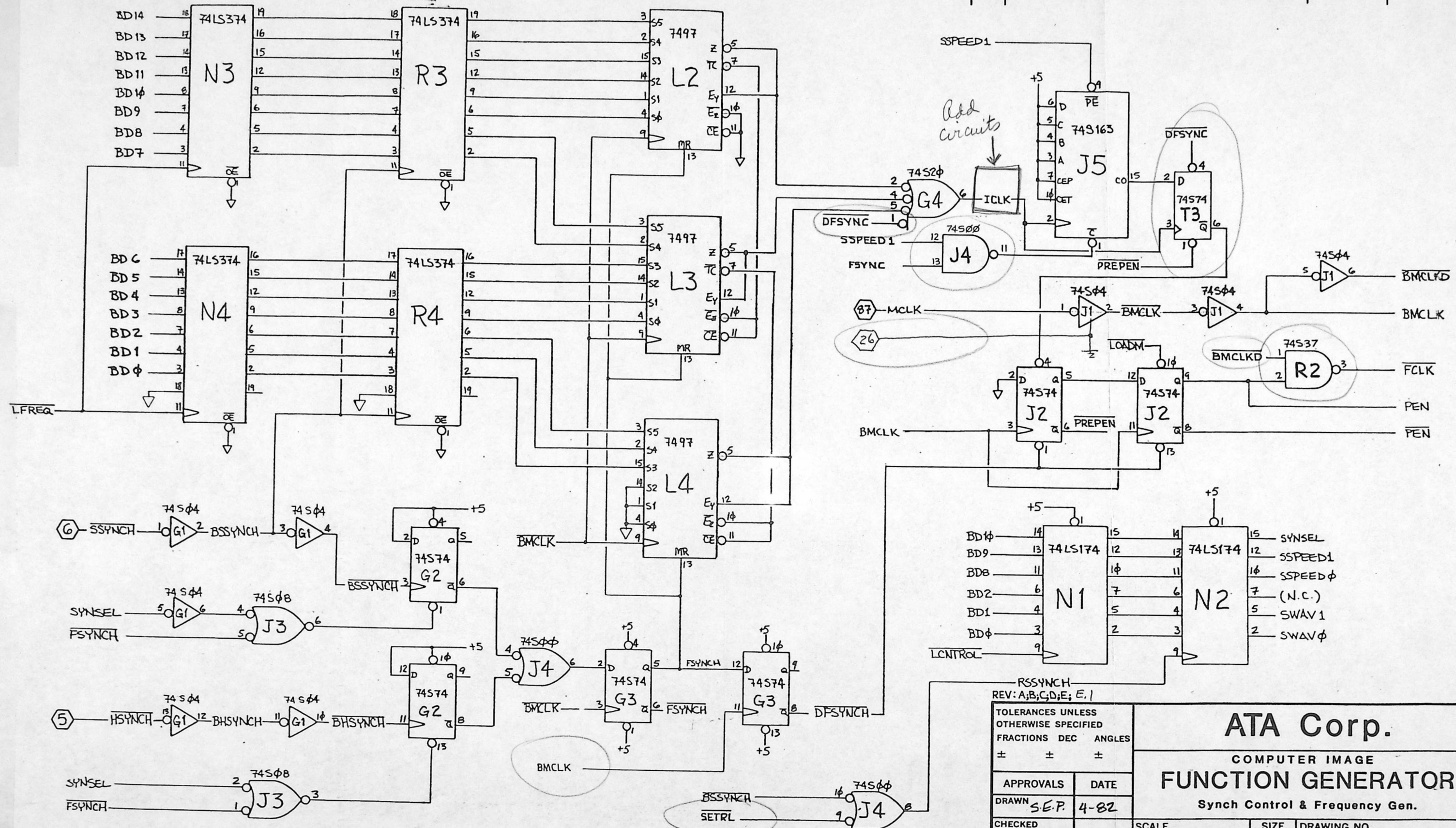
T1	
1	9.53 K Ω - 1%
2	6.81 Ω - 1%
3	3.32 K Ω - 1%
4	4.99 K Ω - 1%
5	100 Ω - 5%
6	51 Ω - 5%
7 (N.C.)	(NO CONNECTION)

REV: A;B;C;D;E;I

TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
$\pm$	$\pm$	$\pm$
APPROVALS	DATE	
DRAWN LPD	7-7-81	
CHECKED REV D		

ATA Corp.			
COMPUTER IMAGE			
FUNCTION GENERATOR			
D/A Output & WFM Storage			
SCALE	SIZE B	DRAWING NO. CIC-W-66	
DO NOT SCALE DRAWING		SHEET 2 OF 3	

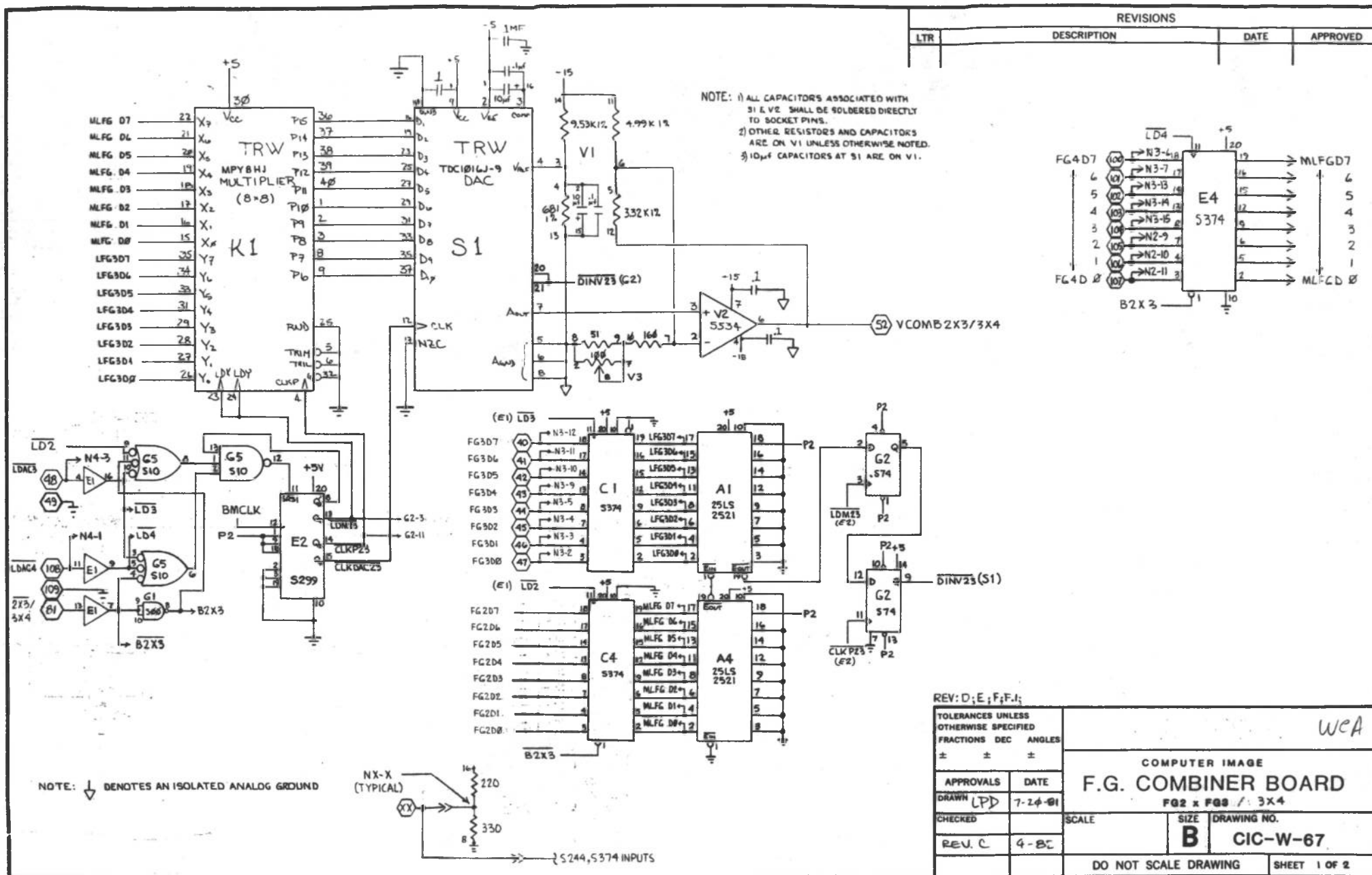
REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

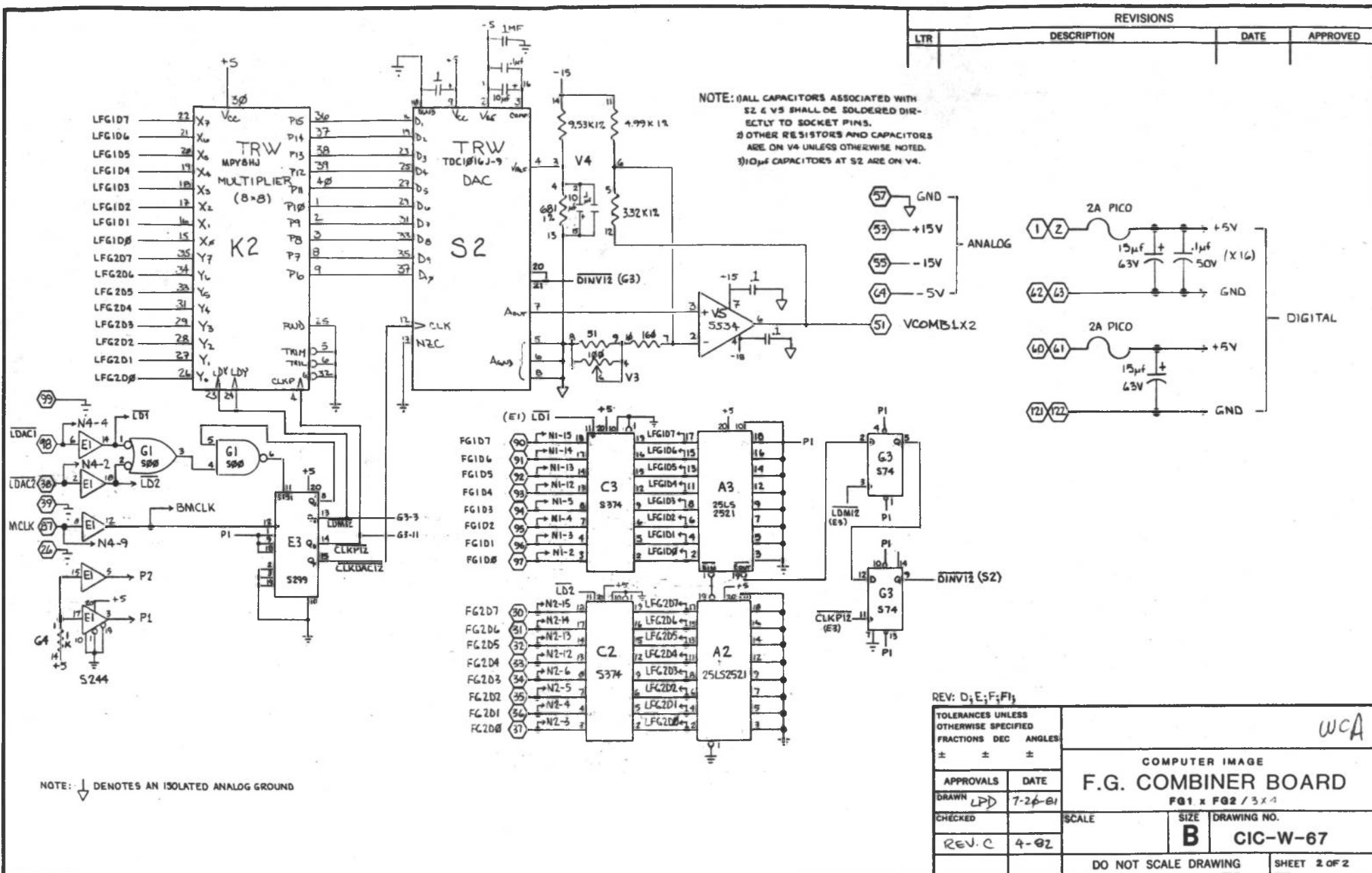


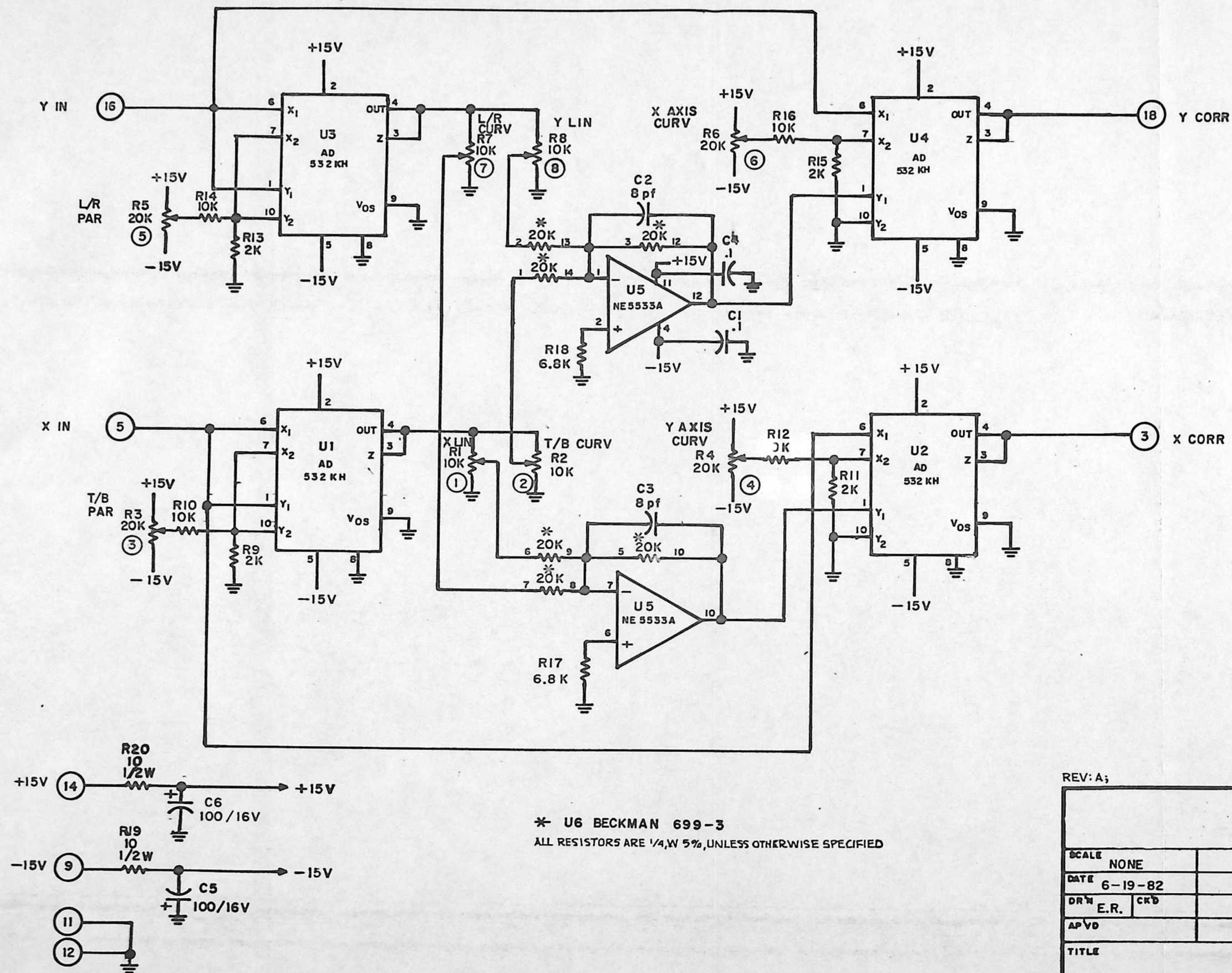
NOTE: L2, L3, L4 MUST BE SELECTED FOR RELIABLE OPERATION

TOLERANCES UNLESS OTHERWISE SPECIFIED			
FRACTIONS	DEC	ANGLES	
±	±	±	
APPROVALS	DATE		
DRAWN S.E.P.	4-82		
CHECKED REV D			

ATA Corp.			
COMPUTER IMAGE			
FUNCTION GENERATOR			
Synch Control & Frequency Gen.			
SCALE	SIZE B	DRAWING NO. CIC-W-66	
DO NOT SCALE DRAWING		SHEET 3 OF 3	

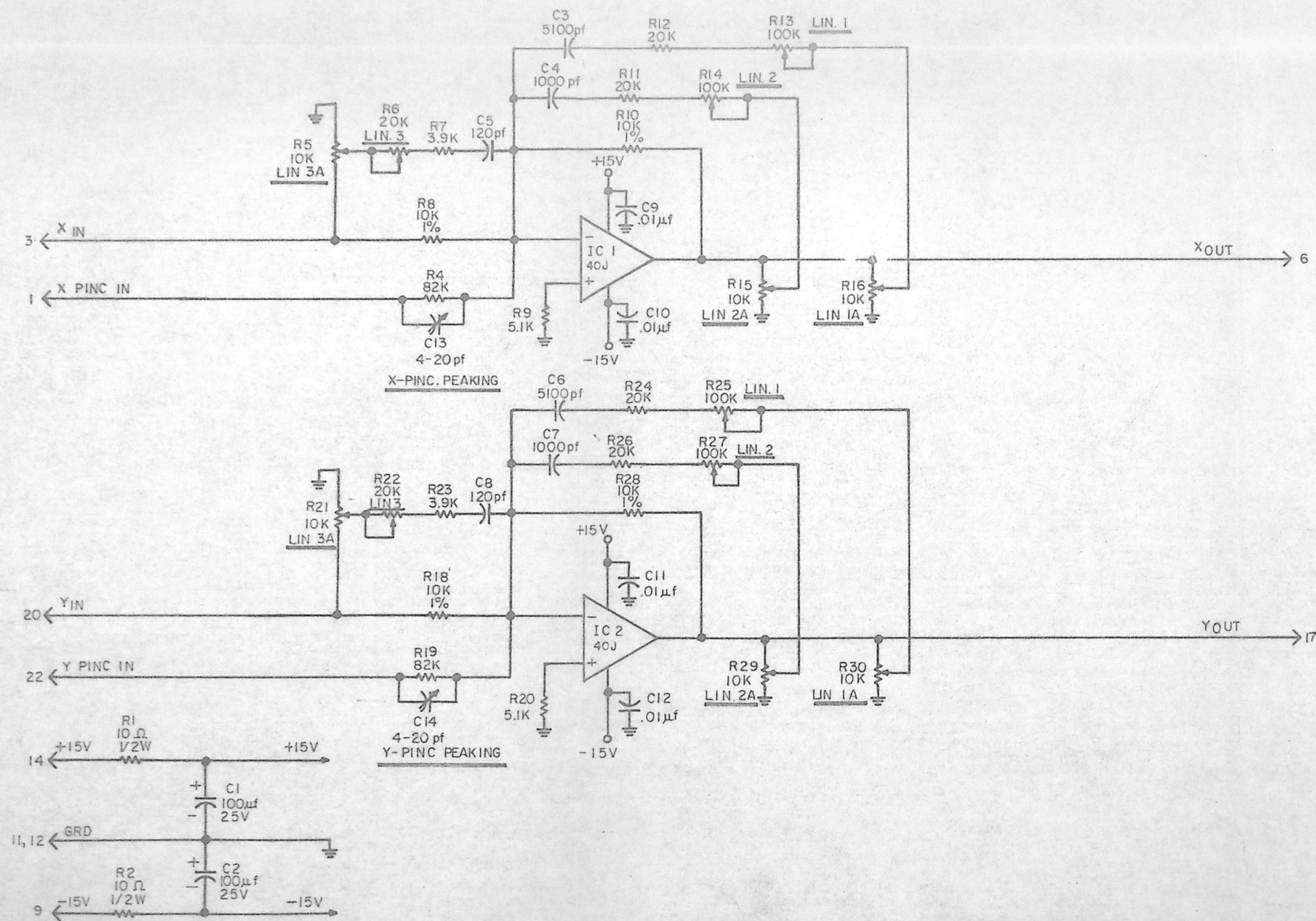




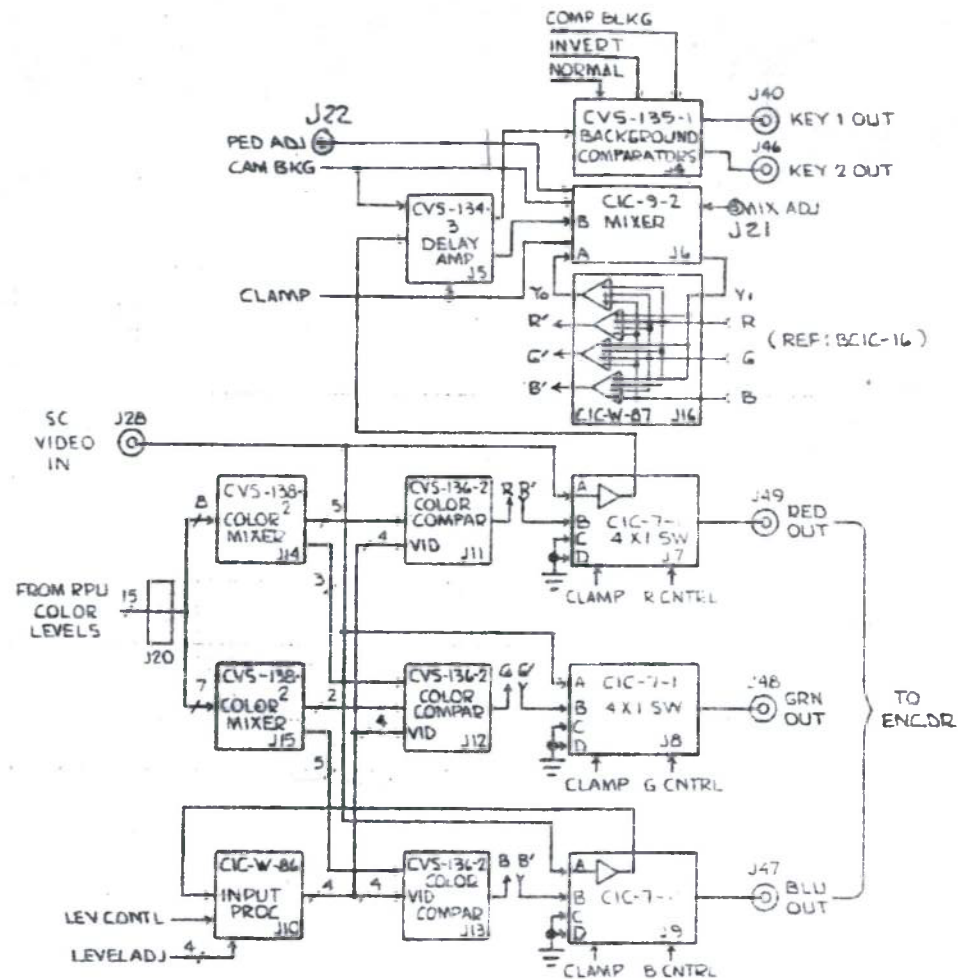
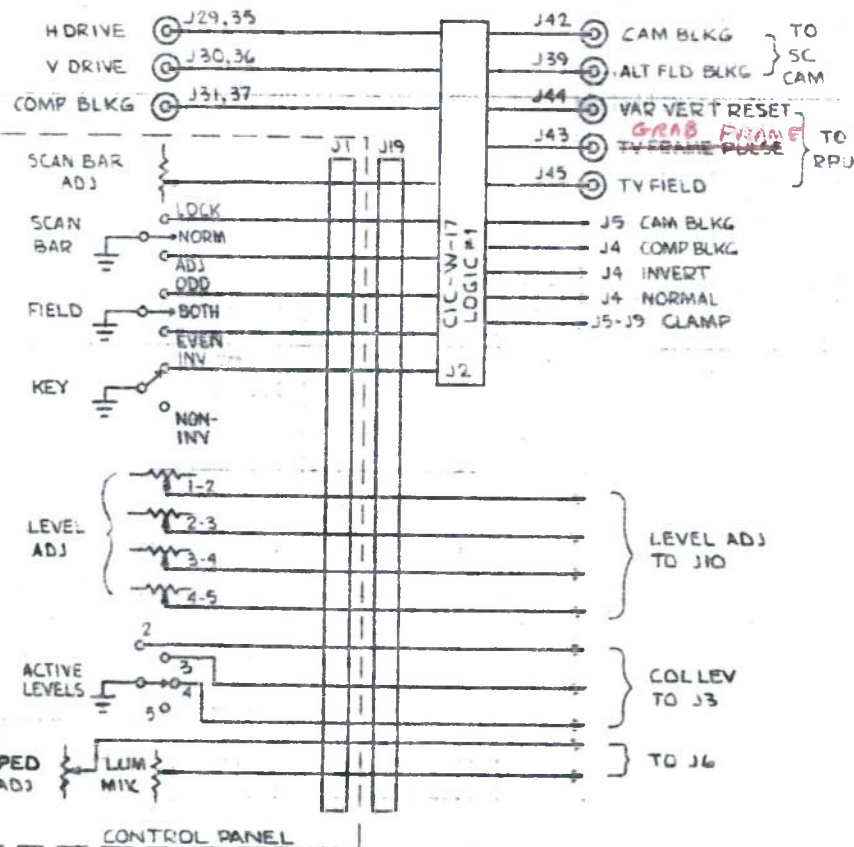
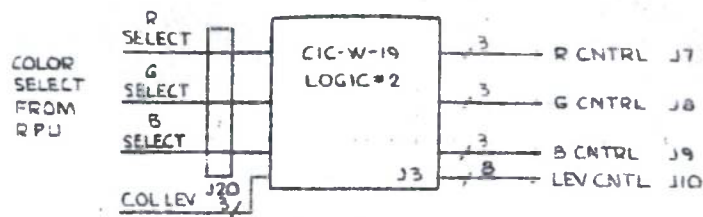


REV: A;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 6-19-82			
DRN E.R.	CKD		
APVD			
TITLE SYSTEM IV MON. PINCUSHION CORRECTION		NO CVS-127-2 SH 1 OF 1	



REV:		COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE. DENVER, COLORADO 80223	
SCALE	NONE	REVISION	BY	DATE	
DATE	10-19-72				
DR'N	E. R.	CR'D			
APP'VD					
TITLE MON YOKE COMP. & PINCUSHION ADDER					NO. CVS-128 SH 1 OF 1

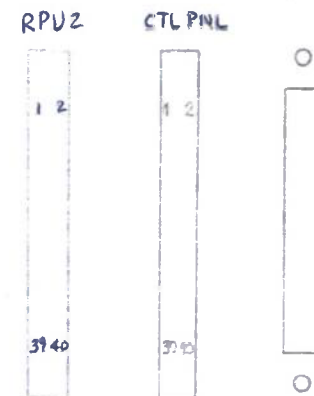
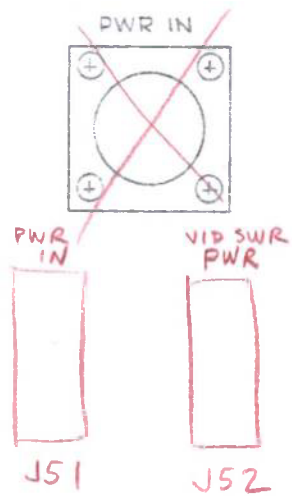
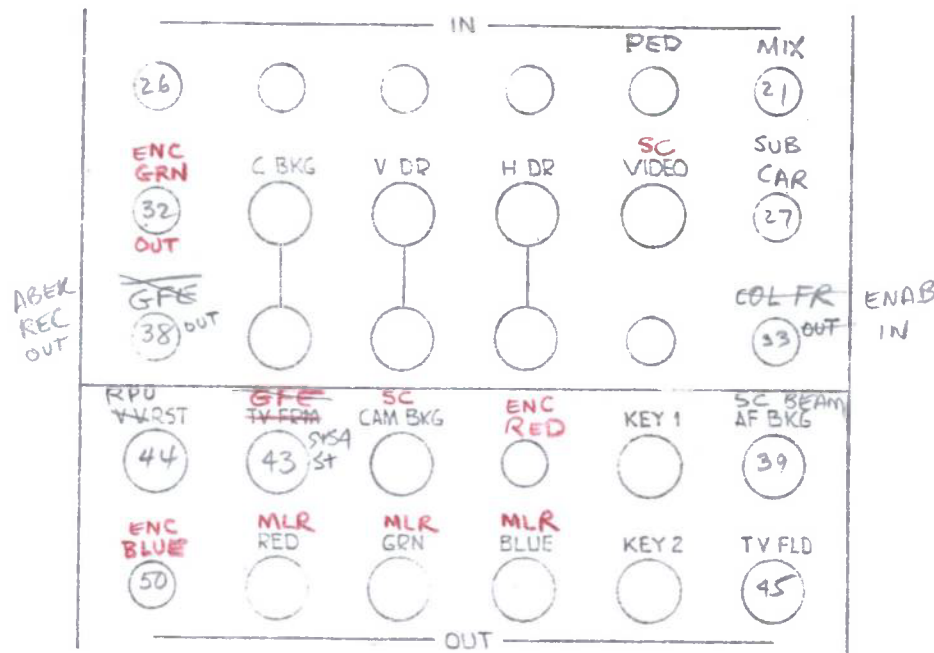


COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	REVISIONS	BY	DATE
DATE 4/20/54			
DR EVL			
AP			
TOLERANCE		TITLE	
FRAC	± 1/32	COLOR CONTROLLER BLOCK DIAGRAM SYSTEM IV	
.XX	± .015		
.XXX	± .005		
		BCIC-7-1	

WCA

CABLING VIEW

TOP COLOR CONTROLLER TOP



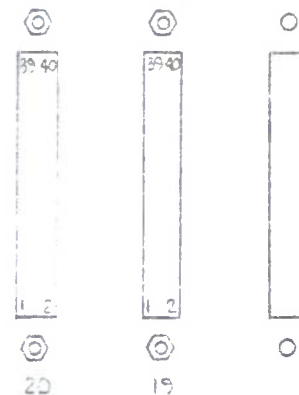
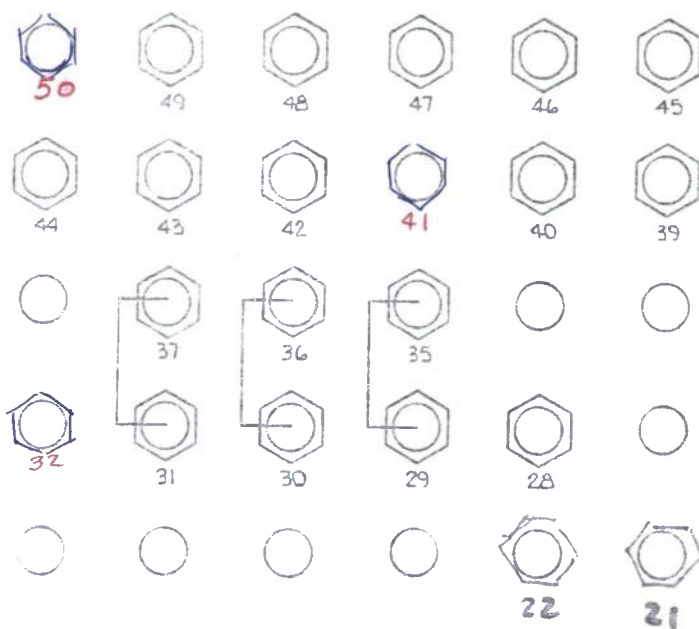
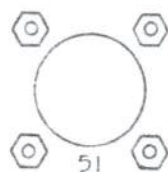
REV	SCHEDULE	COMPUTER IMAGE CORP
11/15/82	ORIG EVL	COLOR CONTROLLER REAR PANEL
A	10/9/84	CONNECTOR FUNCTION
		FCIC-9 SH 1 OF 2

026-4

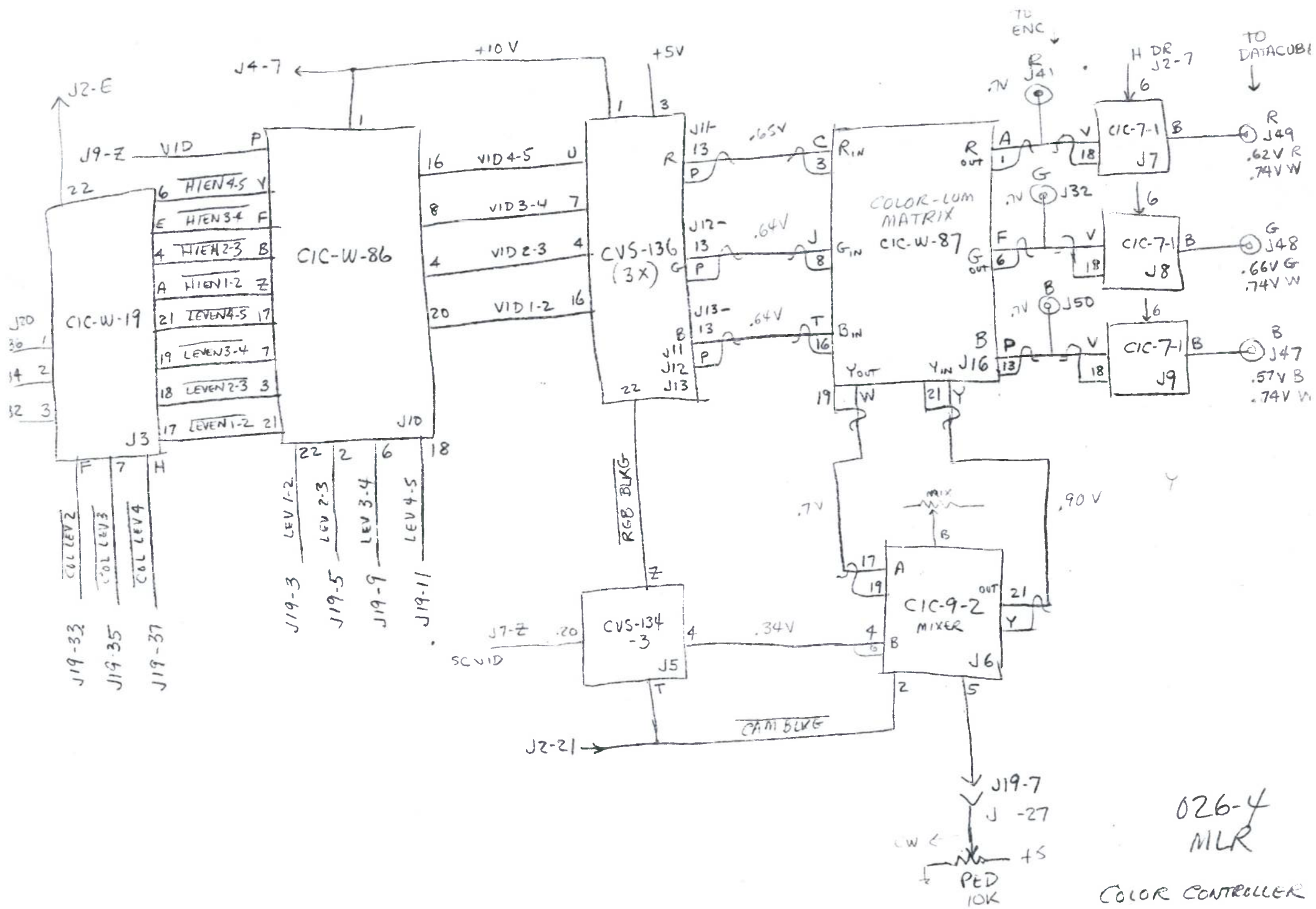
HINGE

WIRING VIEW

HINGE



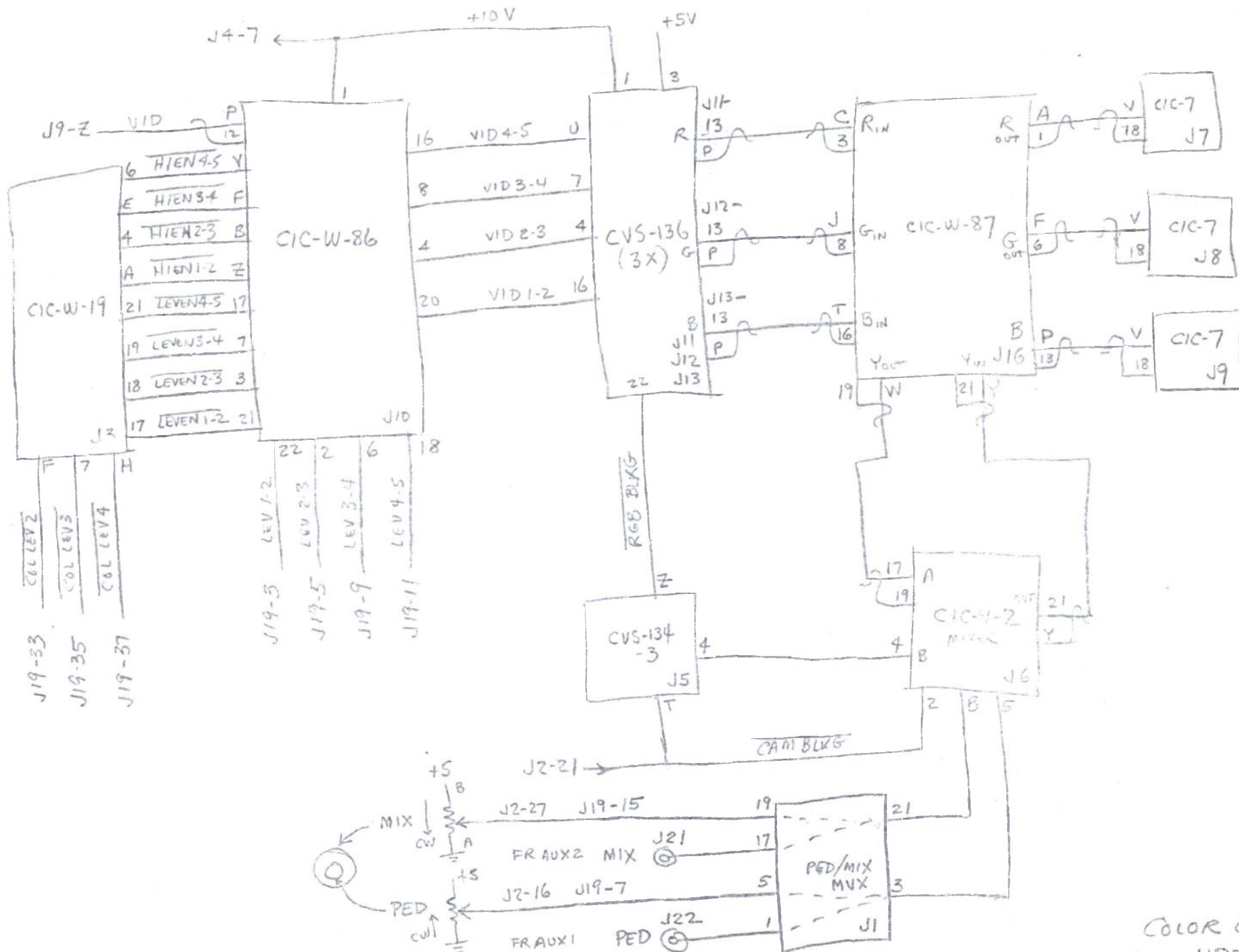
REV: 4:	
COMPUTER IMAGE CORP	
COLOR CONTROLLER REAR PANEL	
CONNECTOR NUMBERING	
FCIC-9	SH 2 OF 2



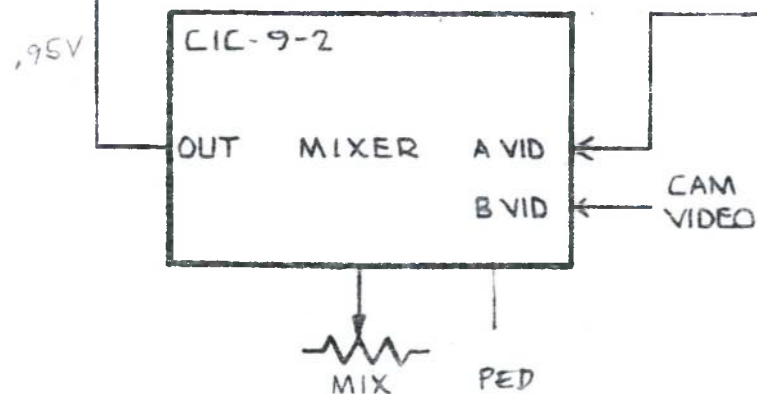
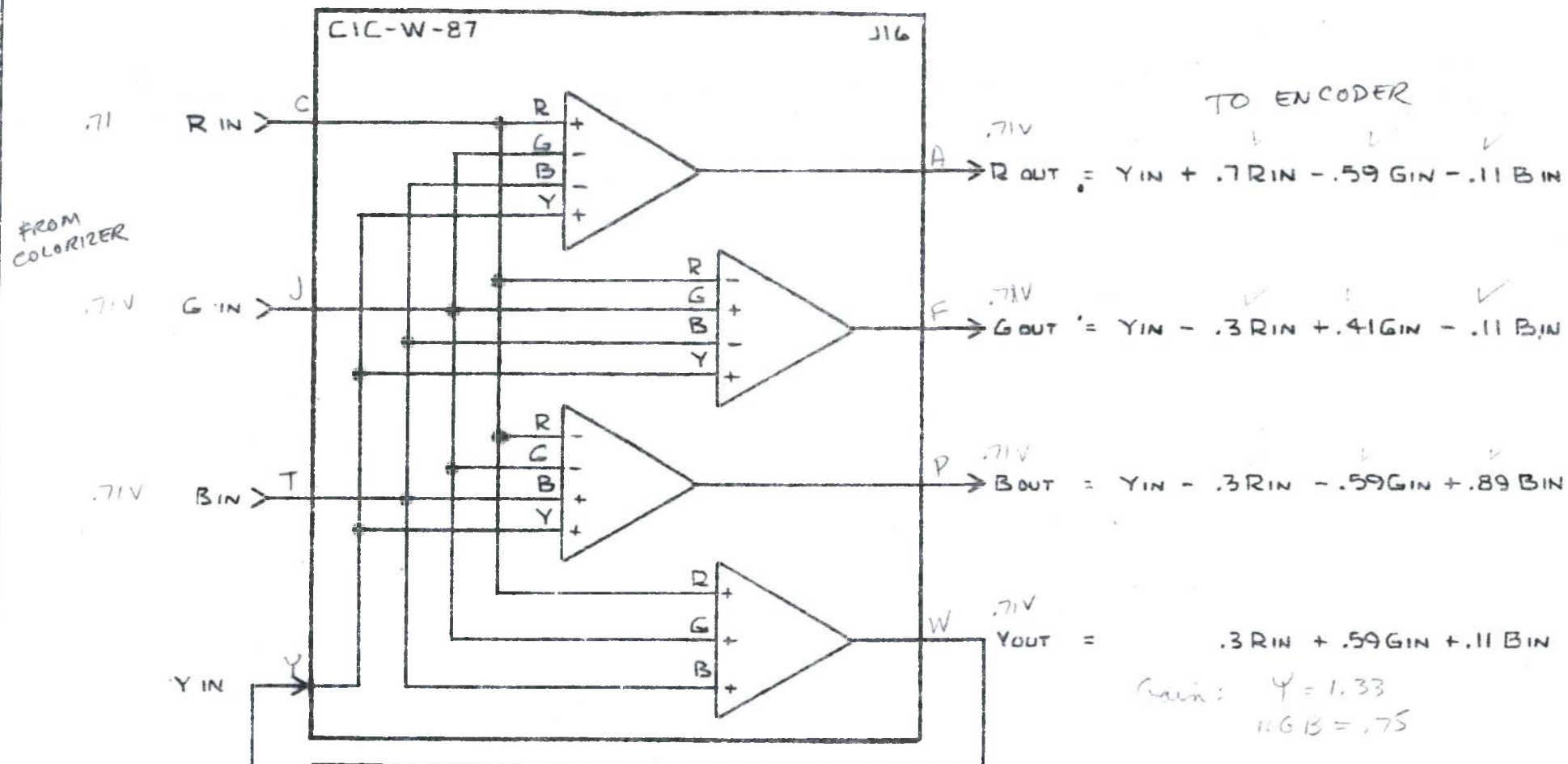
026-4
MLR

COLOR CONTROLLER
UPDATES
10-16-85

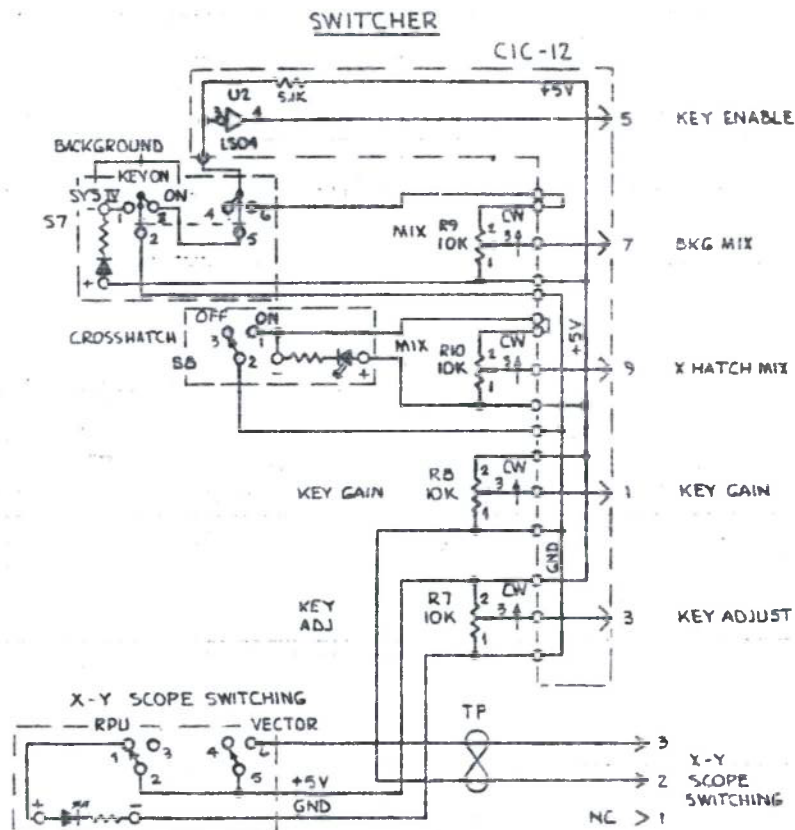
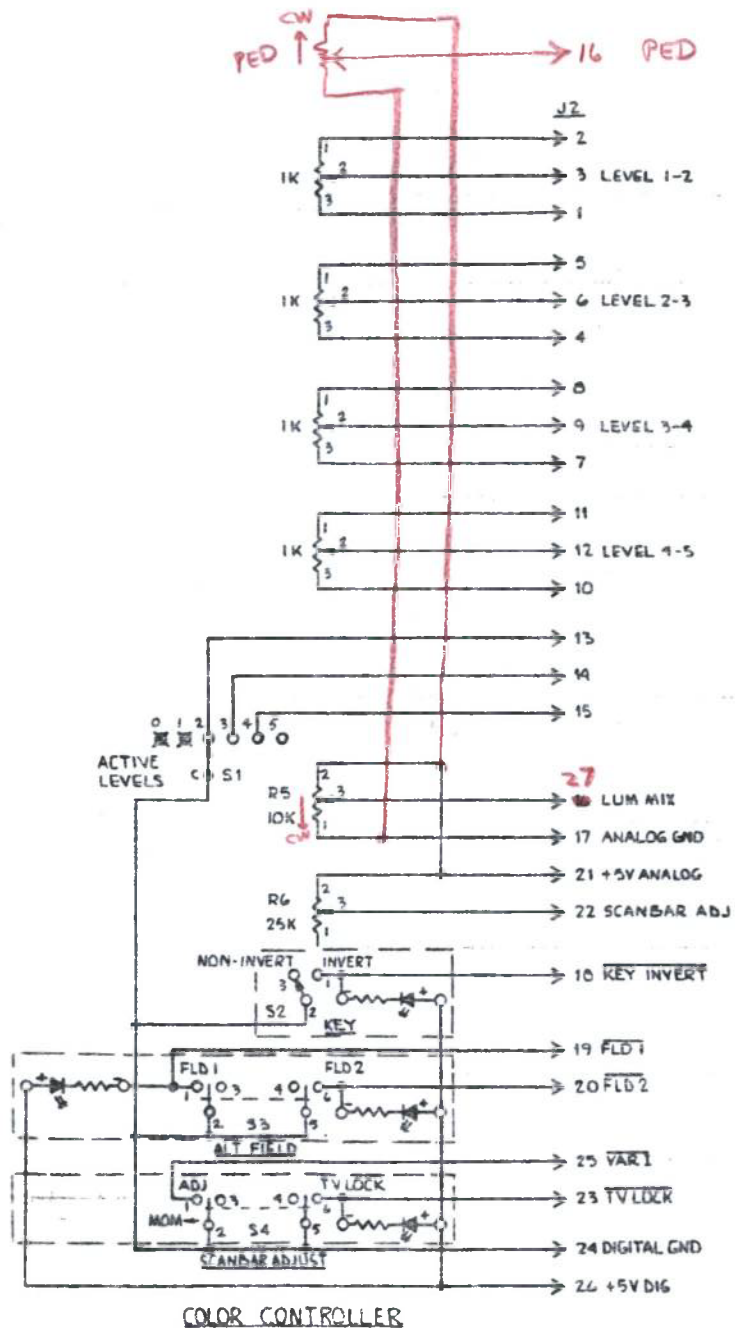
026-4



COLOR CONTROLLER
UPDATES
10-16-85
11-4-86



 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE. DENVER, COLORADO 80223	
SCALE		REVISIONS	BY DATE
DATE	10/23/85		
DRN	EVL	CRD	
APVD			
TITLE		NO	
COLOR LUMINANCE MATRIX		SH 1 OF 1	
		BCIC-16	



REV: A;

COMPUTER IMAGE CORP

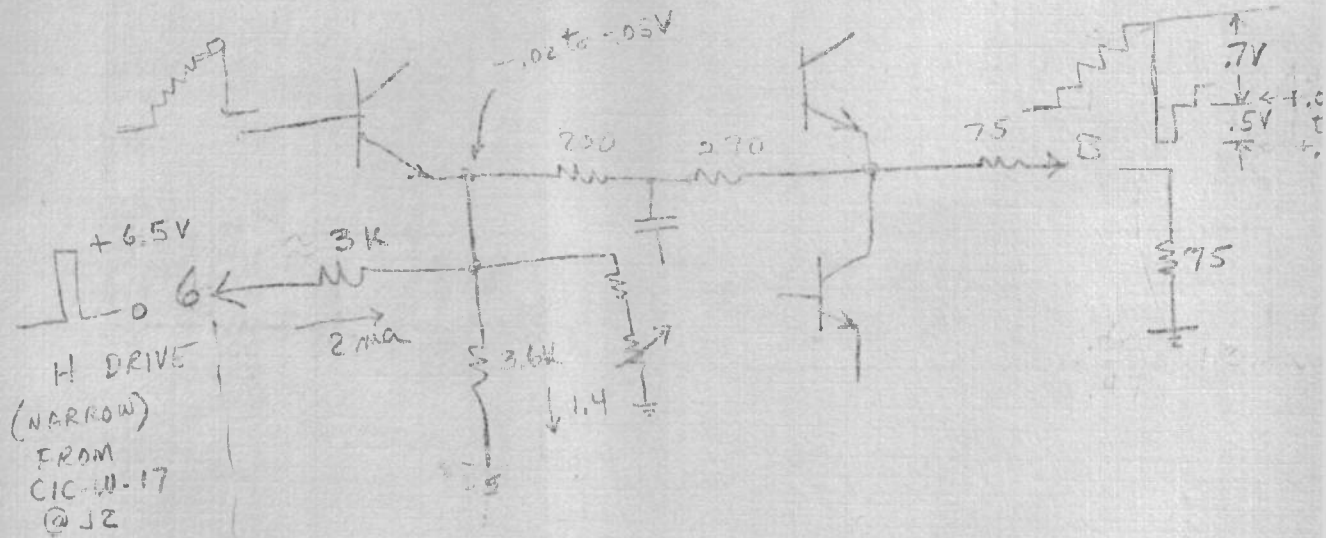
VIDEO SWITCHER/COLORIZER

CONTROL PANEL

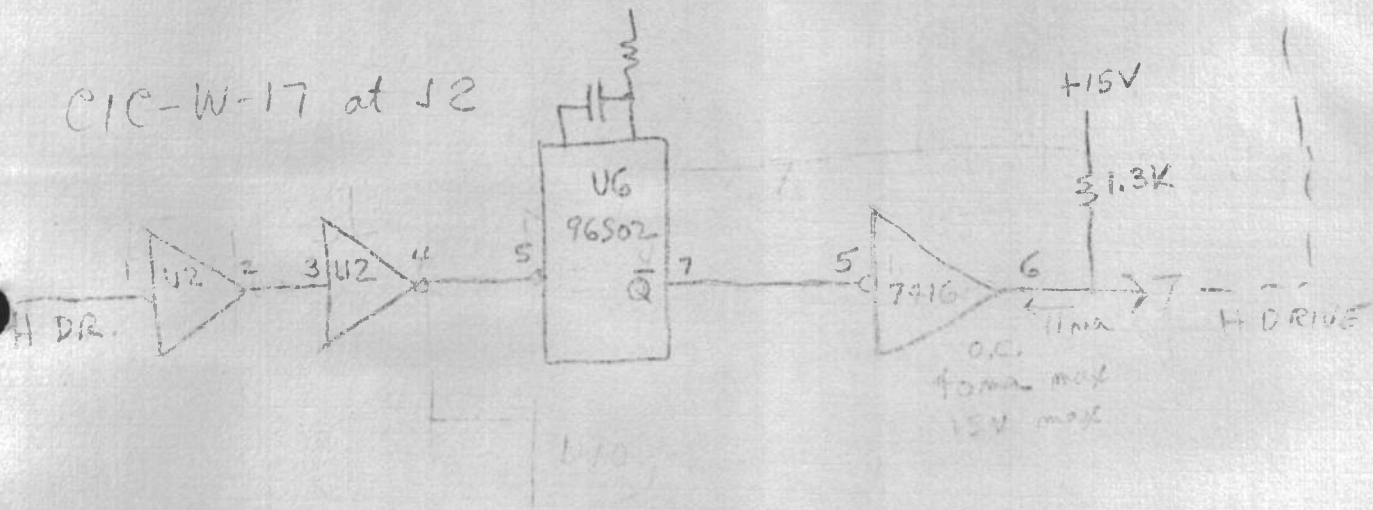
WCIC-104-2 SH 1 OF 1

026-4

CIC-7-1 J7, 8, 9

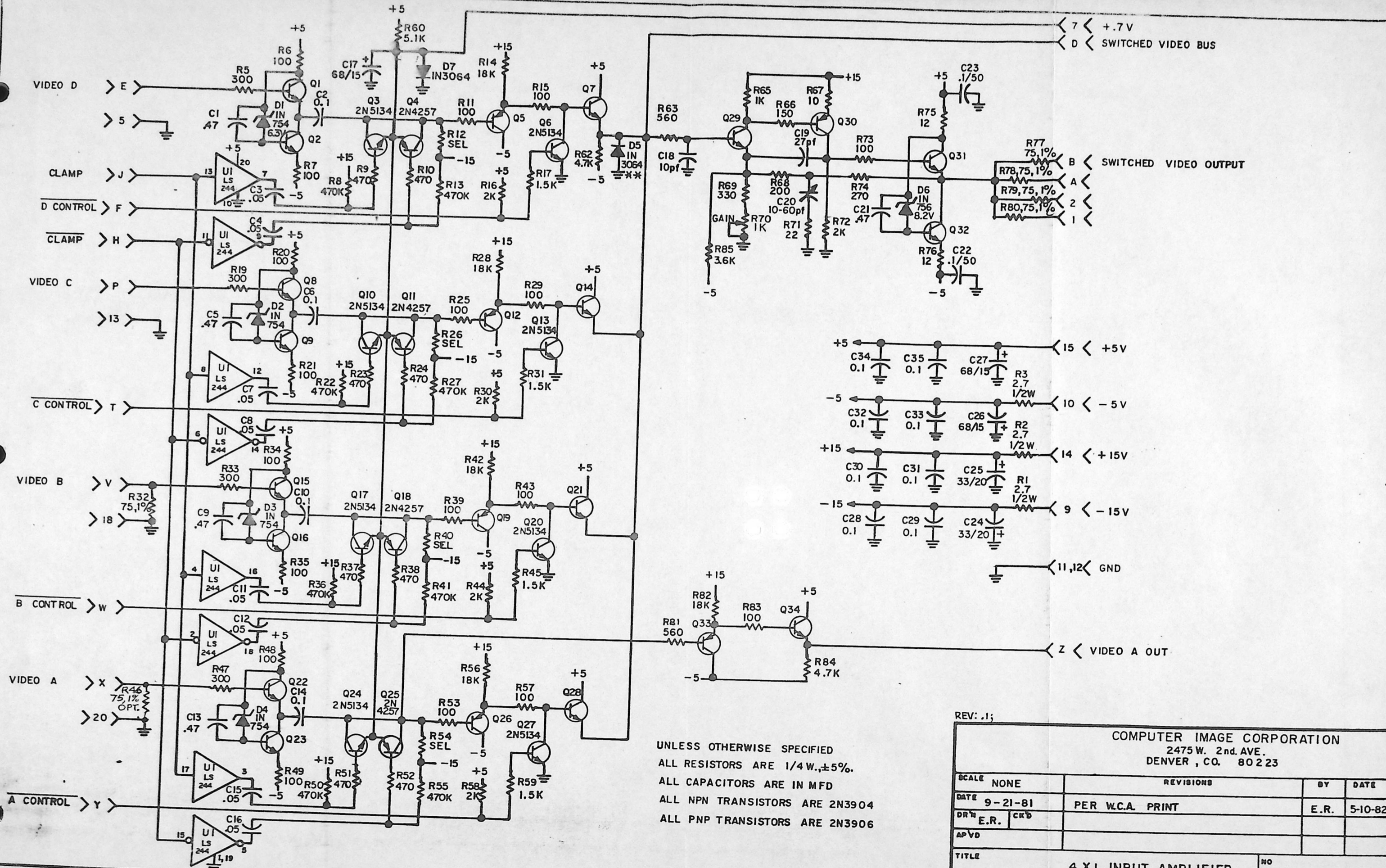


CIC-W-17 at J2



65

MLR
COLOR CONTROLLER

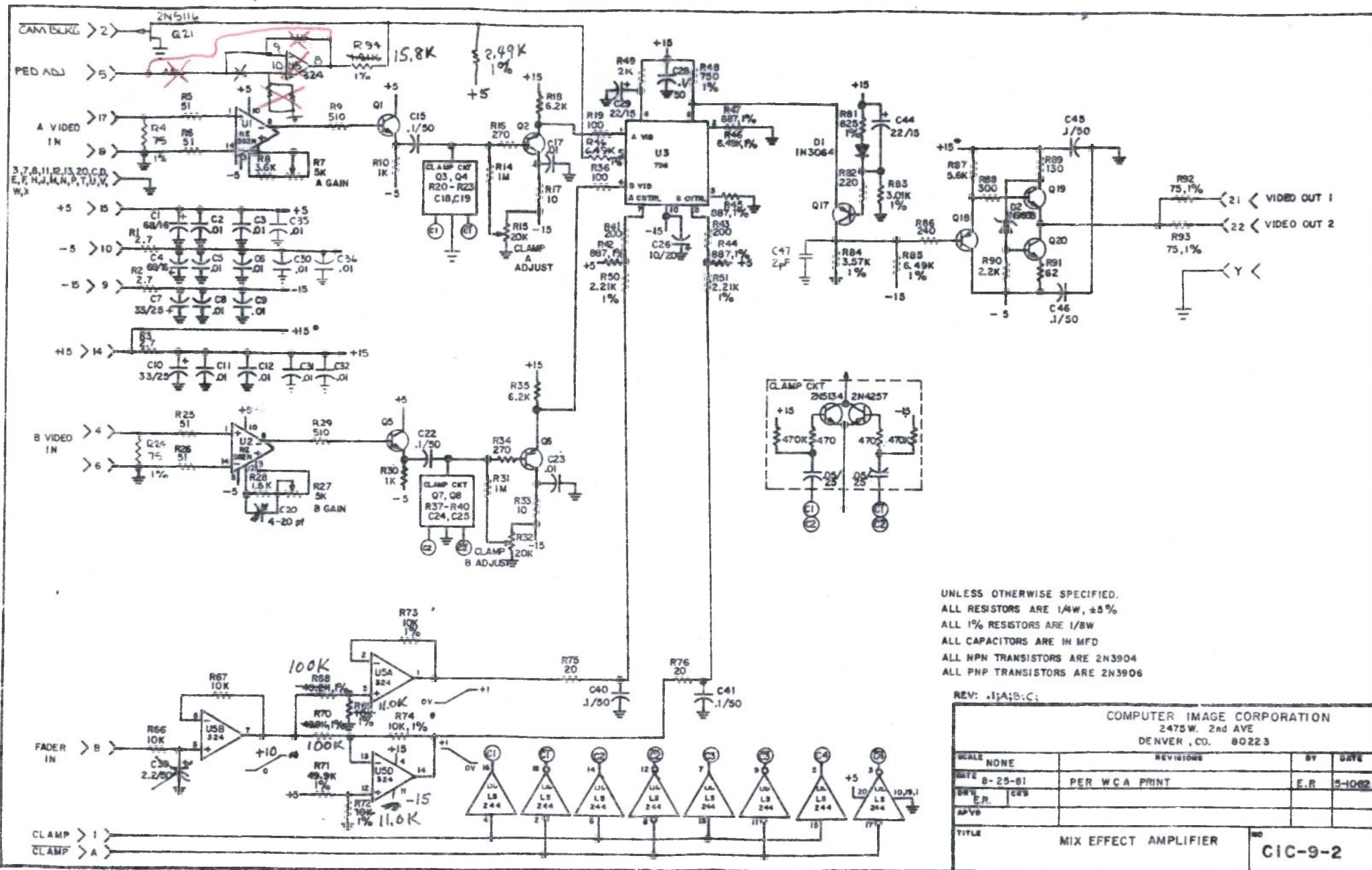


UNLESS OTHERWISE SPECIFIED
ALL RESISTORS ARE 1/4W., ±5%.
ALL CAPACITORS ARE IN MFD
ALL NPN TRANSISTORS ARE 2N3904
ALL PNP TRANSISTORS ARE 2N3906

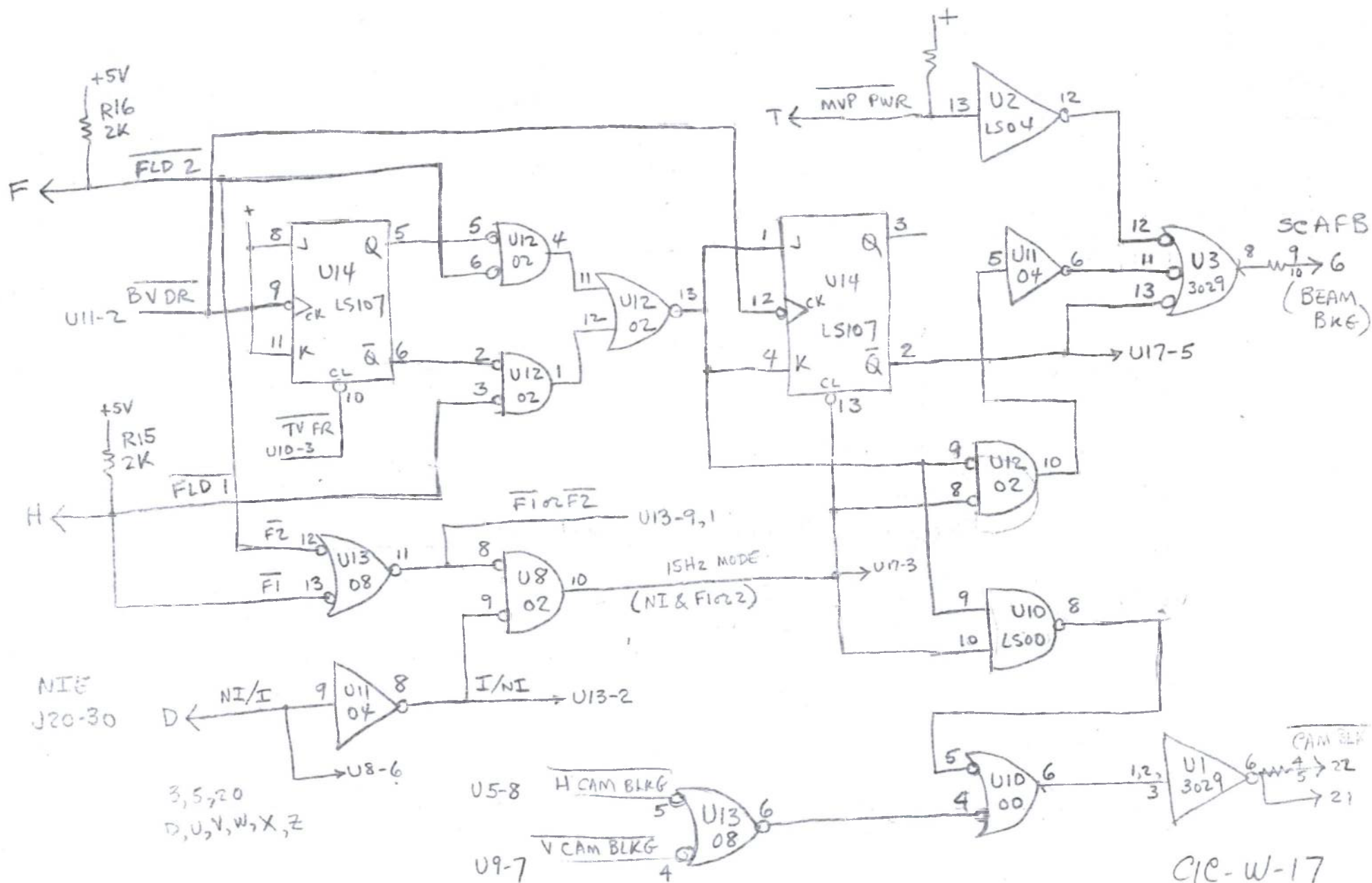
REV: .1;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE. DENVER , CO. 80223				
SCALE NONE	REVISIONS		BY	DATE
DATE 9-21-81	PER W.C.A. PRINT		E.R.	5-10-82
DRN E.R.	CKD			
APVD				
TITLE 4 X 1 INPUT AMPLIFIER 8 SWITCHER		NO CIC-7-1 SH 1 OF 1		

4-10K DIP
↓

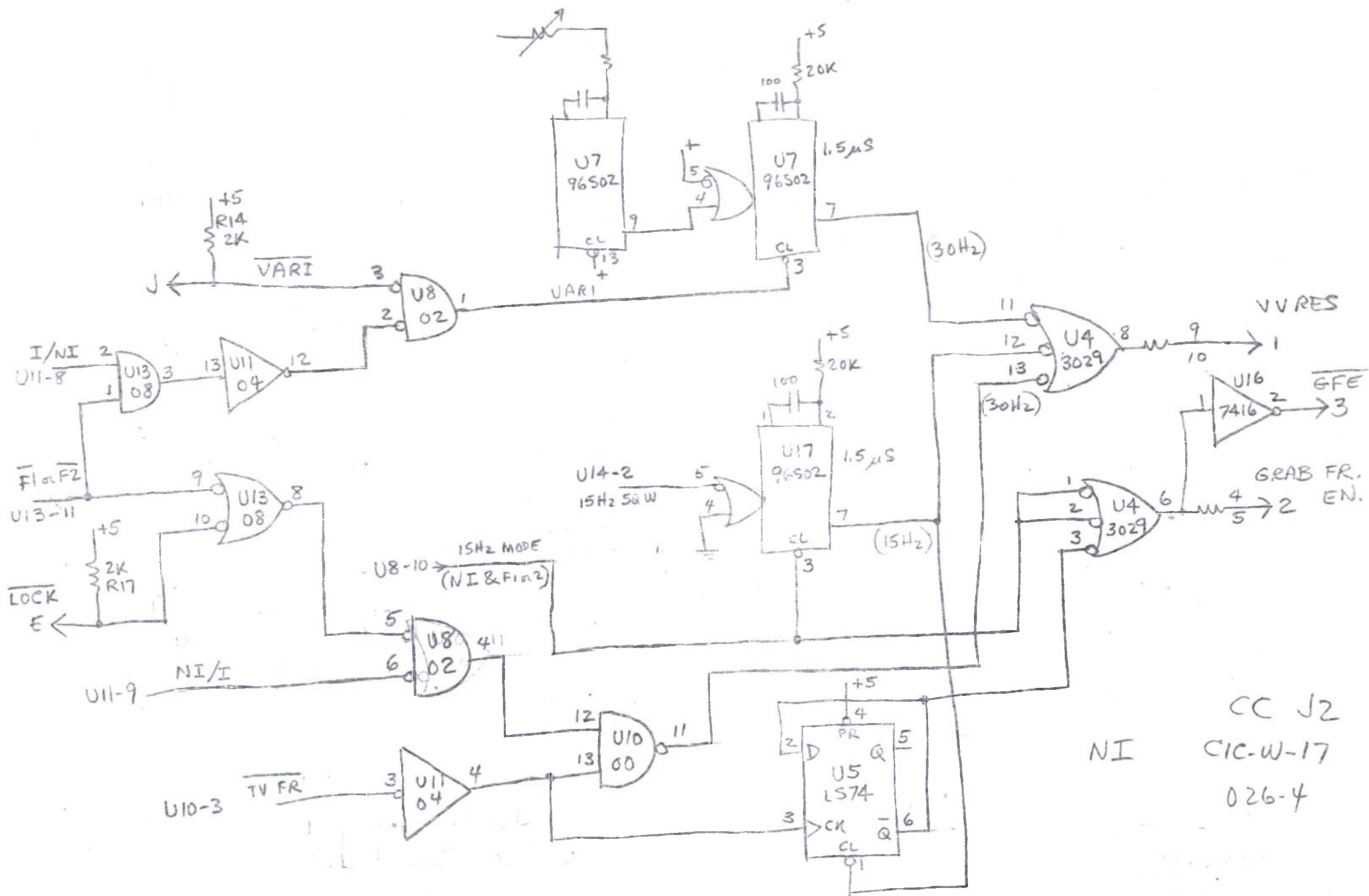


026-4

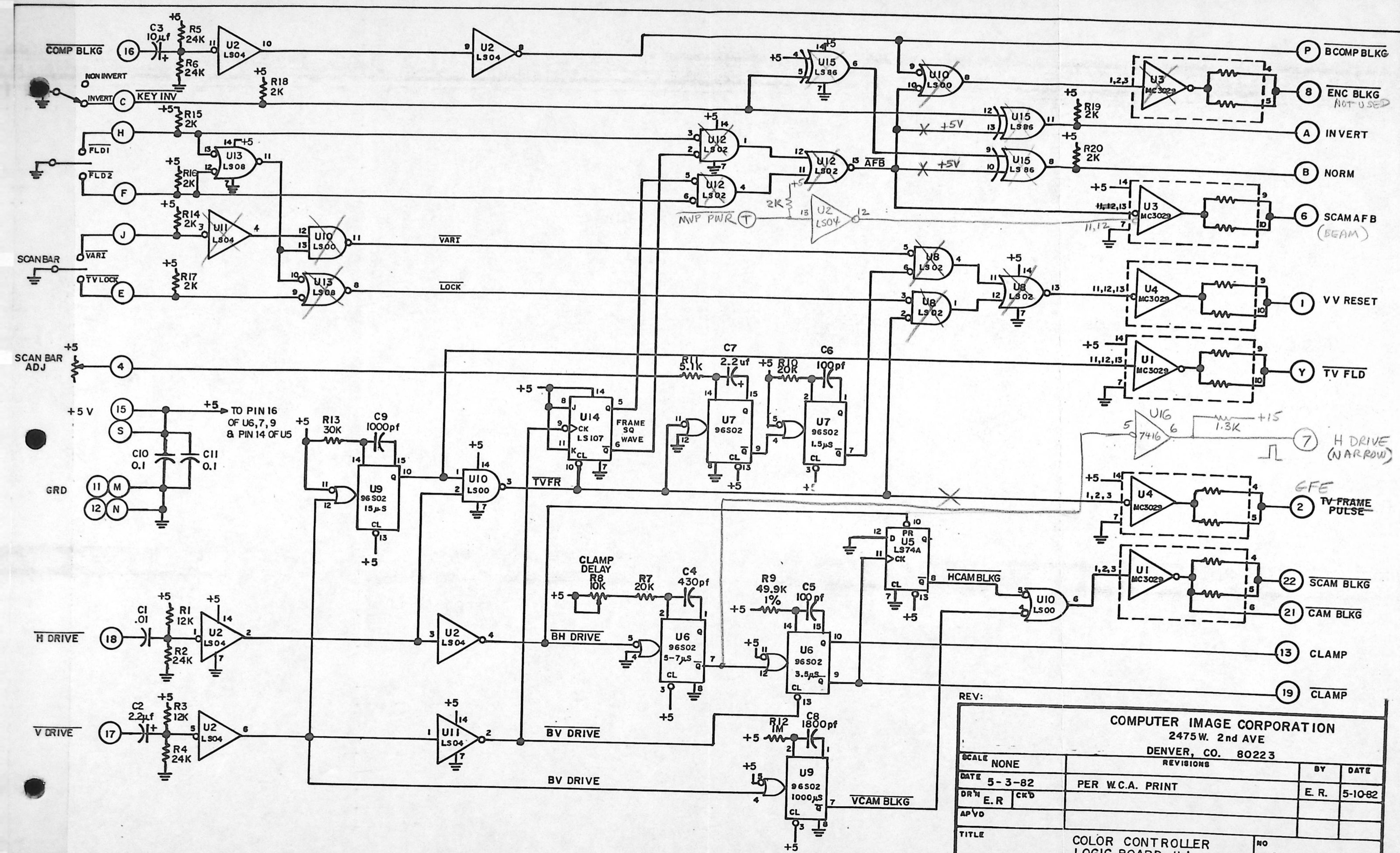


02 U8 D
04 U11 E

CIC-W-17
NI CC J2
026-4

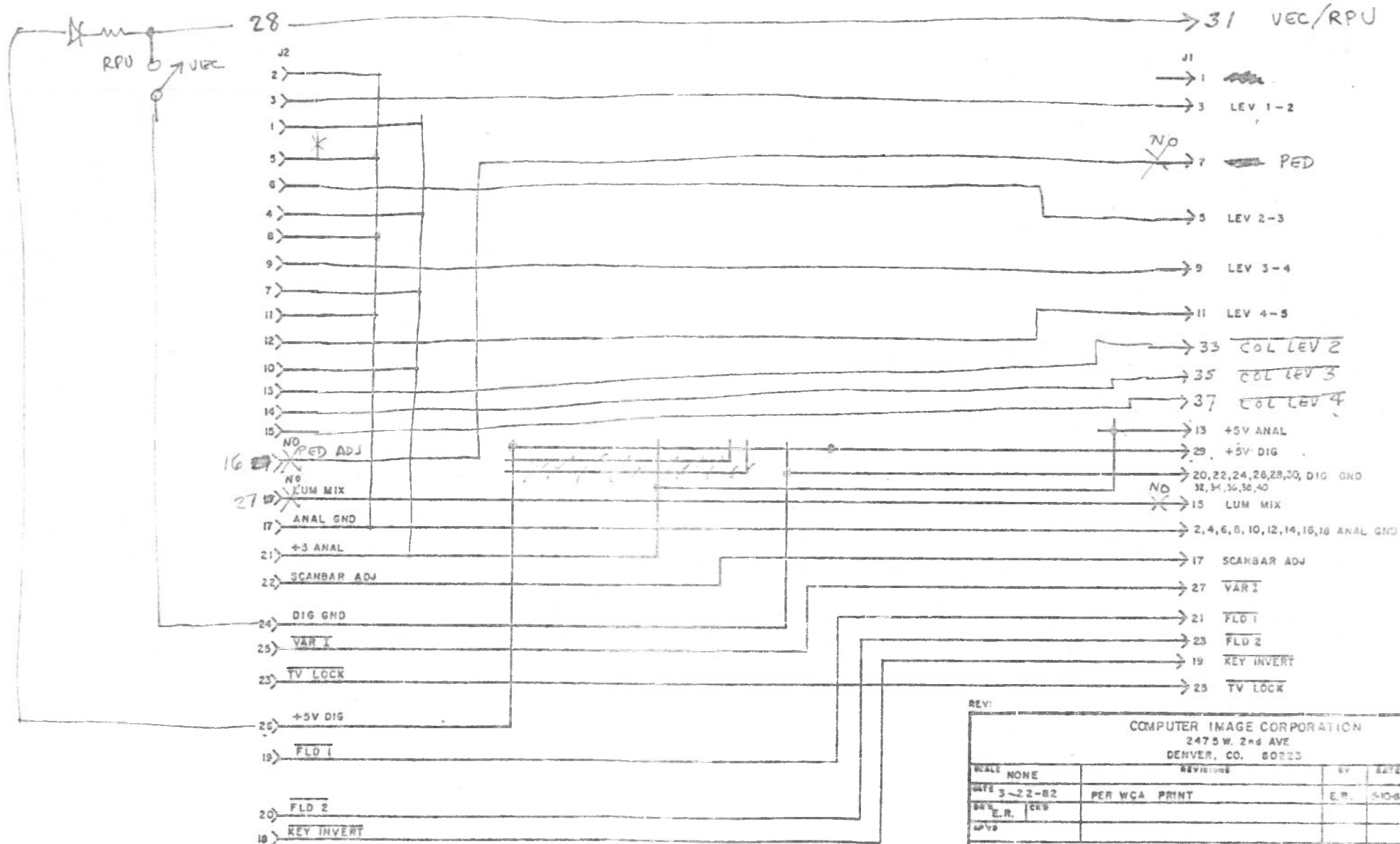


CC J2
NI CIC-W-17
026-4



REV:			
COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE NONE	REVISIONS		BY DATE
DATE 5-3-82	PER W.C.A. PRINT		E. R. 5-10-82
DRN E. R	CKD		
APVD			
TITLE		NO	
COLOR CONTROLLER		CIC-W-17	
LOGIC BOARD #1			

Unused 1, 39

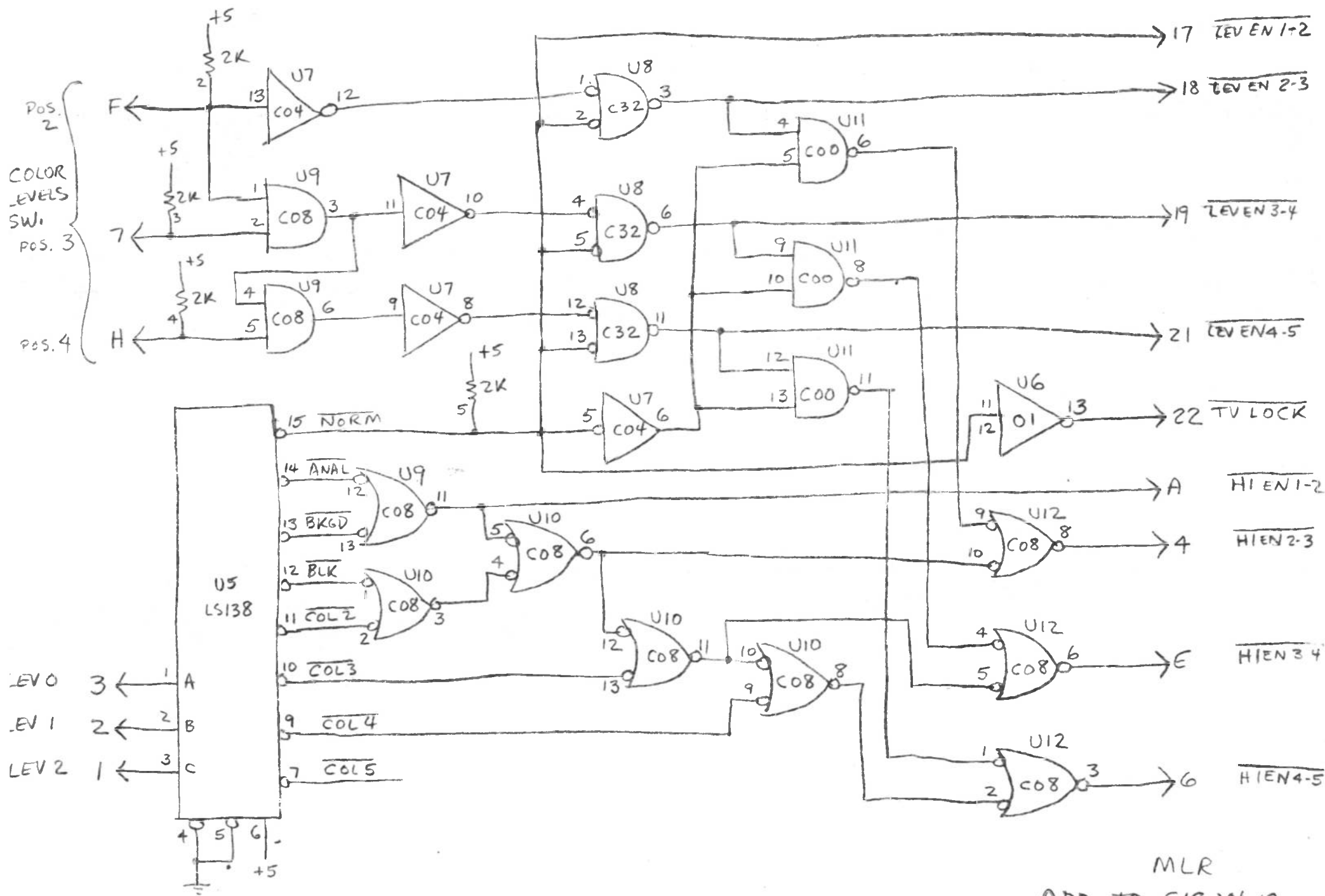


REV: _____

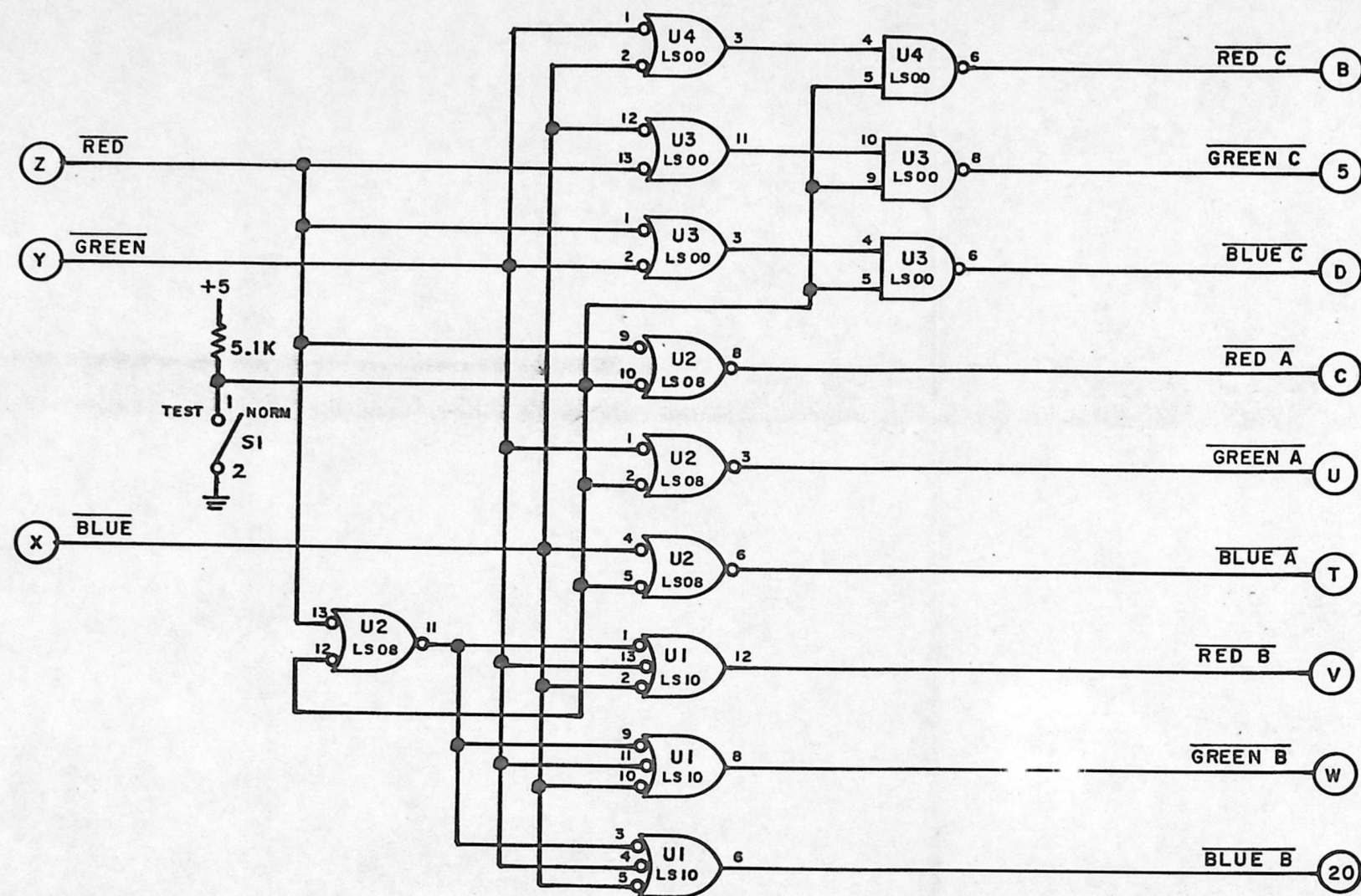
COMPUTER IMAGE CORPORATION
2475 W. 2nd AVE
DENVER, CO. 80223

SCALE NONE	REVISE	BY	DATE
DATE 3-22-82	PER WCA PRINT	E.R.	5-10-82
BY E.R.	REV		
APVS			
TITLE		NO	
COLOR CONTROLLER CONTROL PANEL ELECT		CIC-V-18	

026-4

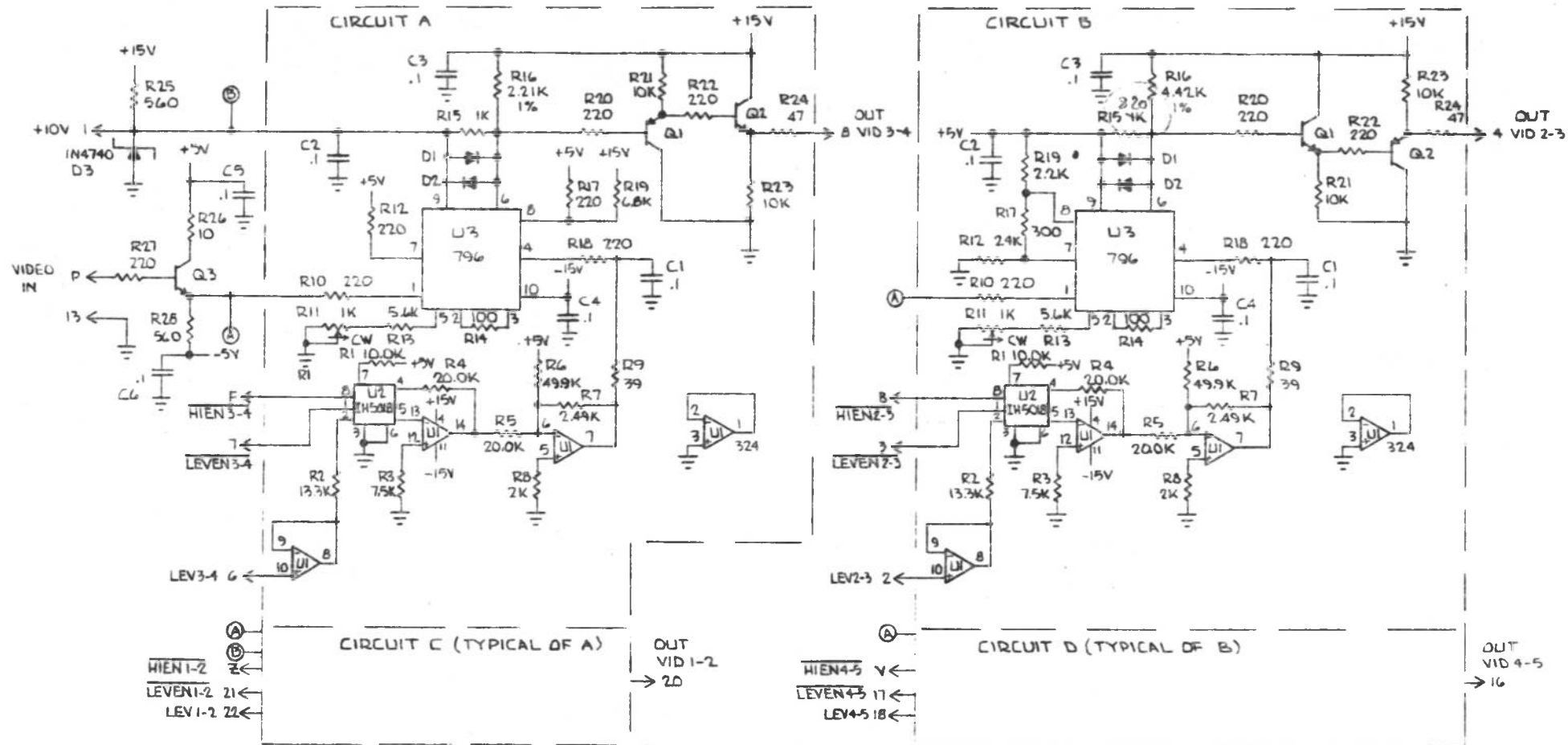


MLR
ADD TO CIC-W-19
CC-J3



REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 3-17-82	PER WCA MARKED PRINT	E.R.	4-27-82
DRN E.R.	CKD		
APVD			
TITLE COLOR CONTROLLER LOGIC 2		NO CIC-W-19	



- NOTE:**
1. ALL RESISTORS ARE 1/4 W, 5% UNLESS OTHERWISE NOTED
 2. ALL CAPACITOR VALUES ARE IN MFDs
 3. ALL DIODES ARE IN 3064 UNLESS NOTED
 4. NPN TRANSISTORS ARE 2N3904
 5. PNP TRANSISTORS ARE 2N3906

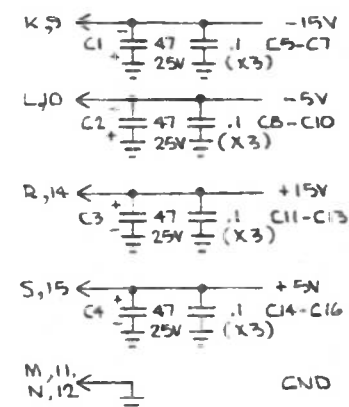
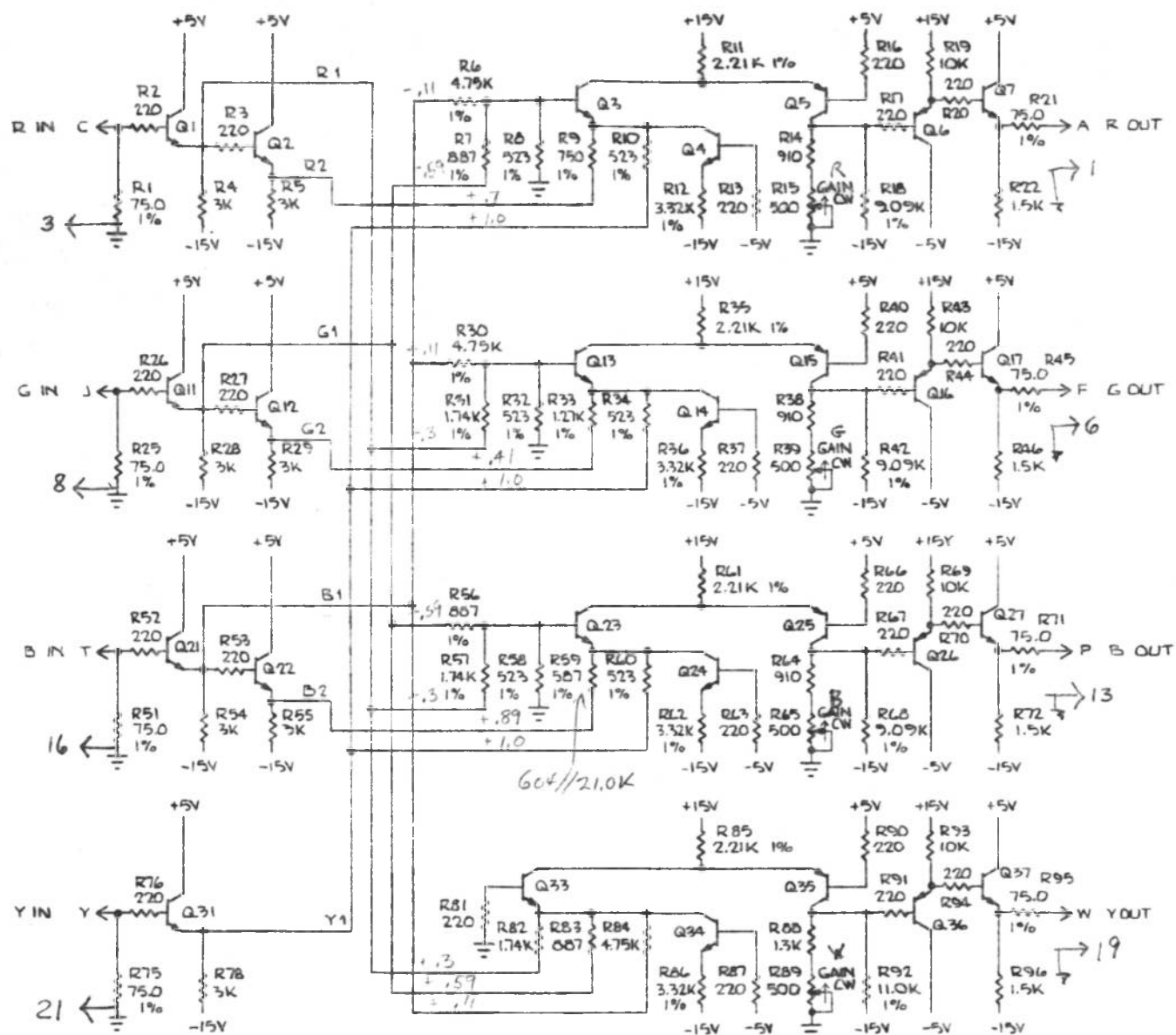
**COMPUTER
IMAGE
CORPORATION**

2475 W. 2nd AVE.
DENVER, COLORADO 80223

SCALE	REVISIONS	BY	DATE
DATE 10/22/85			
DRN EVL			
OP VS			
TITLE INPUT PROCESSORS			SH 1 OF 1
			CIC-W-86

FRAC	TOL
.XX	± 1/32
.XX	± .015
.XX	± .005

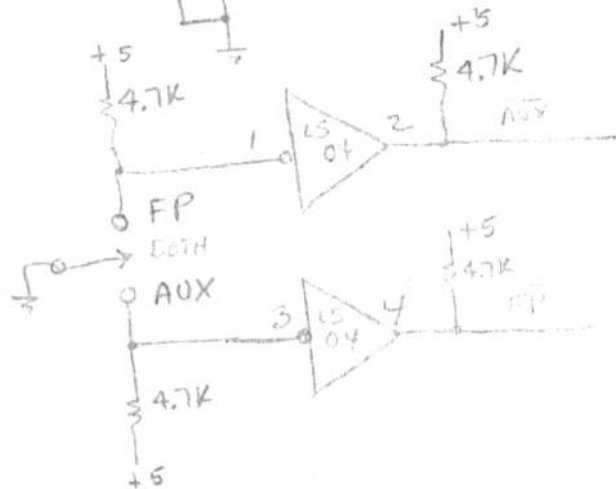
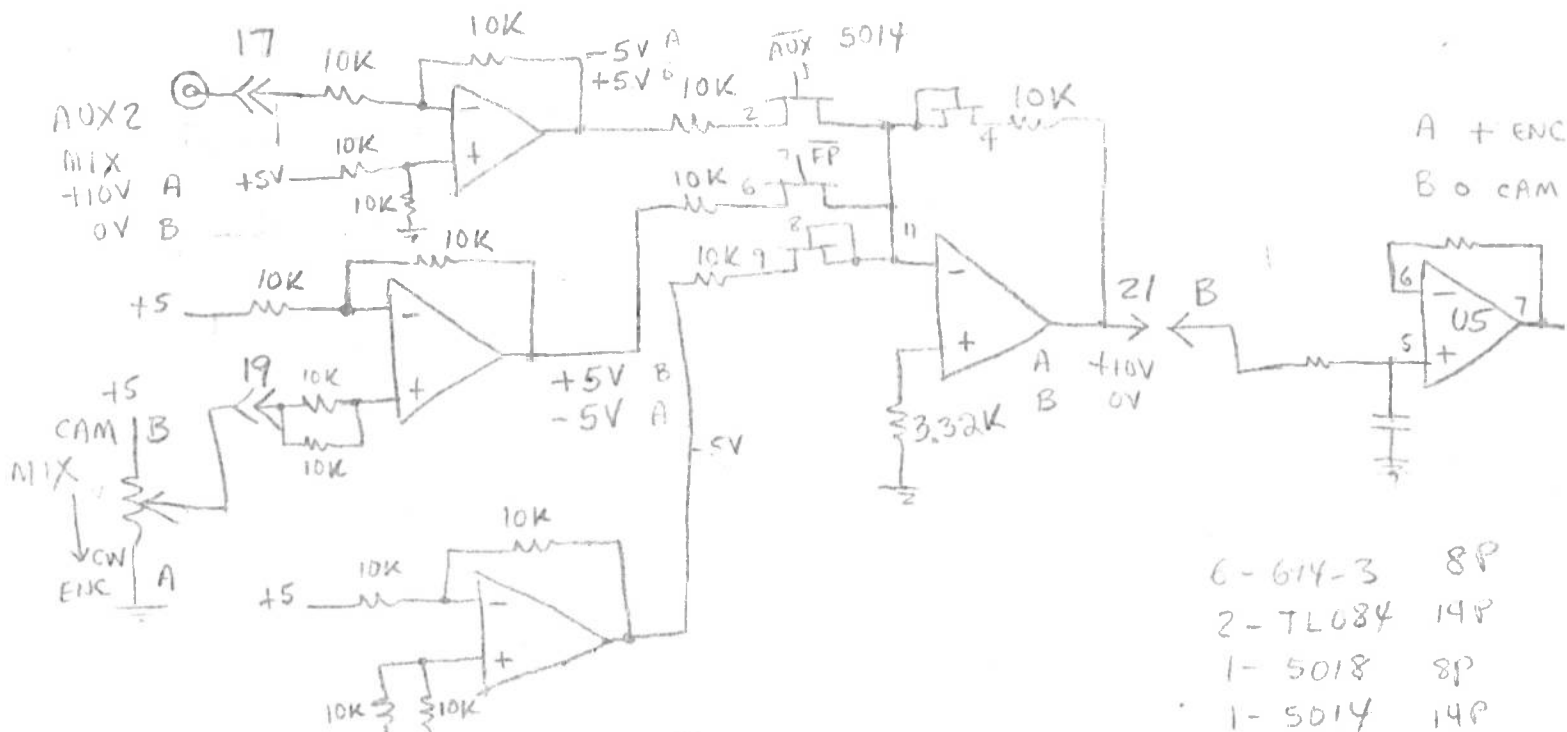
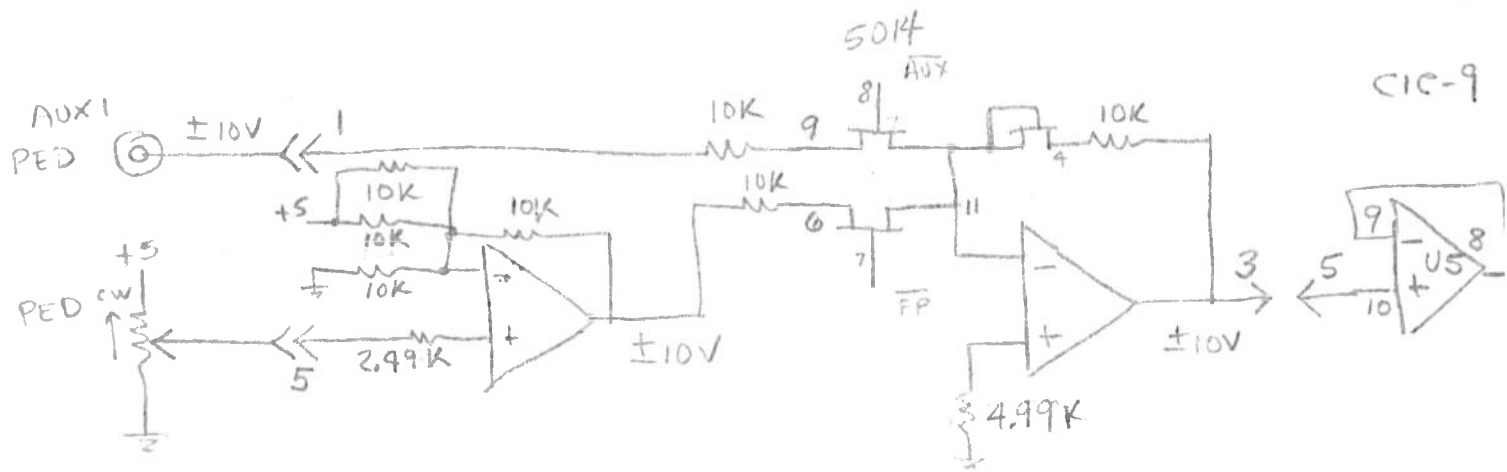
WCA



- NOTES:
1. ALL RESISTORS ARE 1/4 W 5% UNLESS OTHERWISE NOTED
 2. ALL CAPACITOR VALUES ARE IN MFDS
 3. NPN TRANSISTORS ARE 2N3904
 4. PNP TRANSISTORS ARE 2N3906

RGB gain .72 to 1.08
Y gain 1 to 1.3

		2475 W 2nd AVE DENVER, COLORADO 80223	
SCALE		REVISIONS	
DATE	10/1/85		
DESIGNER	EXL	CAD	
APP'D			
TITLE		SH 1 OF 1	
COLOR LUMINANCE MATRIX		CIC-W-87	



C-614-3 8P
 2-TL084 14P
 1-5018 8P
 1-5014 14P

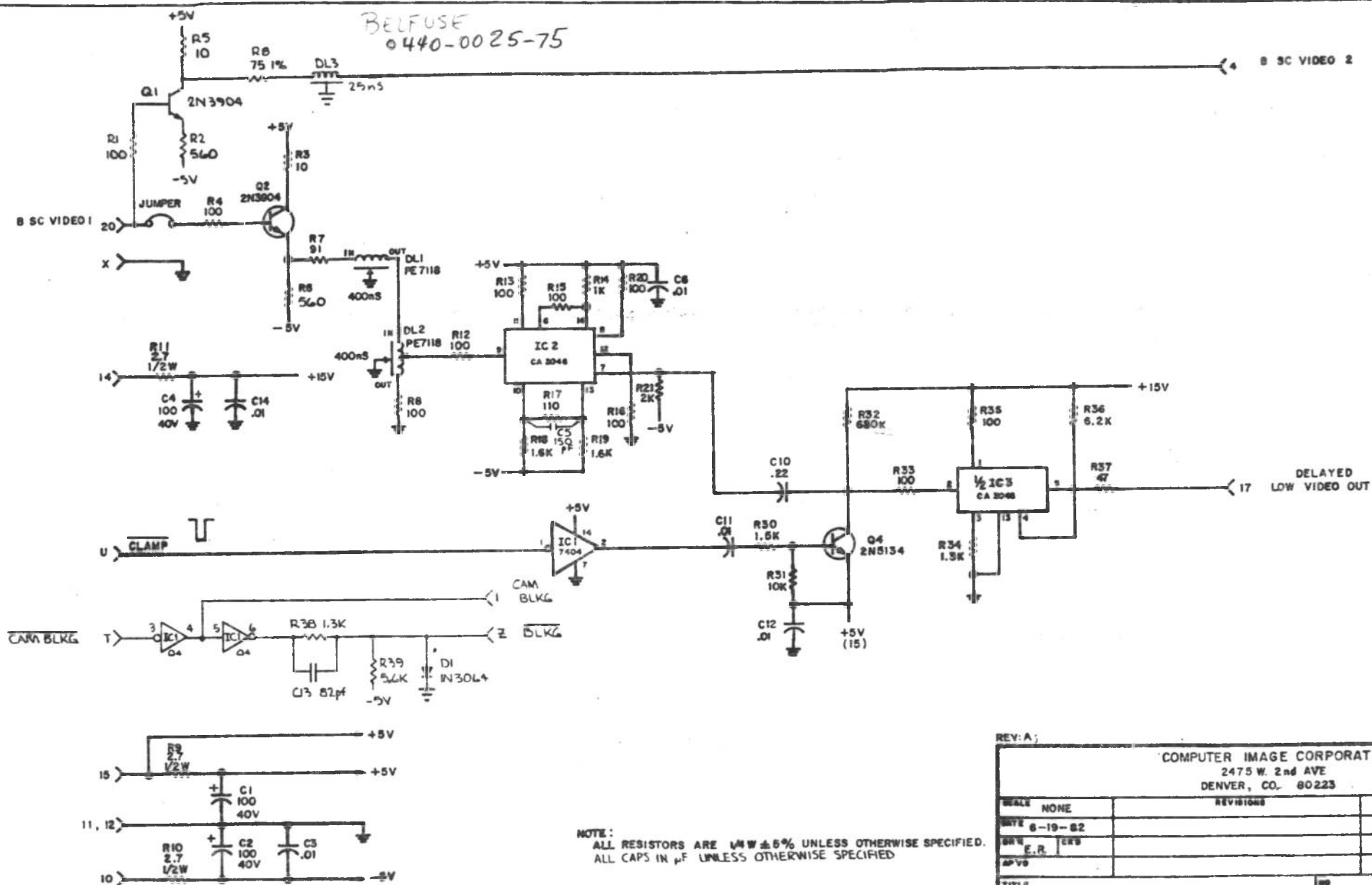
SPS V
 PCLK CONT
 J1
 PED/MIX MUX

SCHEMATIC

WIRE LIST

[illegible]

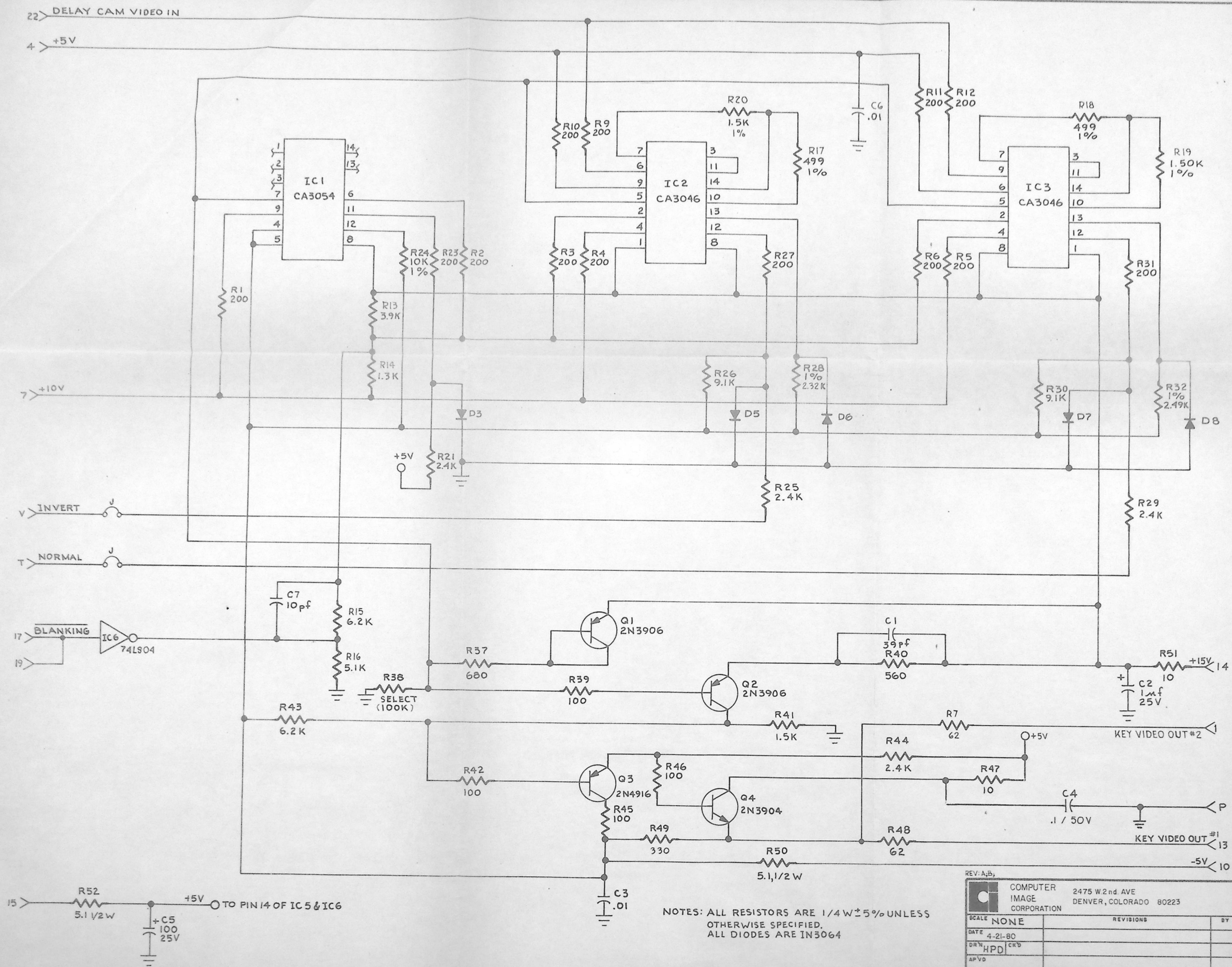
TOLERANCES UNLESS OTHERWISE SPECIFIED		COMPUTER IMAGE	
FRACTIONS	DEC ANGLES		
±	±	DELAY AMP CONTROL SHEET	
±	±		
APPROVALS	DATE	SCALE	
DRAWN			
CHECKED		SIZE	DRAWING NO.
		B	CVS-134-3
		DO NOT SCALE DRAWING	
		SHEET Ø - 1	



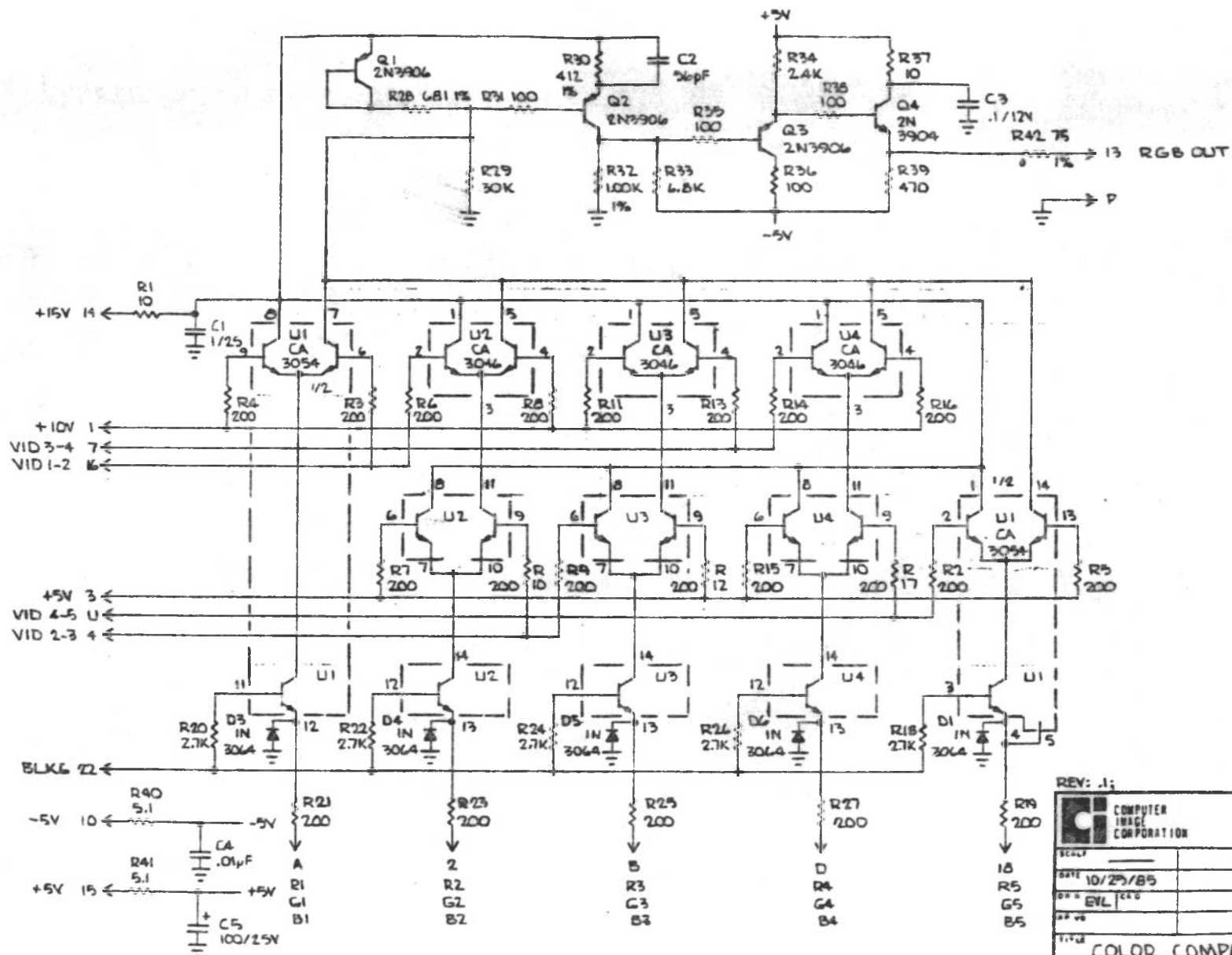
REV: A;

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223				
SCALE	NONE	REVISIONS	BY	DATE
DATE	6-19-82			
DRW	E.R.			
APV				
TITLE			REV	
DELAY AMP			CVS-134-3 SH 1 OF 1	

J5
072, 026-4



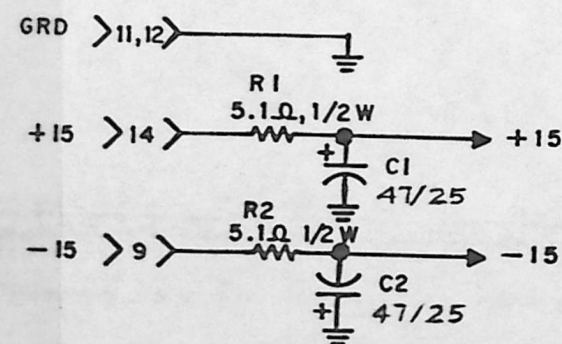
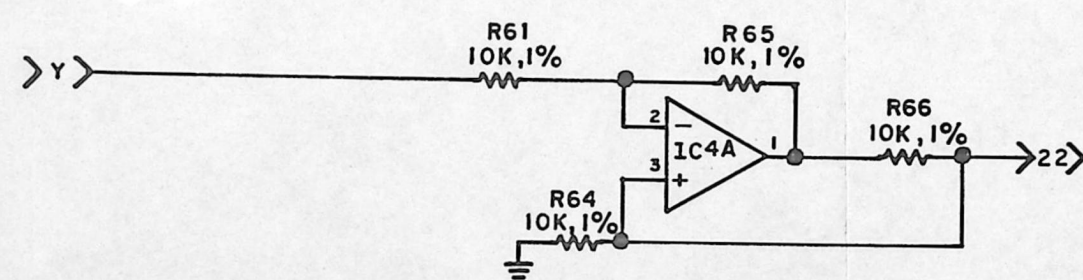
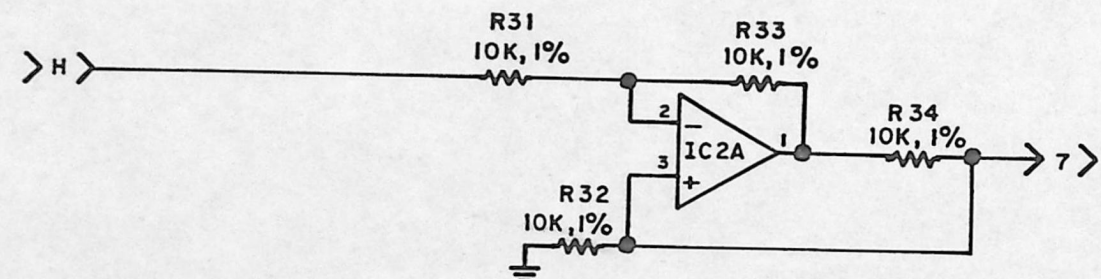
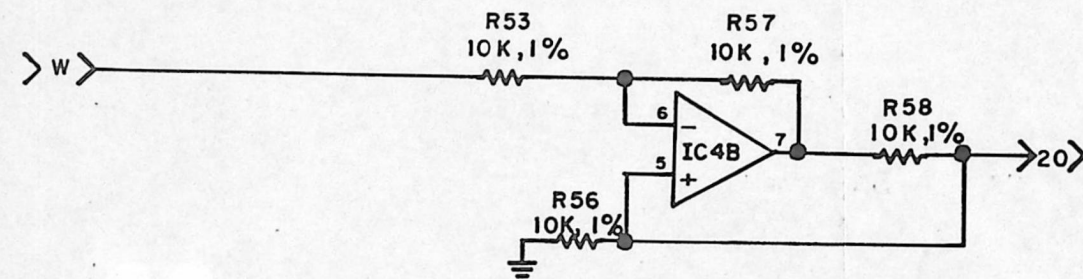
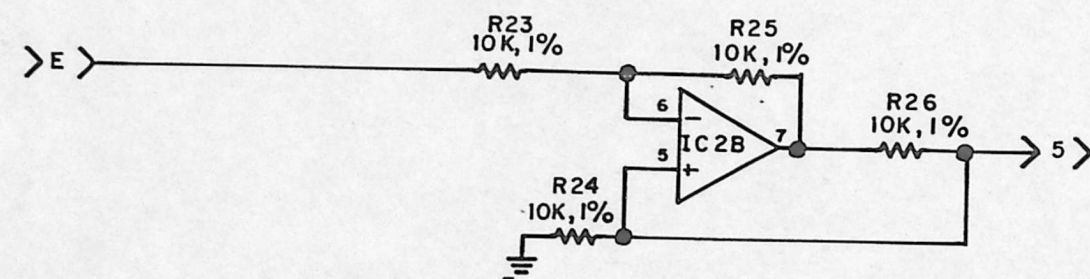
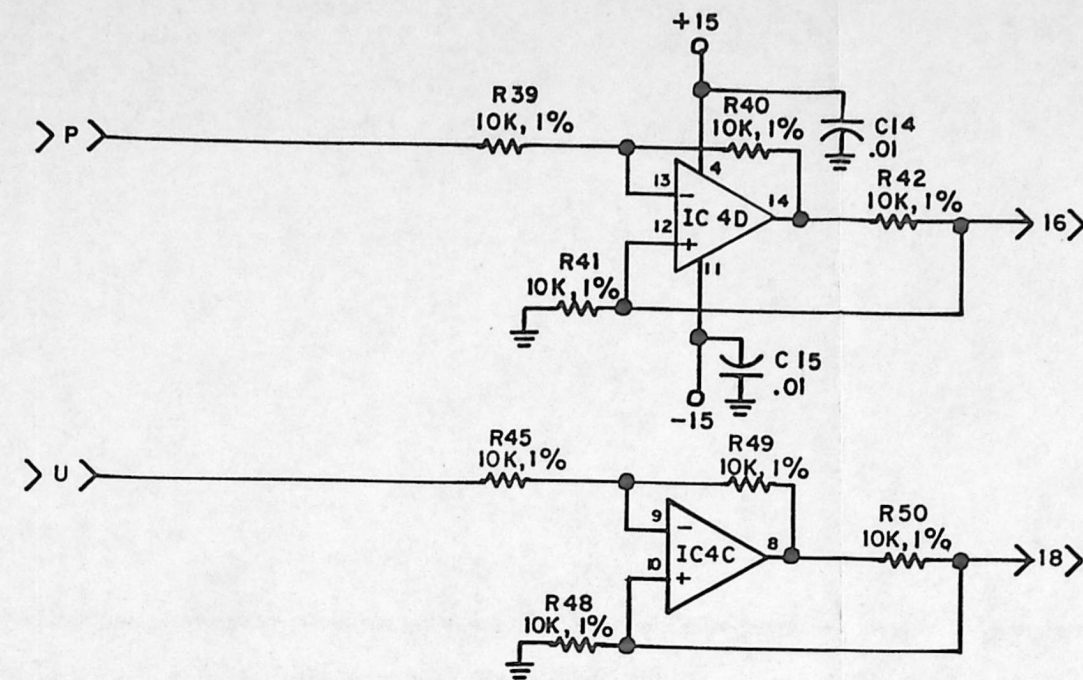
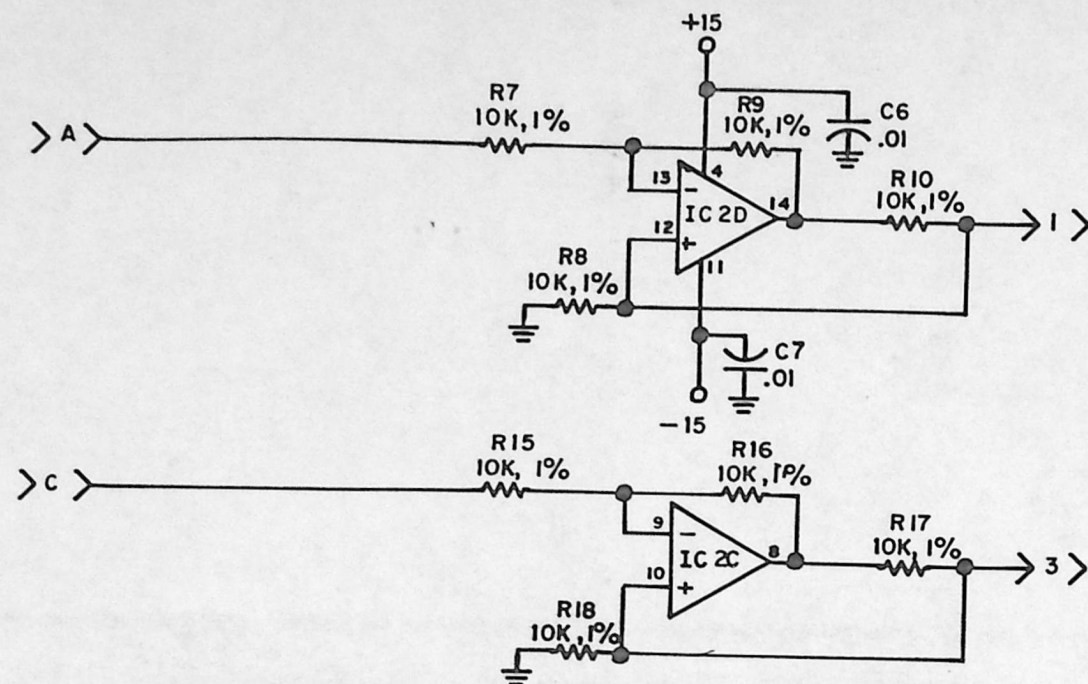
		COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE. DENVER, COLORADO 80223	
SCALE	NONE	REVISIONS	BY DATE
DATE	4-21-80		
DRN	HPD	CRD	
APVD			
TITLE	BACKGROUND COMPARATORS		NO CVS-135-1
			SH 1 OF 1



REV: J1;

		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE		REVISIONS	
DATE	10/23/85		
DRN	EVL		
APP			
TITLE COLOR COMPARATOR		SH 1 OF 1 CVS-136-2	

NCA



UNLESS OTHERWISE SPECIFIED
ALL RESISTORS ARE 1/4 WATT, $\pm 5\%$
ALL CAPACITORS ARE IN μf
ALL OP AMPS ARE TL084CN

REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE. DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 6-29-81			
DRN E.R.	CKD		
APVD			
TITLE COLOR MIXER SYSTEM IV		NO CVS-138-2 SH 1 OF 1	

COLOR CONTROLLER WIRING LIST

V V RESET	J2-1, J44	}	CX
V V RESET G	J2-11, J44-G		
TV FRM	J2-2, J43	}	CX
TV FRM G	J2-12, J43-G		
INVERT	J2-A, J4-V		
NORM	J2-B, J4-T		
KEY INVERT	J2-C, J19-19		
TV LOCK	J2-E, J19-25		
SCNBAR ADJ	J2-4, J19-17		
VARI	J2-J, J19-27		
FLD1	J19-21, J20-37, J2-H		
FLD2	J19-23, J20-39, J2-F		
H DRIVE	J2-18, J29, J35	}	CX
H DRIVE G	J2-M, J29-G		

COLOR CONT.

V DRIVE

J2-17, J30, J36

V DRIVE G

J2-N, J30-G

} CX

COMPBLKG

J2-16, J31, J37

} CX

COMPBLKG G

J2-11, J31-G

CLAMP

J2-13, J6-1, J7-J, J8-J, J9-J

CLAMP

J2-19, J5-U, J6-A, J7-H, J8-H, J9-H, J10-U

CAMBLKG

J2-21, ~~J4-17~~, J10-T

SCAMBLKG

J2-22, J42

} CX

SCAMBLKG G

J2-12, J42-G

SCAMAFB

J2-6, J39

} CX

SCAMAFB G

J2-M, J39-G

ENCBKLG

J2-8, J41

} CX

ENCBKLG G

J2-N, J41-G

BCOMPBLKG

J2-P, J4-17
~~J2-P, J10-T~~

<u>RED</u>	J3-Z, J20-31
<u>GREEN</u>	J3-Y, J20-33
<u>BLUE</u>	J3-X, J20-35
<u>REDA</u>	J3-C, J7-Y
<u>REDB</u>	J3-V, J7-W
<u>REDC</u>	J3-B, J7-T
<u>GREEN A</u>	J3-U, J8-Y
<u>GREEN B</u>	J3-W, J8-W
<u>GREEN C</u>	J3-5, J8-T
<u>BLUE A</u>	J3-T, J9-Y
<u>BLUE B</u>	J3-20, J9-W
<u>BLUE C</u>	J3-D, J9-T
DLY CAM VID	J4-22, J5-17
+5V	J4-4, J4-15, J19-13, J19-29

+10V	J19-1, J4-7, J10-13	
KEY 1	J4-13, J40	} CX
KEY 1 G	J4-P, J40-G	
KEY 2	J4-1, J46	} CX
KEY 2 G	J4-P, J46-G	
B SC VID 1	J5-20, J7-Z	} TP
B SC VID 1 G	J5-X, J7-12	
B SC VID 2	J6-4, J5-4	} TP
B SC VID 2 G	J6-6, J5-11	
LUM IN	J6-17, J32	} CX
LUM IN G	J6-19, J32-G	
LUM MIX	J6-B, J19-15	
LUM OUT	J6-21, J50	} CX
LUM OUT G	J6-Y, J50-G	

R VID J7-V, J11-13

R VID G J7-18, J11-P

J7P. J7-P, J7-13

ENCR VID J7-B, J49

EN R VID G J7-11, J49-G

} TP

} CX

G VID J8-V, J12-13

G VID G J8-18, J12-P

J8P J8-P, J8-13

ENC G VID J8-B, J48

ENC G VID G J8-11, J48-G

} TP

} CX

B VID J9-V, J13-13

B VID G J9-18, J13-P

} TP

J9P J9-P, J9-13

ENC B VID J9-B, J47

} CX

ENC B VID G J9-11, J47-G

B SC VID 3 J10-20, J9-Z

B SC VID 3G J10-X, J9-12

} TP

HIGH VID J10-6, J11-1, J12-1, J13-1

LOW VID J10-17, J11-3, J12-3, J13-3

BLKG J10-Z, J11-22, J12-22, J13-22

LEVEL 1-2 J19-3, J11-16, J12-16, J13-16

LEVEL 2-3 J19-5, J11-4, J12-4, J13-4

LEVEL 3-4	J19-9, J11-7, J12-7 J13-7
LEVEL 4-5	J19-11, J11-U, J12-U, J13-U
B RED 1	J11-A, J14-1
B RED 2 DAC	J11-2, J14-3
B RED 3 DAC	J11-B, J14-5
B RED 4 DAC	J11-D, J14-7
B RED 5 DAC	J11-18, J14-16
B GRN 1	J12-A, J14-18
B GRN 2 DAC	J12-2, J14-20
B GRN 3 DAC	J12-B, J14-22
B GRN 4 DAC	J12-D, J15-1
B GRN 5 DAC	J12-18, J15-3
B BLU 1	J13-A, J15-5
B BLU 2 DAC	J13-2, J15-7

BBLU 3 DAC J13-B, J15-16.

B BLU 4 DAC J13-D, J15-18

B BLU 5 DAC J13-18, J15-20

RED 1 J20-1, J14-A

RED 2 DAC J20-7, J14-C

RED 3 DAC J20-13, J14-E

RED 4 DAC J20-19, J14-H

RED 5 DAC J20-25, J14-P

GRN 1 J20-3, J14-U

GRN 2 DAC J20-9, J14-W

GRN 3 DAC J20-15, J14-Y

GRN 4 DAC J20-21, J15-A

GRN 5 DAC J20-27, J15-C

BLU 1 J20-5, J15-E

BLU 2 DAC J20-11, J15-H

BLU 3 DAC J20-17, J15-P

BLU 4 DAC J20-23, J15-U

BLU 5 DAC J20-29, J15-W

SC VID J28, J7-X, J8-X, J9-X

SC VID G J28-G, J7-20, J8-20, J9-20

J7E J7-E, J7-5

J8E J8-E, J8-5

J9E J9-E, J9-5

+15v J19-7

TV FLD J2-Y, J45

TV FLD G J3-12
~~J2-N~~, J45-G

} cx & tw. pr.

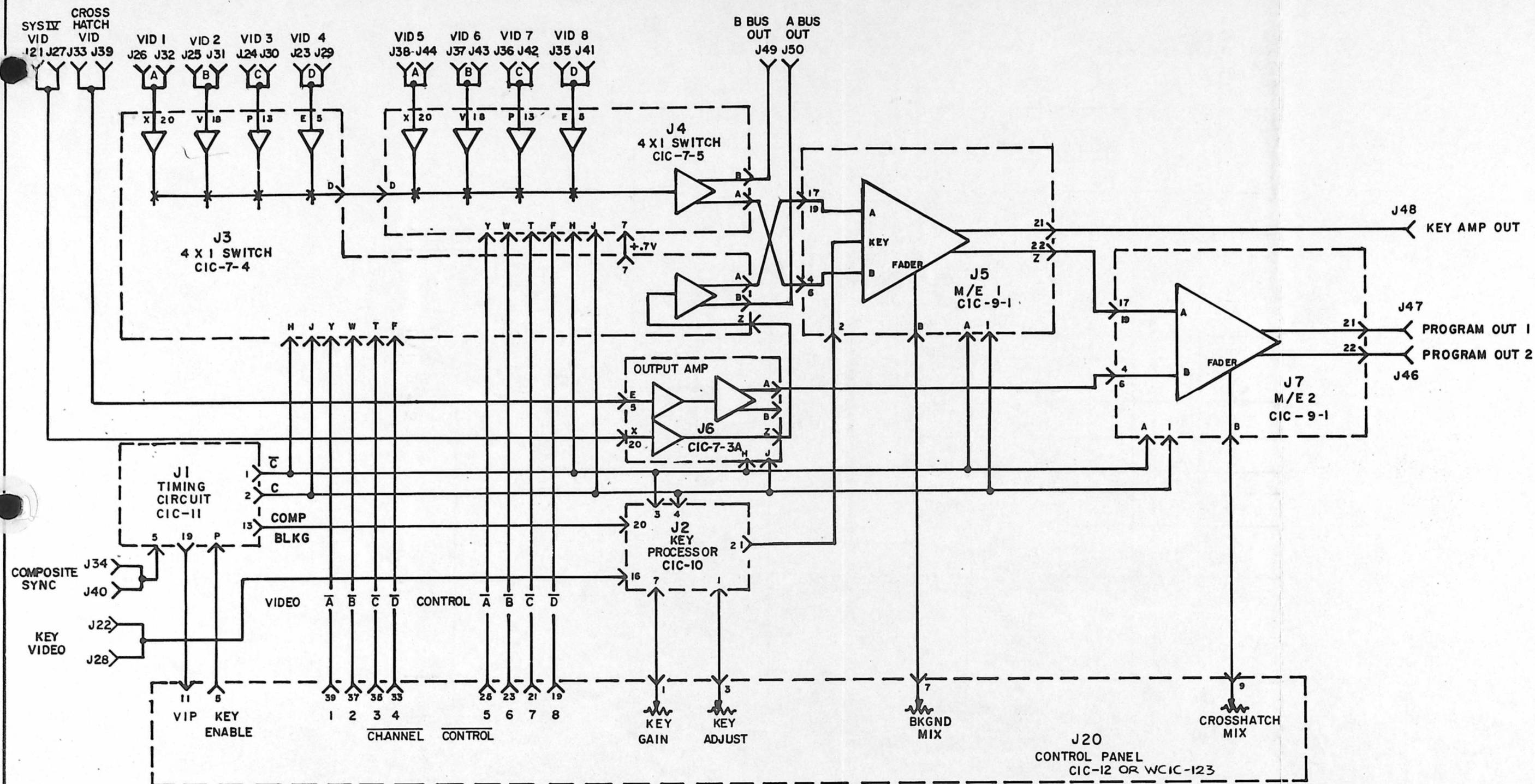
} CX

J19

+10V	J4-7	1	2	Analog Gnd
LEVEL 1-2	J11-16	3	4	
LEVEL 2-3	J11-4	5	6	
+15V		7	8	
LEVEL 3-4	J11-7	9	10	
LEVEL 4-5	J11-U	11	12	
ANAL+5		13	14	
LUM MIX	J6-B	15	16	
SCANBAR ADJ	J2-4	17	18	
KEY INVERT	J2-G	19	20	
FLD 1	J2-H - J20-37	21	22	
FLD 2	J2-F - J20-39	23	24	
TV LOCK	J2-E	25	26	
VARI	J2-J	27	28	
DIG +5		29	30	
		31	32	
		33	34	
		35	36	
		37	38	
		39	40	Digital Gnd

2

RED 1	- J14-A -	1	2
GRN 1	— J14-U —	3	4
BLU 1	— J15-E —	5	6
RED 2	DAC-J14-C —	7	8
GRN 2	DAC — J14-W —	9	10
BLU 2	DAC — J15-H —	11	12
RED 3	DAC — J14-E —	13	14
GRN 3	DAC — J14-Y —	15	16
BLU 3	DAC — J15-P —	17	18
RED 4	DAC — J14-H —	19	20
GRN 4	DAC — J15-A —	21	22
BLU 4	DAC — J15-U —	23	24
RED 5	DAC — J14-P —	25	26
GRN 5	DAC — J15-C —	27	28
BLU 5	DAC — J15-W —	29	30
RED	— J3-Z —	31	32
GREEN	— J3-Y —	33	34
BLUE	— J3-X —	35	36
FLD 1	- J2-H - J19-21 -	37	38
FLD 2	- J2-F - J19-23 -	39	40



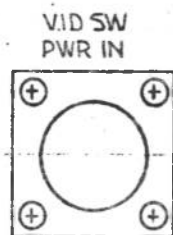
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SCALE NONE	REVISIONS		BY	DATE
DATE 9-21-81	PER WGA. PRINT		E.R.	3-11
DRN E.R. CRD	" " "		E.R.	5-10-82
APVD	ECN #34		EVL	10/11/82
TITLE	VIDEO SWITCHER BLOCK DIAGRAM			NO BCIC-4

CABLING VIEW

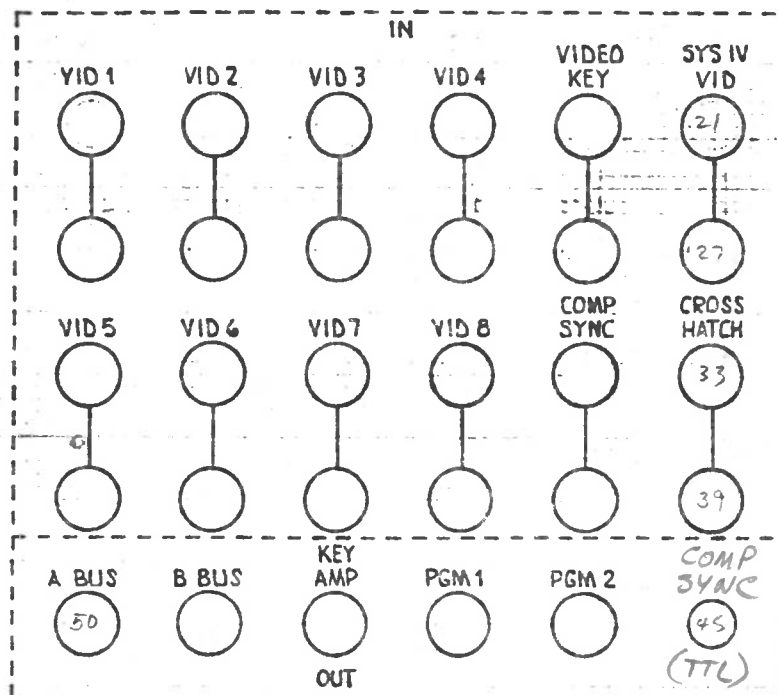
TOP

TOP

VIDEO SWITCHER



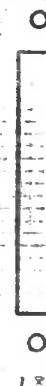
51



SW CONTR
PNL

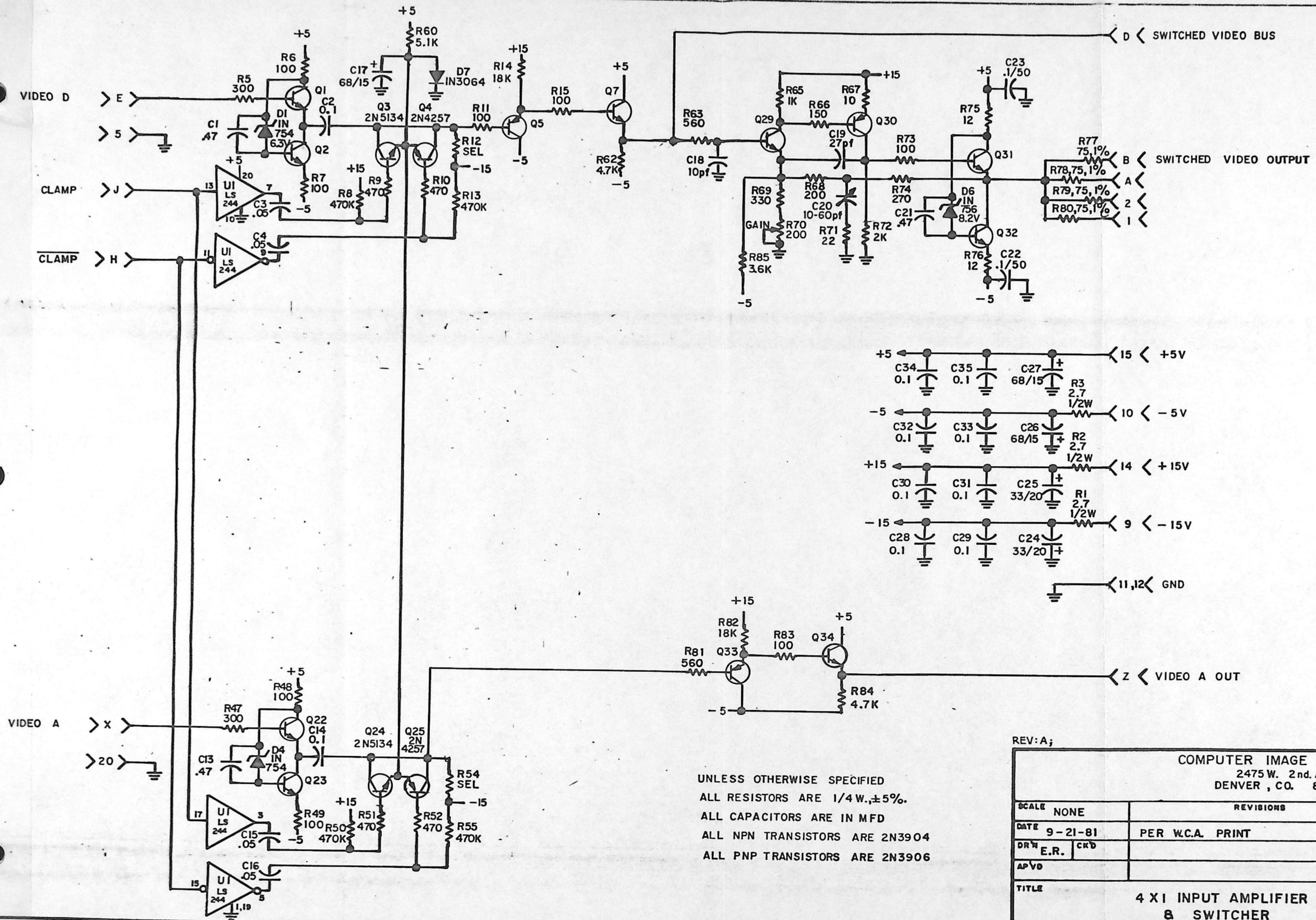


20



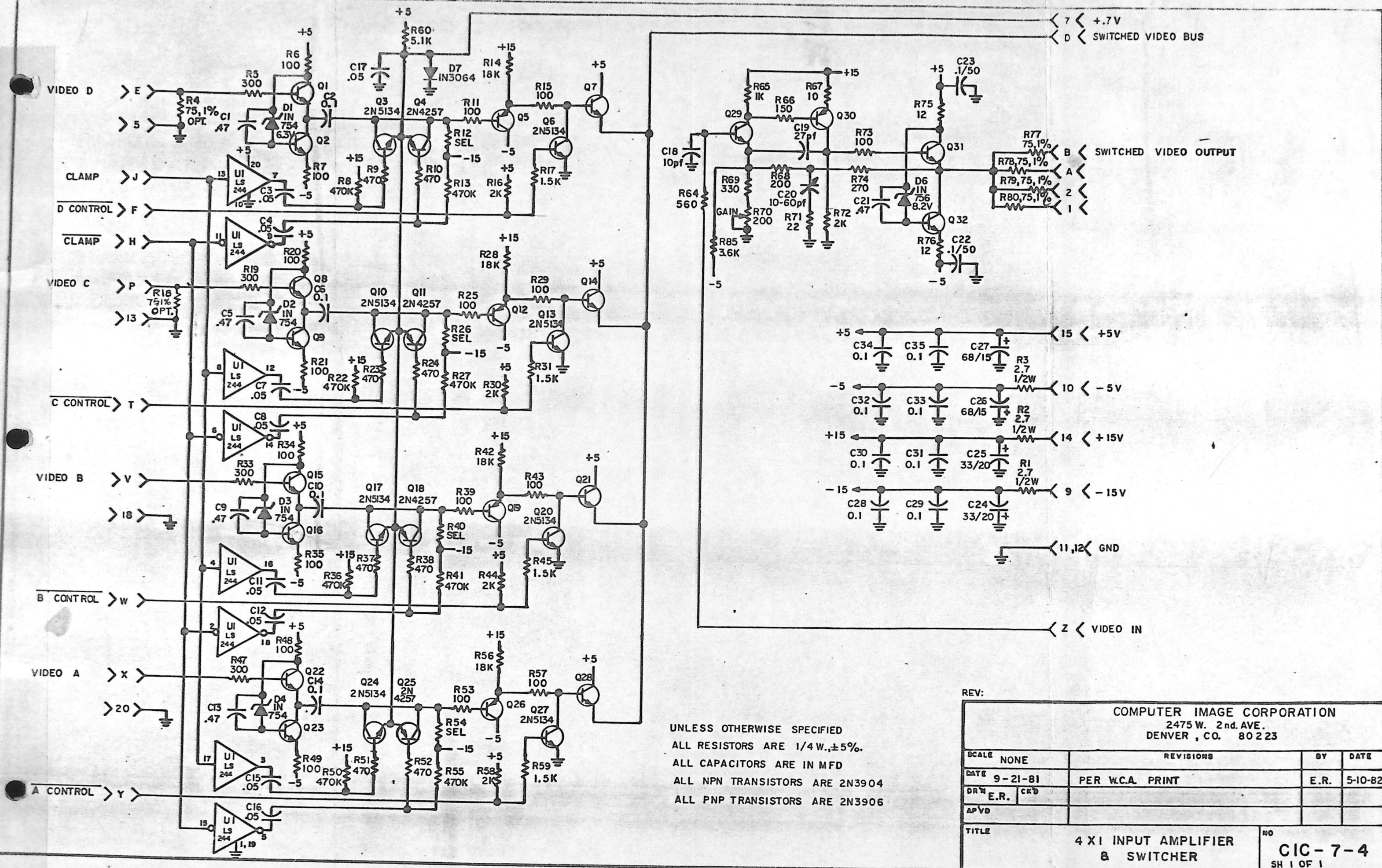
REV SCHEDULE
11/15/82 DRG EVL
A 10/1/82

COMPUTER IMAGE CORP.
VIDEO SWITCHER REAR PANEL
CONNECTOR FUNCTION
FC IC-10 SH 1 OF 2



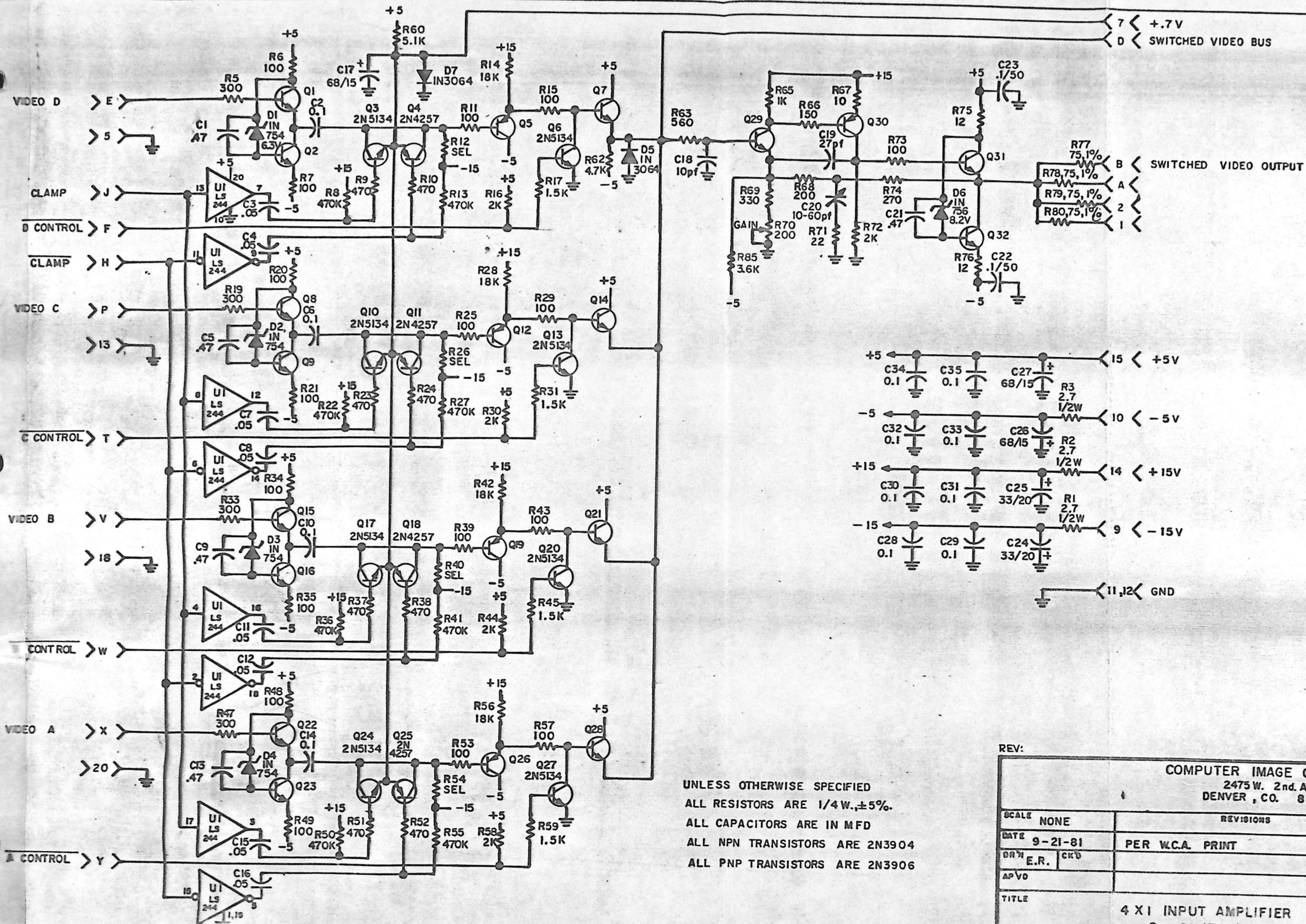
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COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE.			
DENVER, CO. 80223			
SCALE	REVISIONS	BY	DATE
NONE			
DATE 9-21-81	PER W.C.A. PRINT	E.R.	5-10-82
DRN E.R.	CRD		
APVD			
TITLE		NO	
4 X1 INPUT AMPLIFIER		CIC-7-3	
& SWITCHER		SH 1 OF 1	



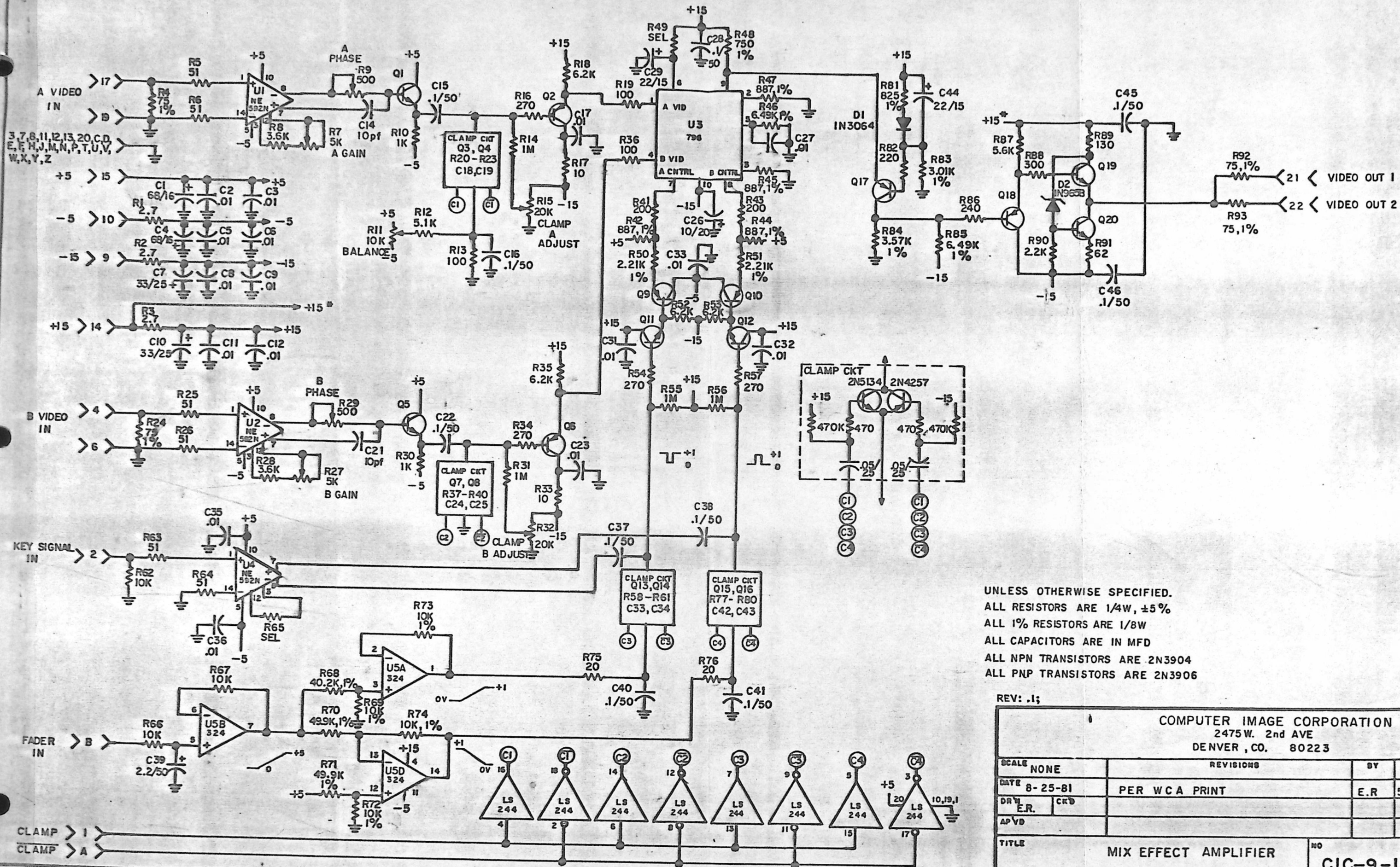
UNLESS OTHERWISE SPECIFIED
ALL RESISTORS ARE 1/4 W., ±5%.
ALL CAPACITORS ARE IN MFD
ALL NPN TRANSISTORS ARE 2N3904
ALL PNP TRANSISTORS ARE 2N3906

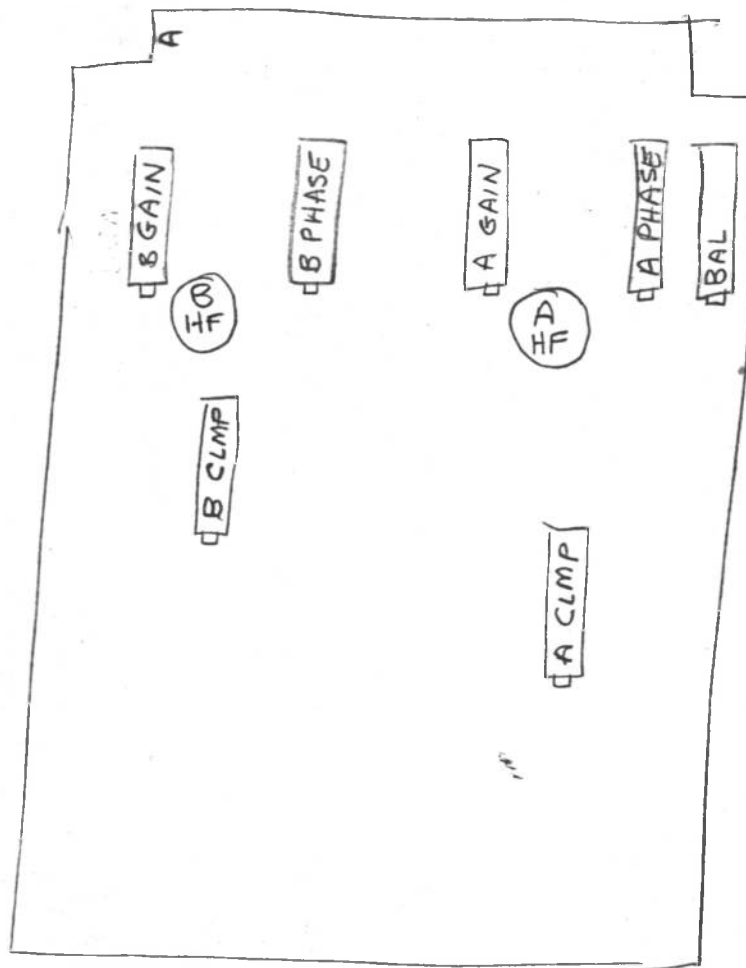
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SCALE	NONE	REVISIONS		BY
DATE	9-21-81	PER W.C.A. PRINT		E.R.
DRW	E.R.			5-10-82
APVD	CRD			
TITLE		4 X1 INPUT AMPLIFIER & SWITCHER		NO
		CIC-7-4		SH 1 OF 1



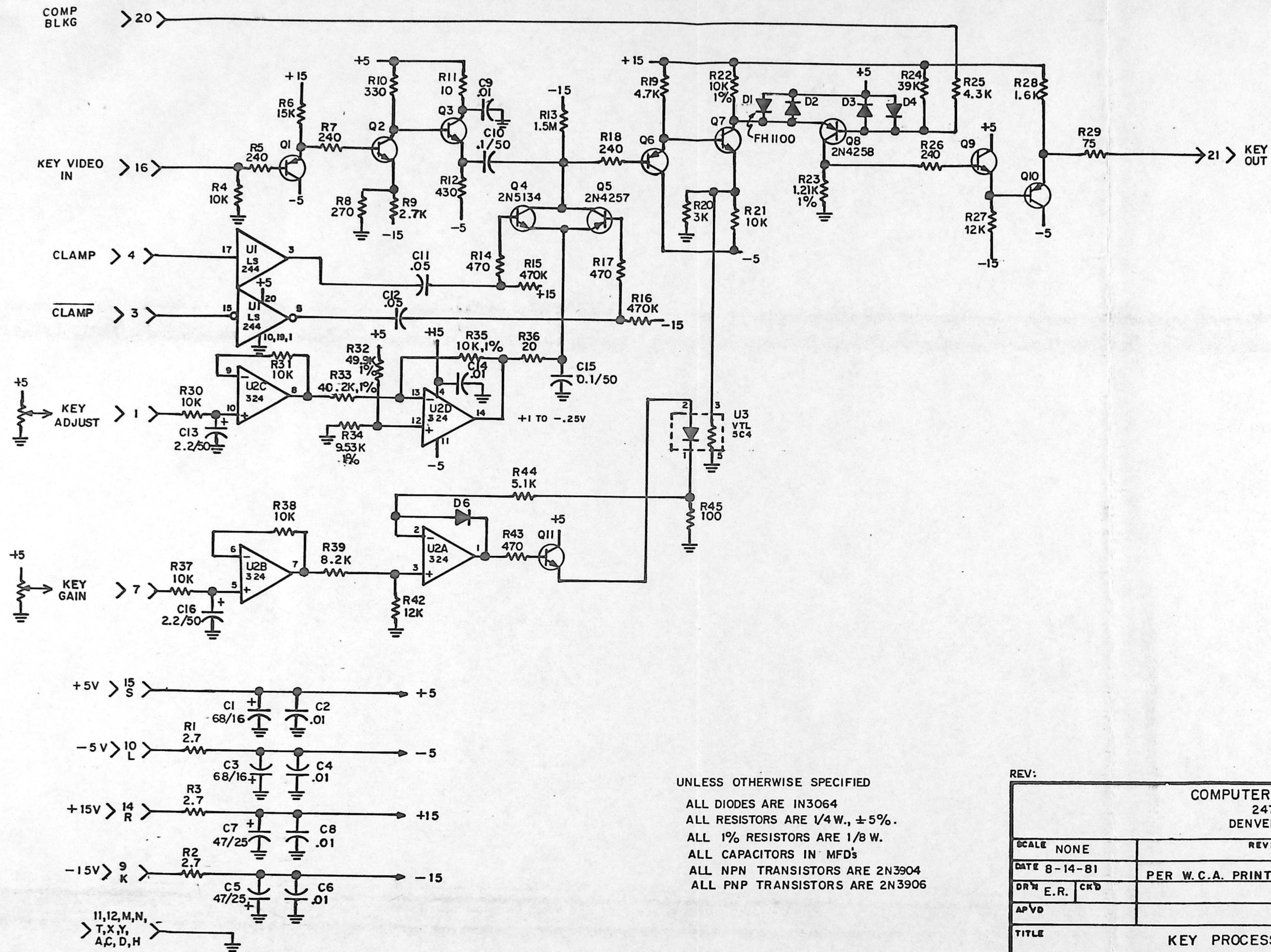
REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd. AVE. DENVER, CO. 80223			
SCALE NONE	REVISIONS		BY DATE
DATE 9-21-81	PER W.C.A. PRINT		E.R. 5-10-82
DRW E.R.	CRD		
APV 0			
TITLE 4 X 1 INPUT AMPLIFIER & SWITCHER		NO CIC-7-5 SH 1 OF 1	





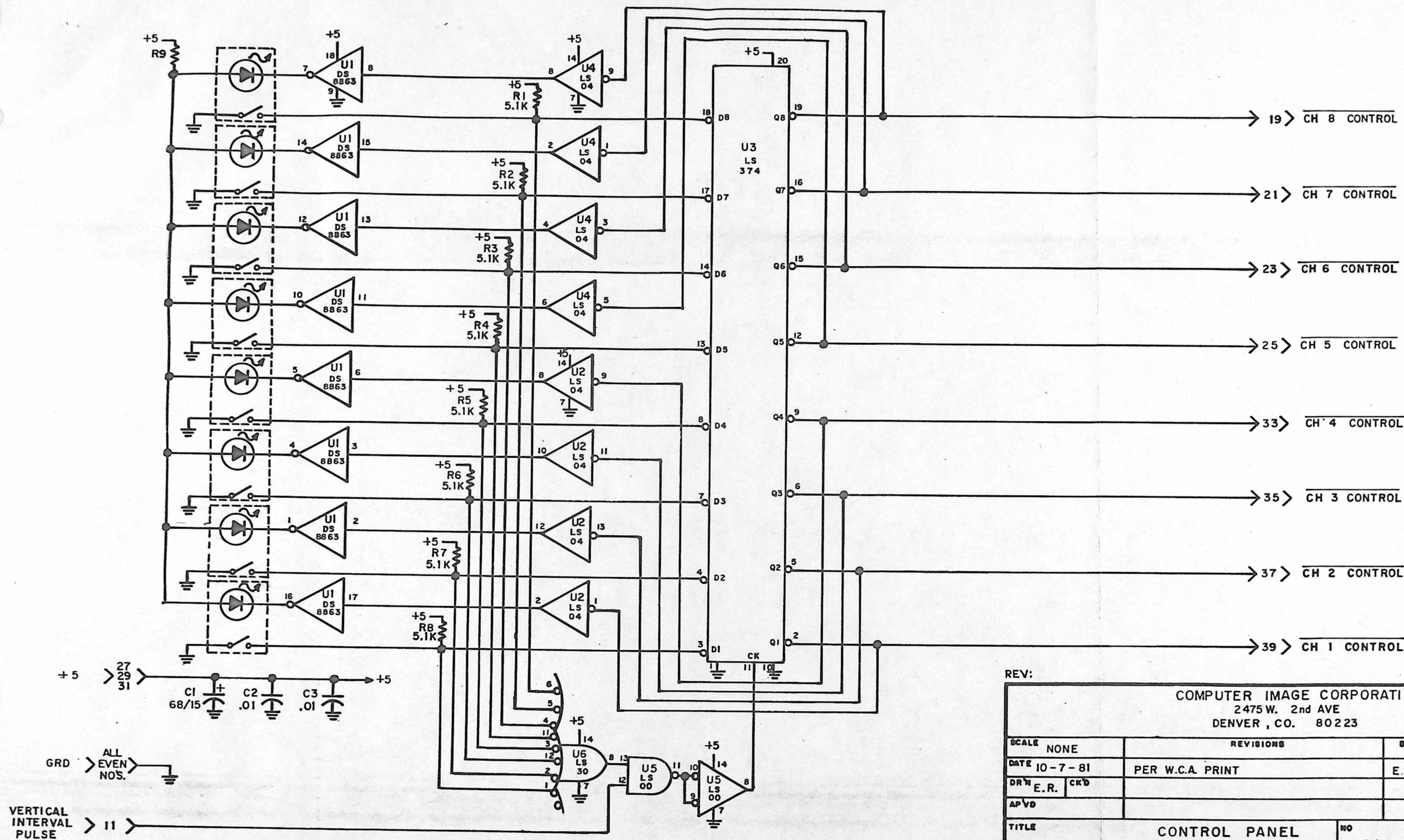
CIC-9



UNLESS OTHERWISE SPECIFIED
 ALL DIODES ARE 1N3064
 ALL RESISTORS ARE 1/4 W., $\pm 5\%$.
 ALL 1% RESISTORS ARE 1/8 W.
 ALL CAPACITORS IN MFD's
 ALL NPN TRANSISTORS ARE 2N3904
 ALL PNP TRANSISTORS ARE 2N3906

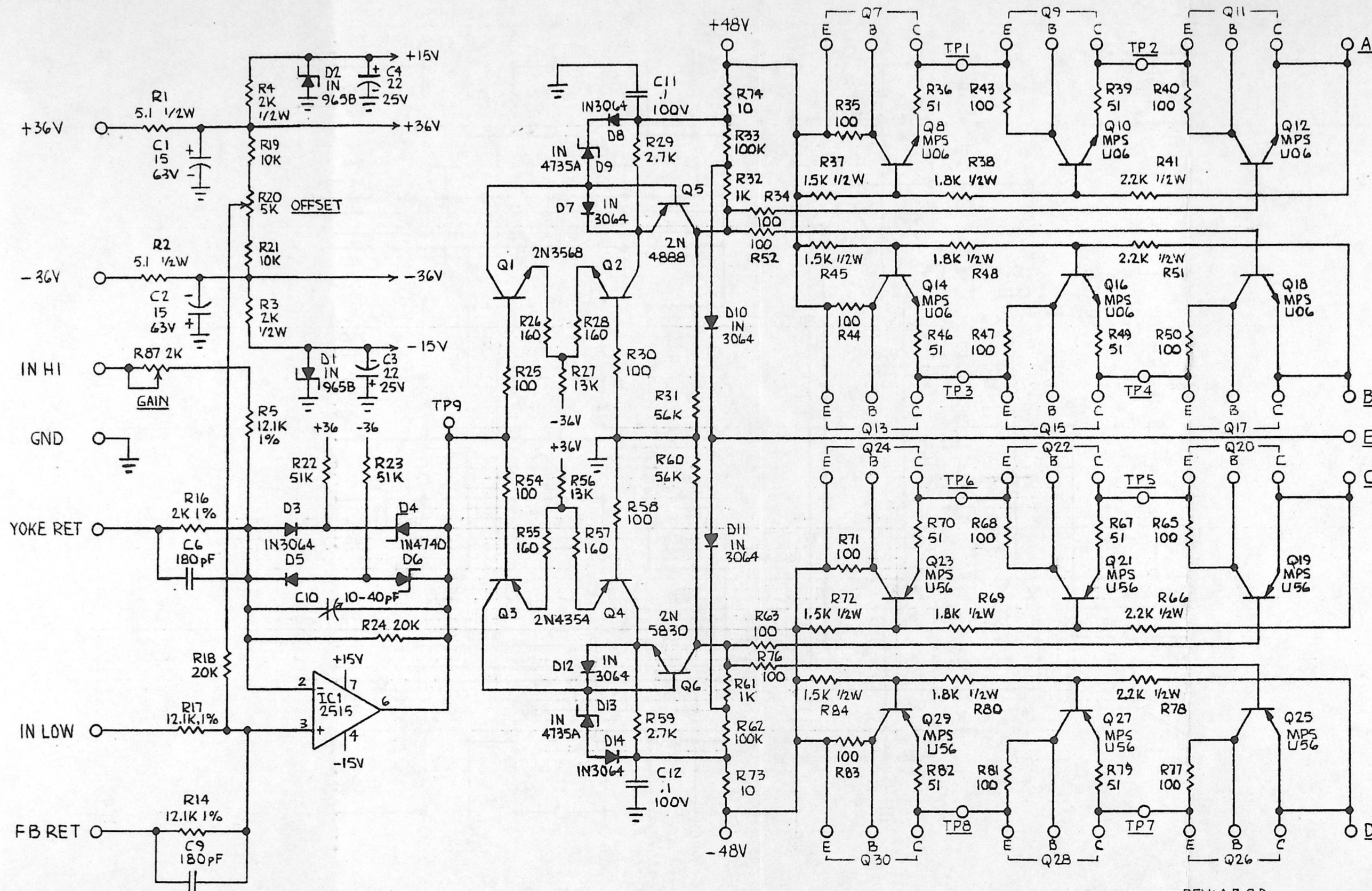
REV:

COMPUTER IMAGE CORPORATION			
2475W. 2nd AVE			
DENVER, CO. 80223			
SCALE NONE	REVISIONS		BY DATE
DATE 8-14-81	PER W.C.A. PRINT		E.R. 3-10
DRN E.R. CKD			
APVD			
TITLE	KEY PROCESSOR		NO SH 1 OF 1
			CIC-10

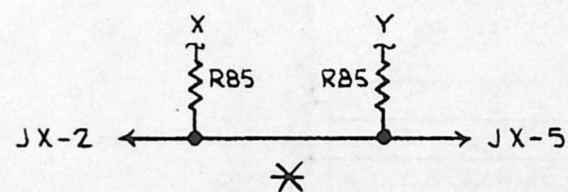
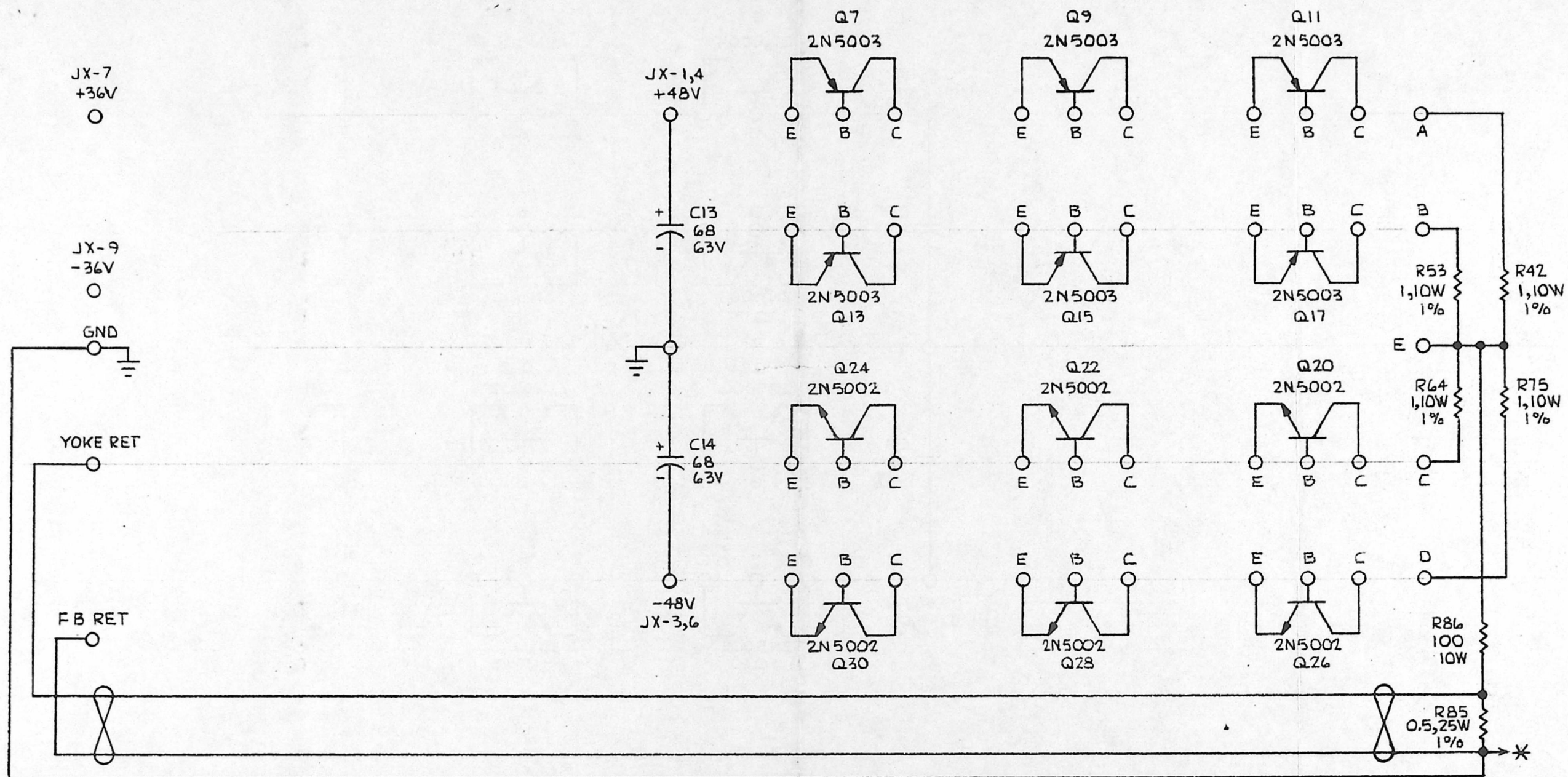


REV:

COMPUTER IMAGE CORPORATION 2475 W. 2nd AVE DENVER, CO. 80223			
SCALE NONE	REVISIONS	BY	DATE
DATE 10-7-81	PER W.C.A. PRINT	E.R.	3-10
DRW E.R. CK'D			
APVD			
TITLE	CONTROL PANEL VIDEO SWITCHER		NO CIC-12

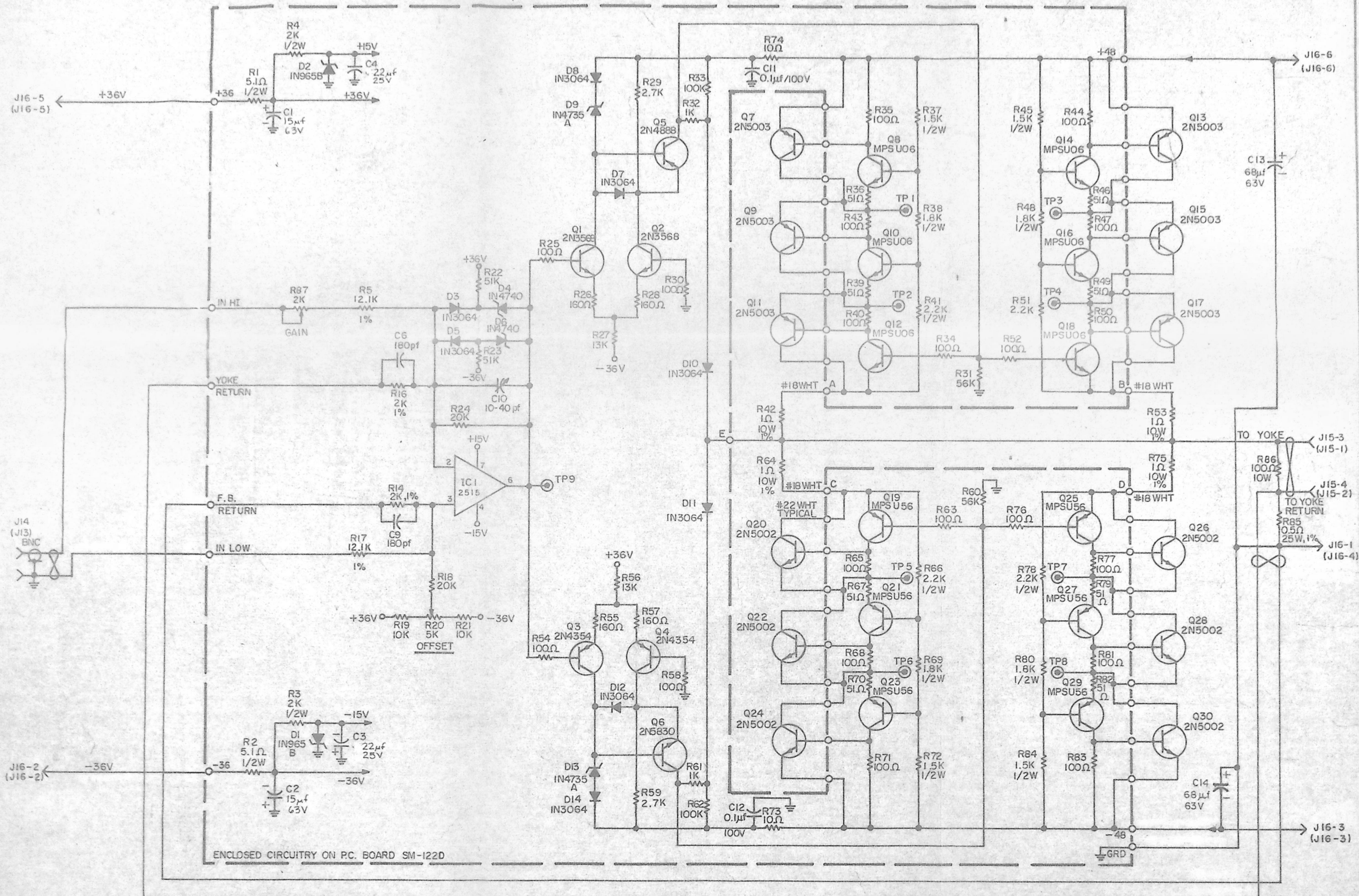


DWN: 8/17/83		REV: A;B;C;D;	
EVL		COMPUTER IMAGE CORP	
UNLESS NOTED:		DEFLECTION AMPLIFIER SH 1 OF 2	
CAP	μF	X OR Y	
RES	Ω, ¼W, 5%	MONITOR	
		SM-122	



DWN: 8/17/83 EVL		REV: A; B; C; D;	
UNLESS NOTED:		COMPUTER IMAGE CORP	
CAP	µF	DEFLECTION AMPLIFIER SH 2 OF 2	
RES	Ω, ¼W, 5%	X OR Y MONITOR	

SM-122.



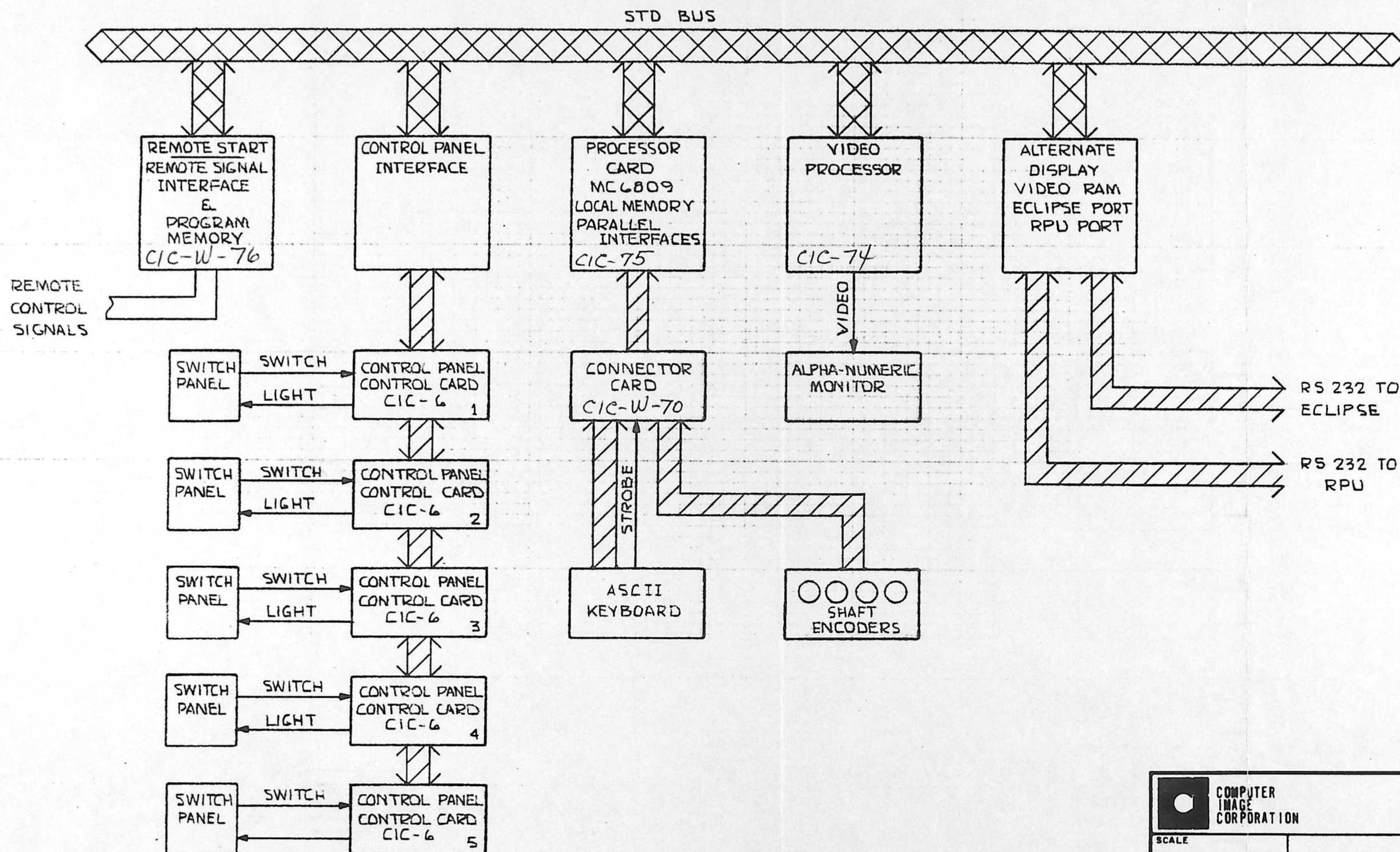
HEAT SINK
ASTRODYNE -2404-4

NOTE:
RESISTORS ARE 1/4W, UNLESS OTHERWISE SPECIFIED.

TWO COMPLETE CIRCUITS REQUIRED FOR ONE X AND Y DEFLECTION UNIT
PIN NUMBERS IN PARENTHESIS ARE FOR MON Y CHANNEL.
OTHER PIN NUMBERS ARE FOR MON X CHANNEL

REV: A, B, C, D

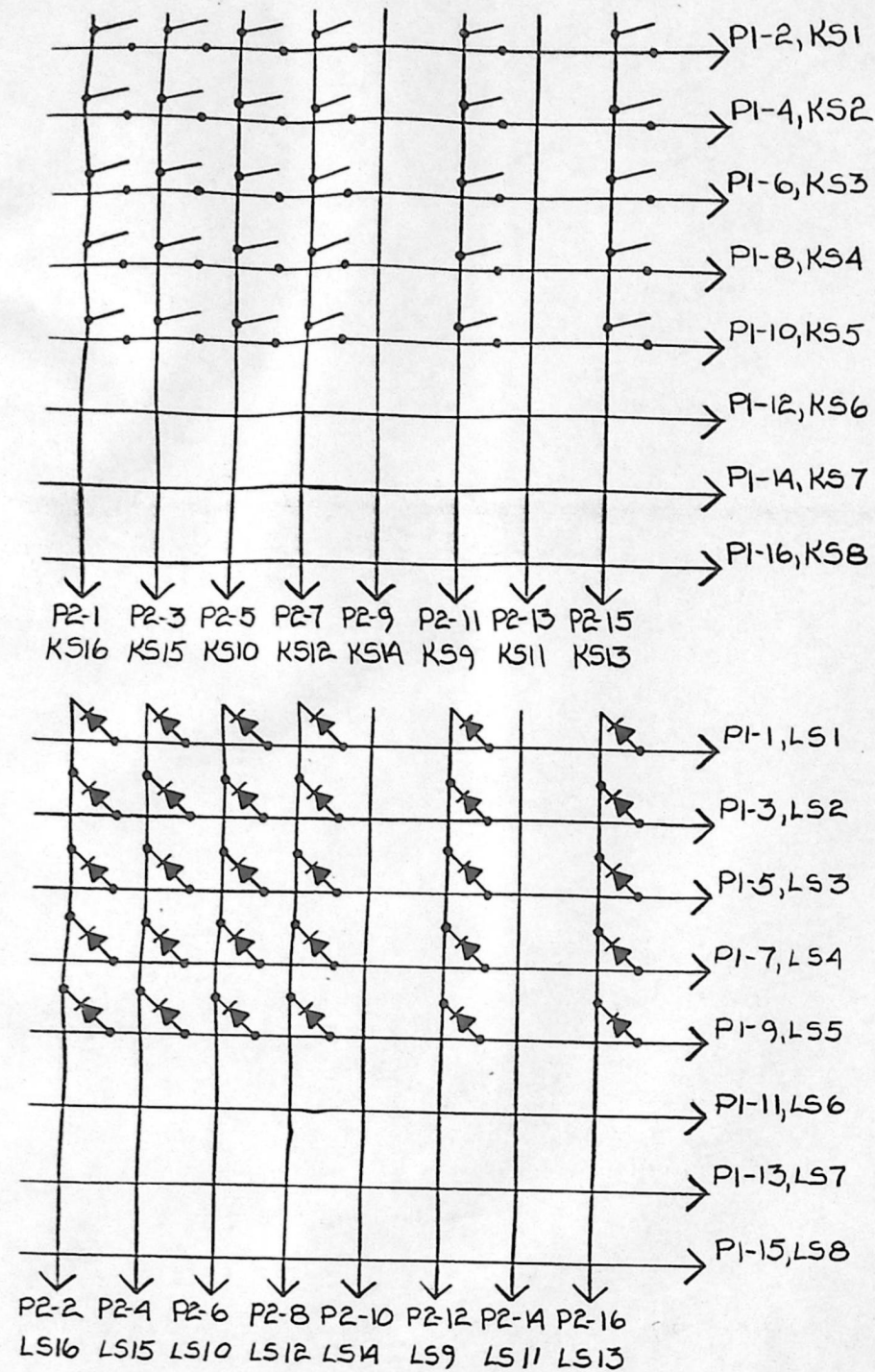
COMPUTER IMAGE CORPORATION		2475 W. 2ND. AVE. DENVER, COLORADO 80223	
SCALE NONE	REVISION	BY	DATE
DATE 9-2-71			12-16-73
DR. N. E. R.	CRD.	PER WCA MARKED PRINT	E. R. 4-28-82
FILE MONITOR X OR Y DEFLECTION AMPLIFIER SCHEMATIC		NO. 34 OF 1 SM-122	



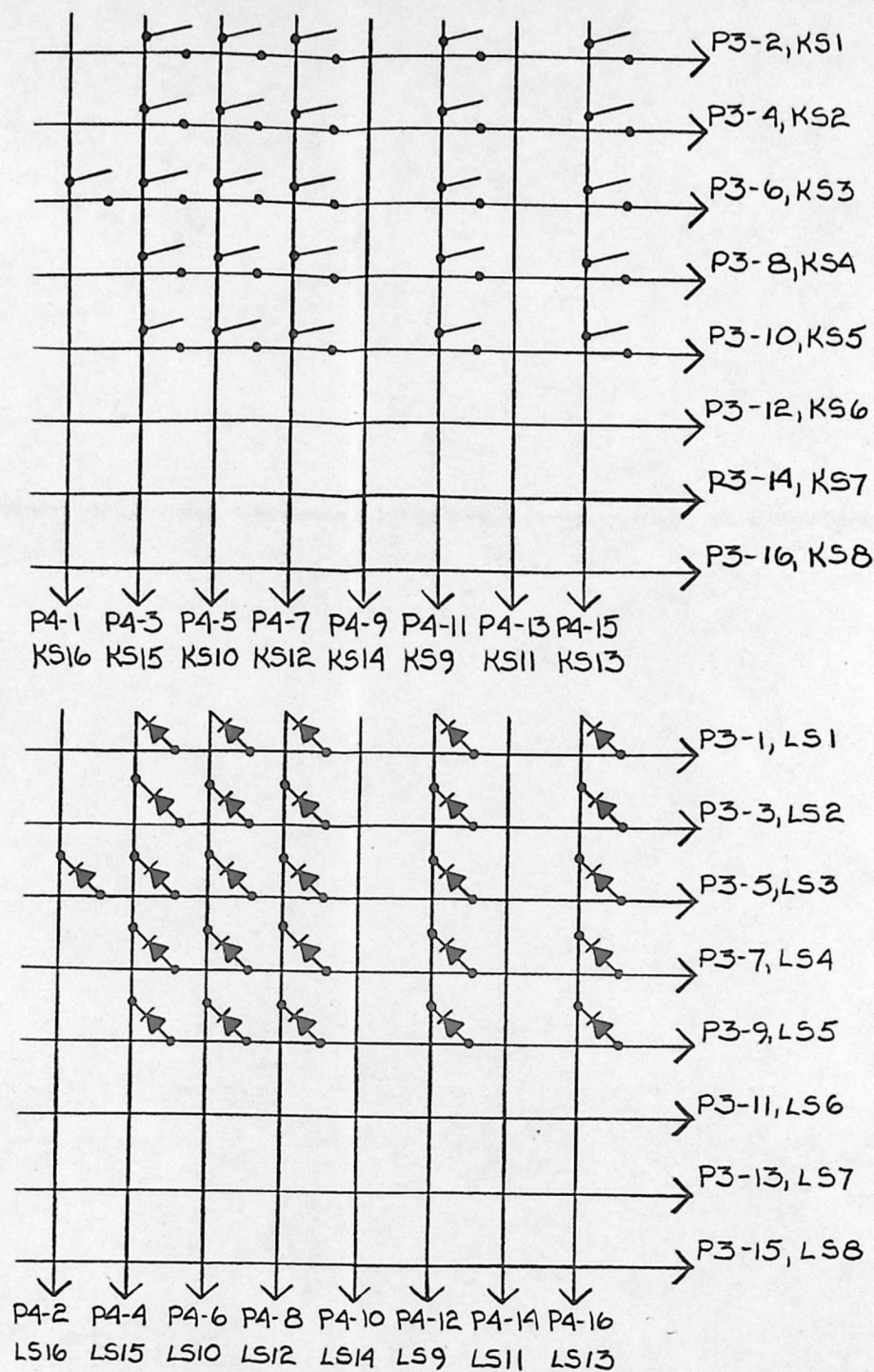
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE. DENVER, COLORADO 80223	
SCALE	REVISIONS		BY
DATE 3/26/84			DATE
DR'N EVL CKD <i>Ed</i>			
AP'VD			
TITLE CONTROL PANEL BLOCK DIAGRAM			NO BCIC-6

TOLERANCE		
FRACT.	±	1/32
.XX	±	.015
.XXX	±	.005

MODULE 1 ANIMATE MODE SWITCHES



MODULE 2 CONTROL MODE SWITCHES



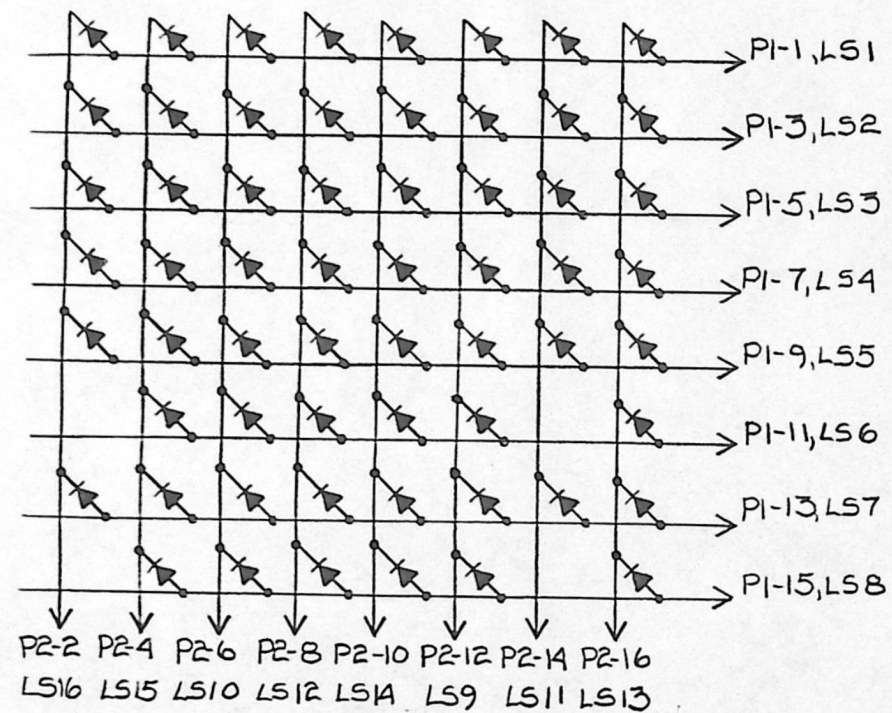
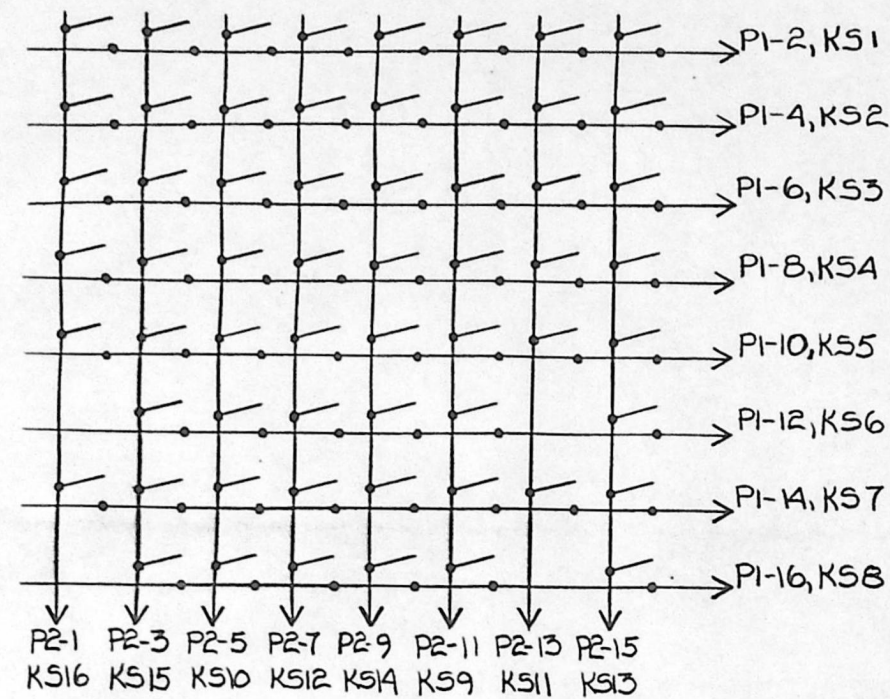
NOTES
- ALL DIODES ARE LED'S
- THE KS AND LS NUMBERS
ARE THE MATRIX CODES OF
THE CIC-6 MODULE

REV:

COMPUTER IMAGE CORP.
DENVER, COLORADO

SCALE	REVISIONS	BY	DATE
DATE 5/26/82			
DRN GSH CRD			
APVD			
TITLE	NO		
ANIMATE/CONTROL SWITCHES	CIC-2		
	SH 1 OF 1		

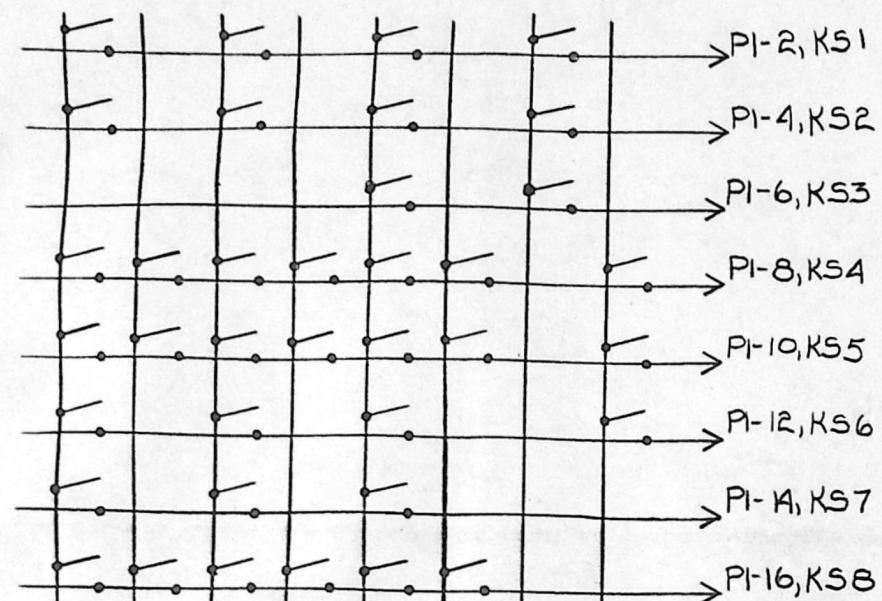
MODULE 3
MISC. FUNCTION SWITCHES



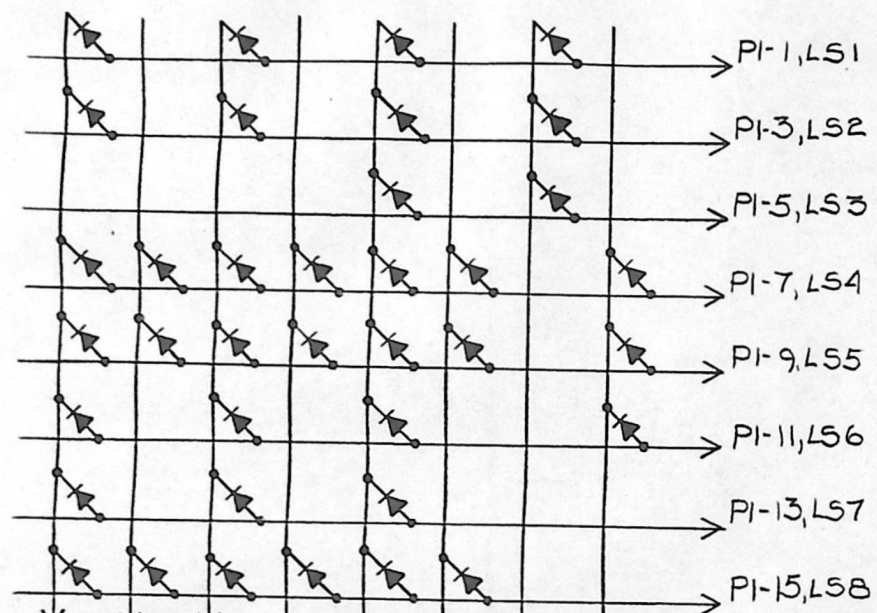
NOTES -
- ALL DIDDIES ARE LED'S
- THE KS AND LS NUMBERS
ARE THE MATRIX CODES OF
THE CIC-6 MODULE.

REV:

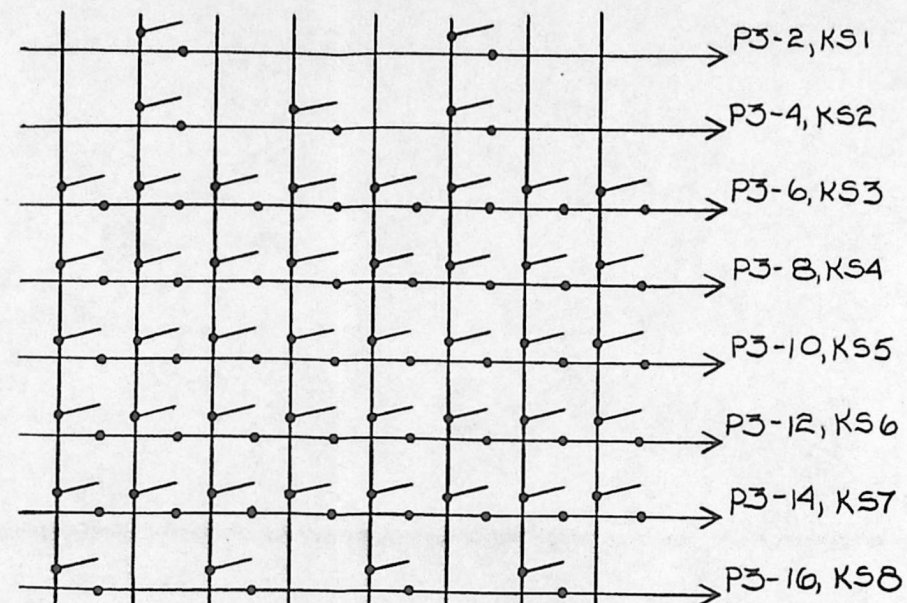
COMPUTER IMAGE CORP. DENVER, COLORADO			
SCALE	REVISIONS	BY	DATE
DATE 5/26/82			
DRN GSH CRB			
APVD			
TITLE MISC. FUNCTION SWITCHES		NO CIC-3 SH 1 OF 1	



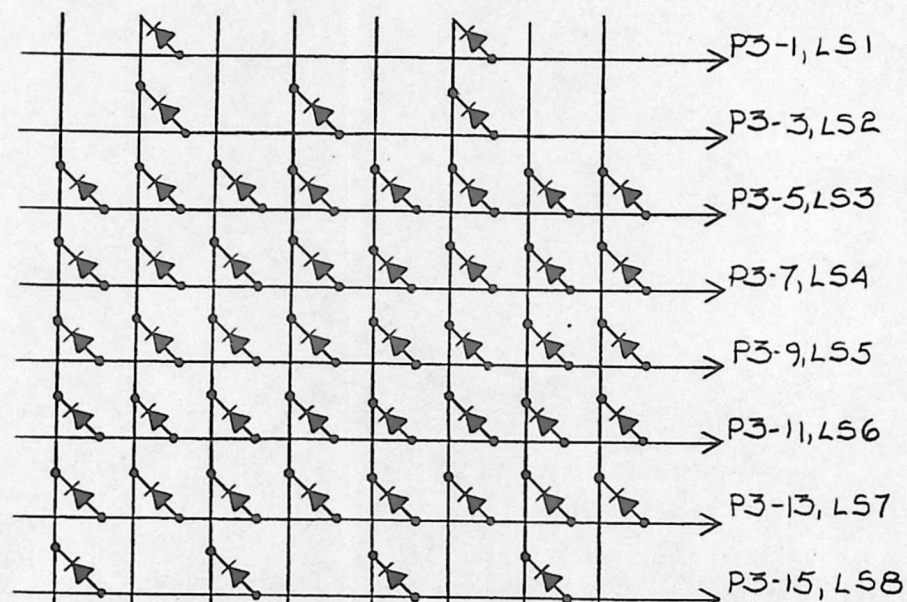
P2-1 P2-3 P2-5 P2-7 P2-9 P2-11 P2-13 P2-15
KS16 KS15 KS10 KS12 KS14 KS9 KS11 KS13



P2-2 P2-4 P2-6 P2-8 P2-10 P2-12 P2-14 P2-16
LS16 LS15 LS10 LS12 LS14 LS9 LS11 LS13



P4-1 P4-3 P4-5 P4-7 P4-9 P4-11 P4-13 P4-15
KS16 KS15 KS10 KS12 KS14 KS9 KS11 KS13



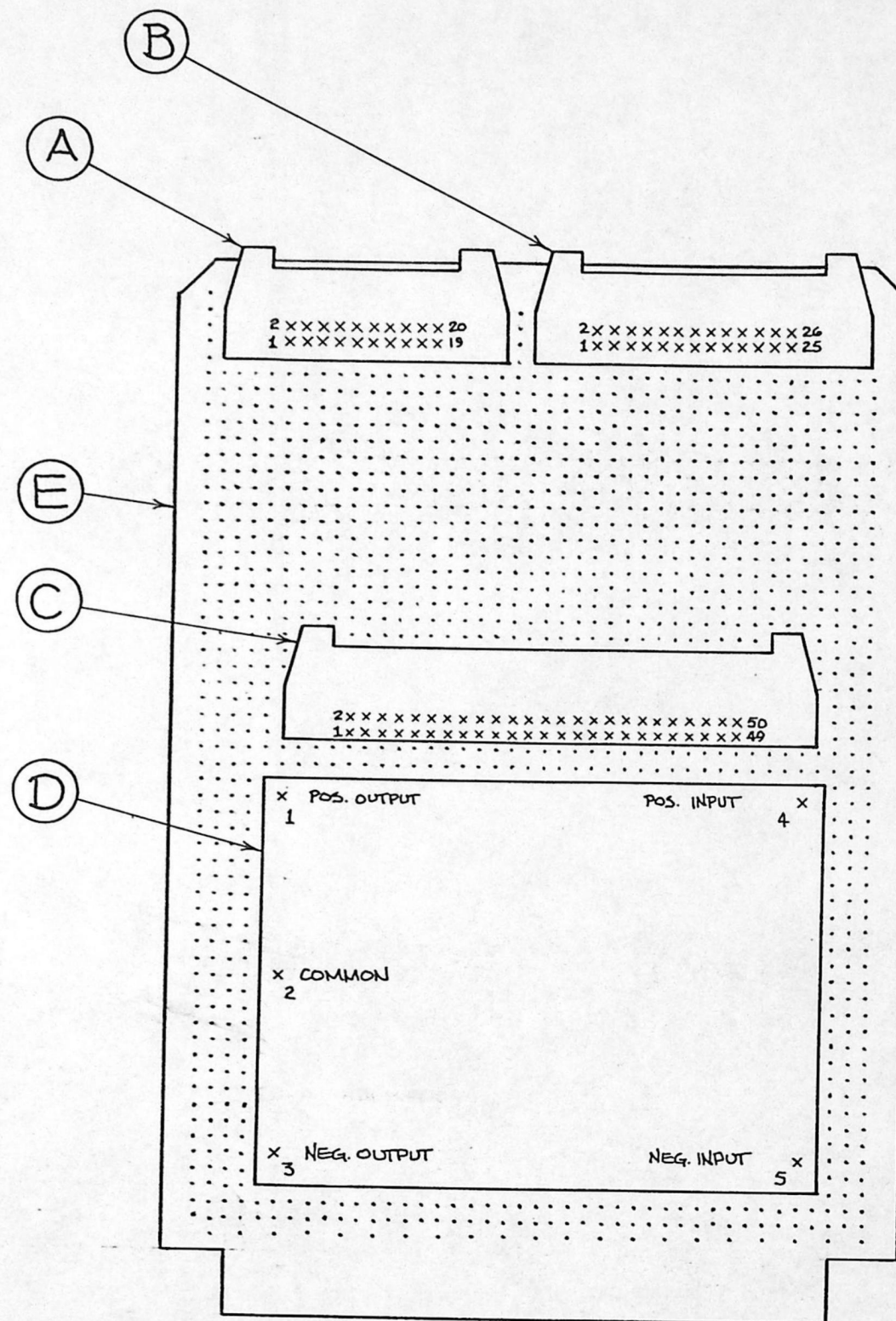
P4-2 P4-4 P4-6 P4-8 P4-10 P4-12 P4-14 P4-16
LS16 LS15 LS10 LS12 LS14 LS9 LS11 LS13

NOTES

- ALL DIODES ARE LED'S
- THE KS AND LS NUMBERS ARE THE MATRIX CODES OF THE CIC-6 MODULES.

REV: A

COMPUTER IMAGE CORP. DENVER, COLORADO			
SCALE	REVISIONS	BY	DATE
DATE 5/27/82			
DRN GSH CRD			
APVD			
TITLE SHAFT ENCODER SWITCHES		NO CIC-4A SH 1 OF 1	



REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

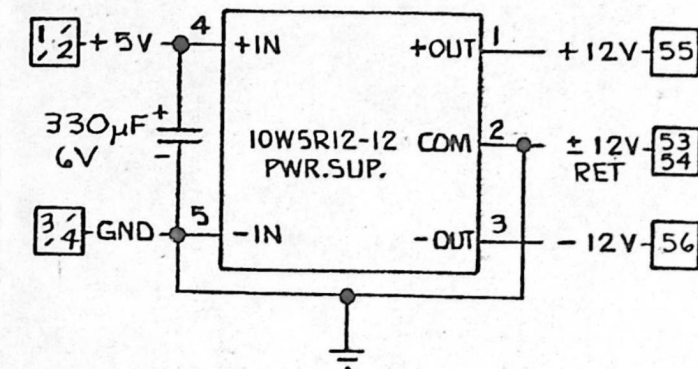
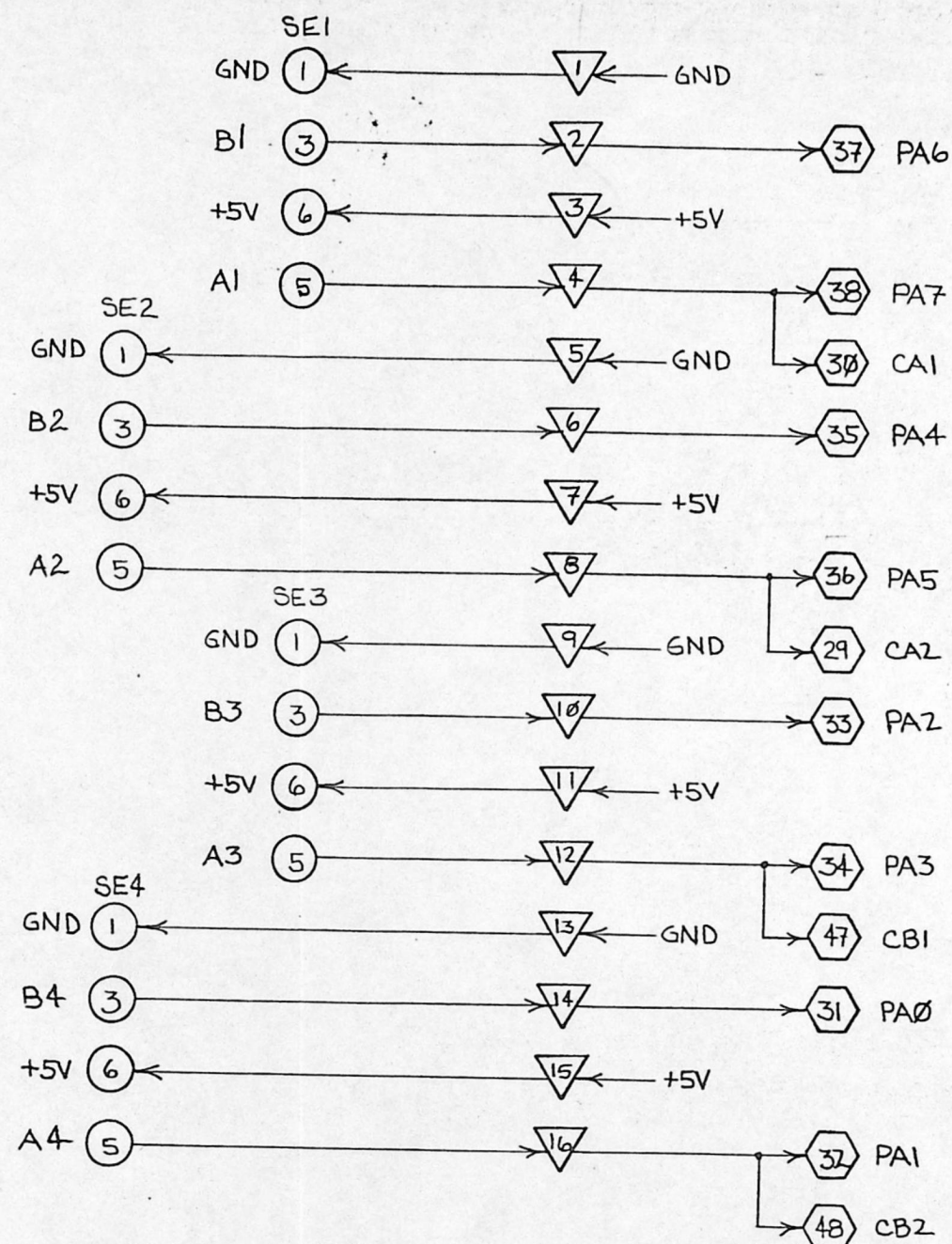
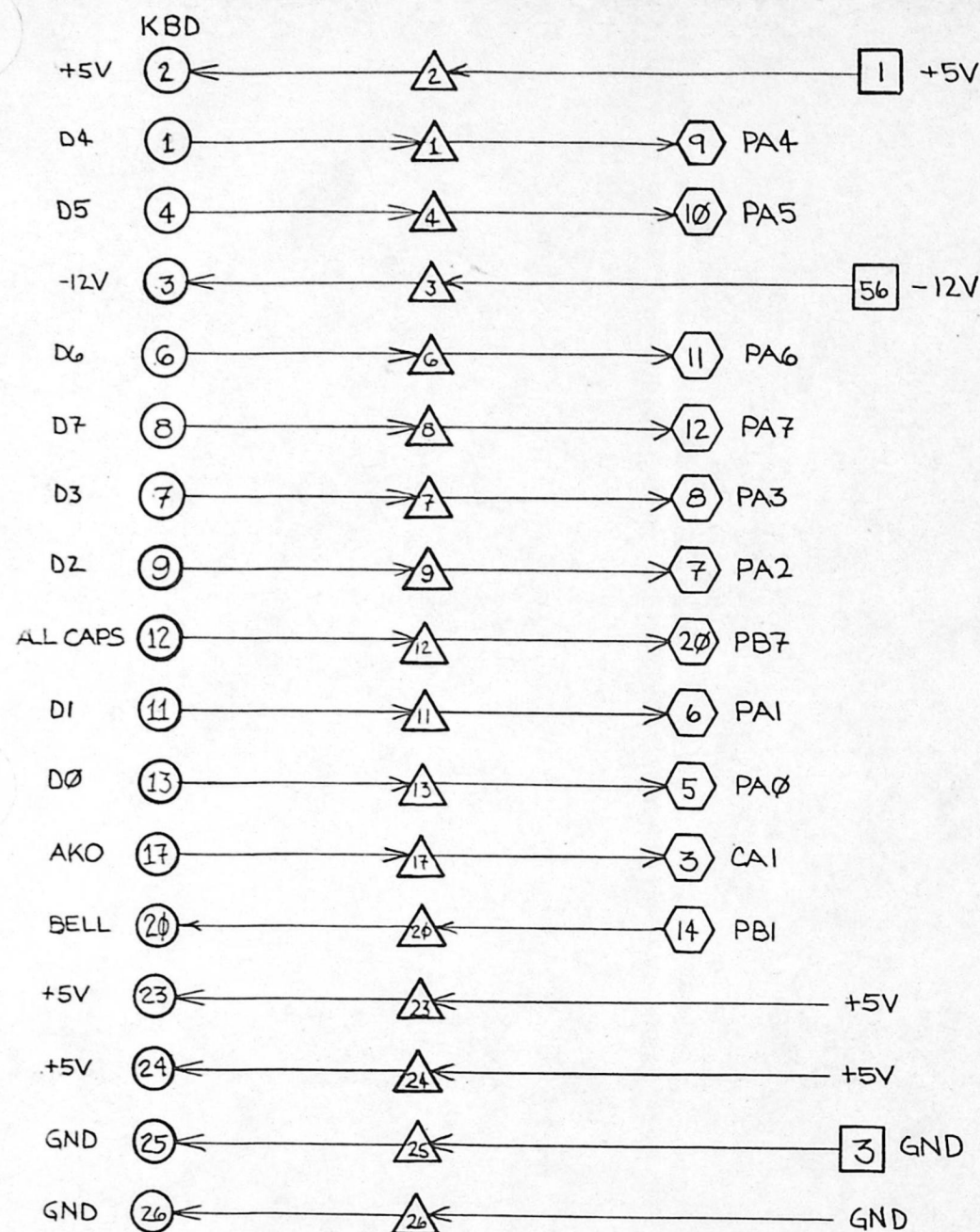
PARTS LIST (REF ONLY)

QUAN	P/N	MFG.	Description	Ref.
1	3428-3205	3M	20 Pin Header	A
1	3429-3205	3M	26 Pin Header	B
1	3433-3205	3M	50 Pin Header	C
1	10W5R12-12	Reliability	5VDC to + 12VDC Converter	D
1	4610-1	Vector	Std. Bus W.W. Board	E

REV: B;

TOLERANCES UNLESS OTHERWISE SPECIFIED FRACTIONS DEC ANGLES ± ± ±	ATA Corp.		
	COMPUTER IMAGE		
APPROVALS	DATE	AUX CONNECT/12V P S CONTROL PANEL - SYSTEM IV	
DRAWN S.P.	4-2-82		
CHECKED		SCALE	SIZE DRAWING NO.
			B CIC-W-70
DO NOT SCALE DRAWING			SHEET 1 OF 2

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
B	AS PER E.C.O N°: 0204-02L1	4-2-82	



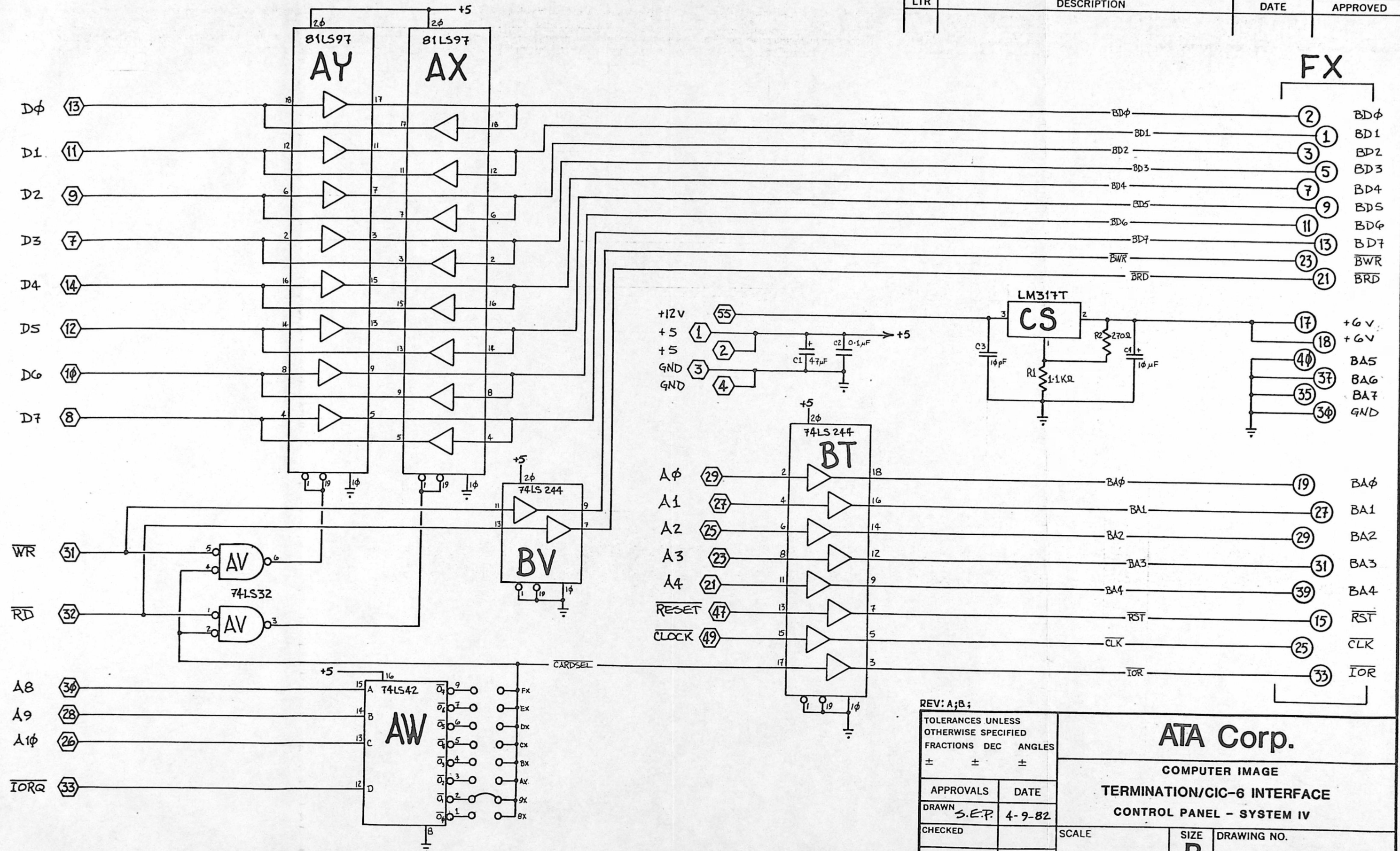
PB7 (46) to 37 VBLNK

REV: B;

- 26 PIN CONNECTOR B
- 20 PIN CONNECTOR A
- CONNECTORS AT KEYBOARD & SHAFT ENCODERS
- CONNECTOR TO/FROM 6809 BOARD C
- STANDARD BUS PINS

TOLERANCES UNLESS OTHERWISE SPECIFIED			ATA Corp.	
FRACTIONS	DEC	ANGLES		
±	±	±	COMPUTER IMAGE	
APPROVALS			AUX CONNECT/12V P S	
DRAWN LPD			CONTROL PANEL - SYSTEM IV	
CHECKED			SCALE	DRAWING NO.
			SIZE B	CIC-W-70
			DO NOT SCALE DRAWING	
			SHEET 2 OF 2	

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



REV: A;B;

TOLERANCES UNLESS
OTHERWISE SPECIFIED
FRACTIONS DEC ANGLES
± ± ±

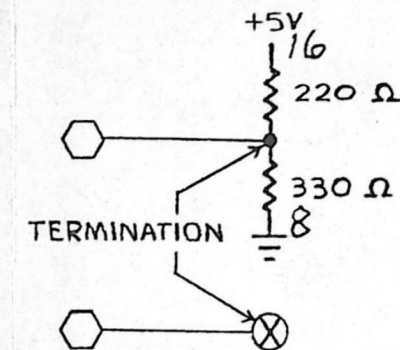
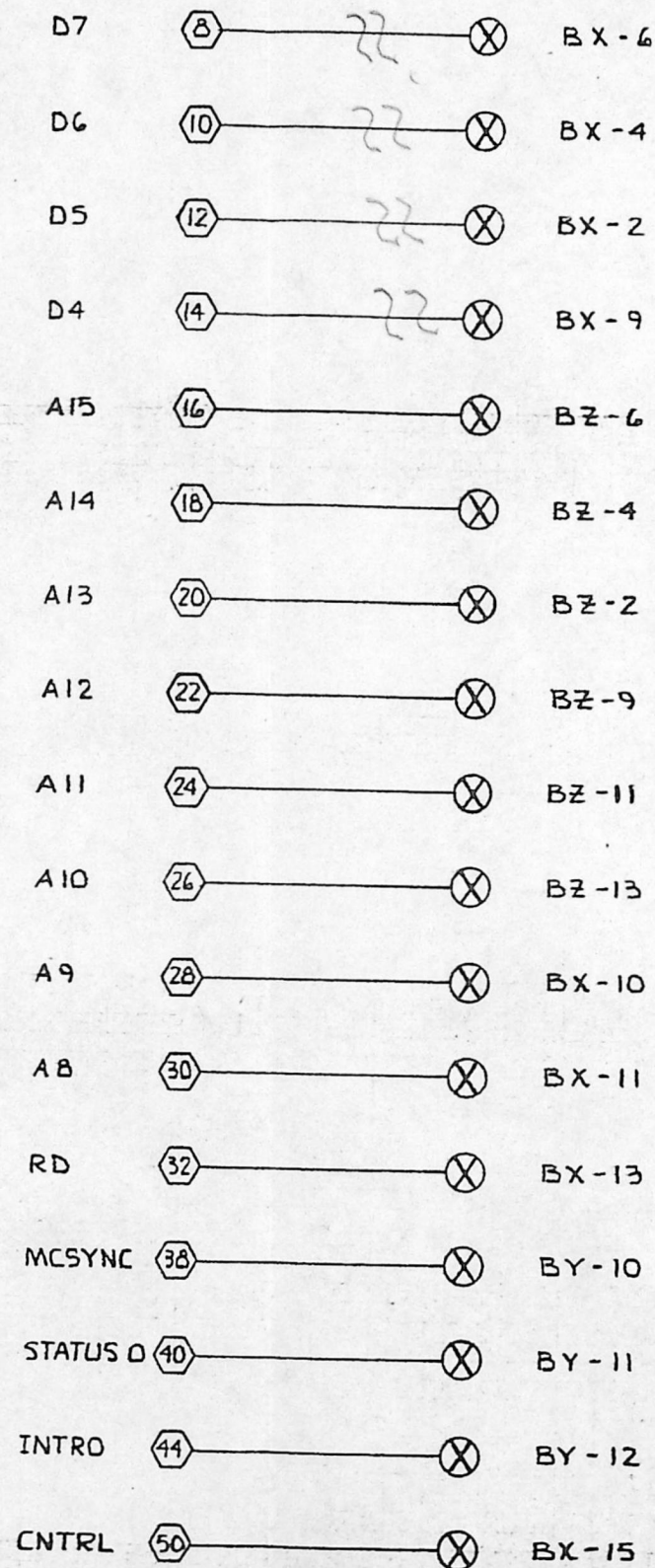
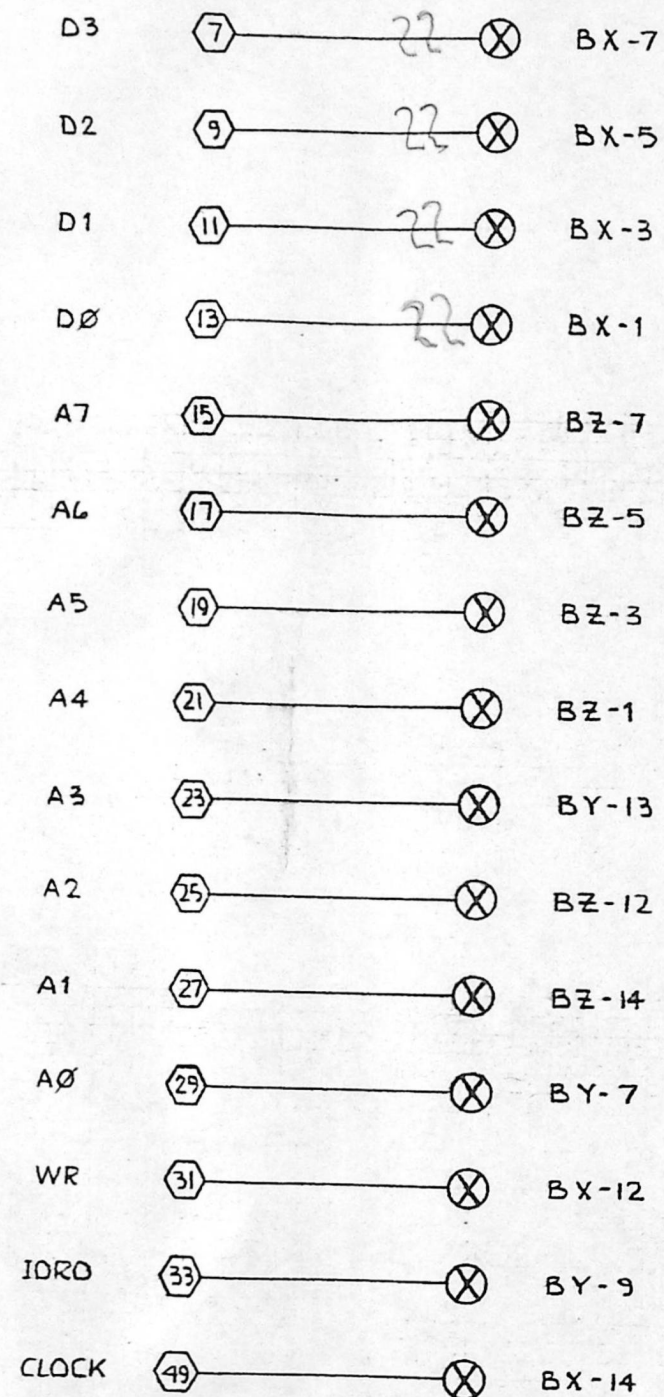
APPROVALS	DATE
DRAWN S.E.P.	4-9-82
CHECKED	

ATA Corp.

COMPUTER IMAGE

TERMINATION/CIC-6 INTERFACE
CONTROL PANEL - SYSTEM IV

SCALE	SIZE	DRAWING NO.
	B	CIC-W-71
DO NOT SCALE DRAWING		SHEET 1 OF 2



22 = LIFTED TERMINATION WIRE

REV: A; B;

APPROVALS		DATE	COMPUTER IMAGE		
			TERMINATION/CIC-6 INTERFACE		
DRAWN		4-18-83	CONTROL PANEL - SYSTEM IV		
EVL			SCALE	SIZE	DRAWING NO.
CHECKED				CIC-W-71	
				SHEET 2 OF 2	

←	→
↓	↑
{	}
[	]
⋮	CEM

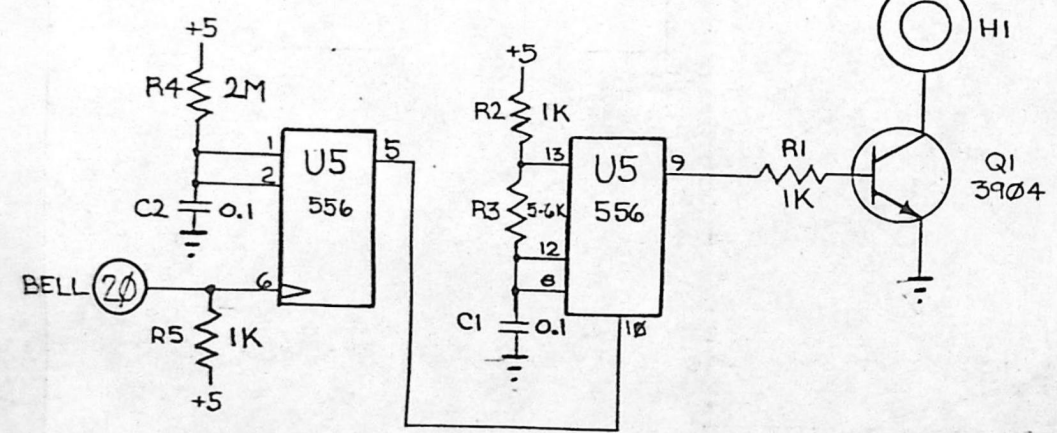
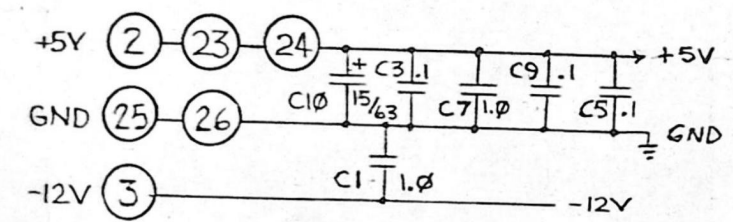
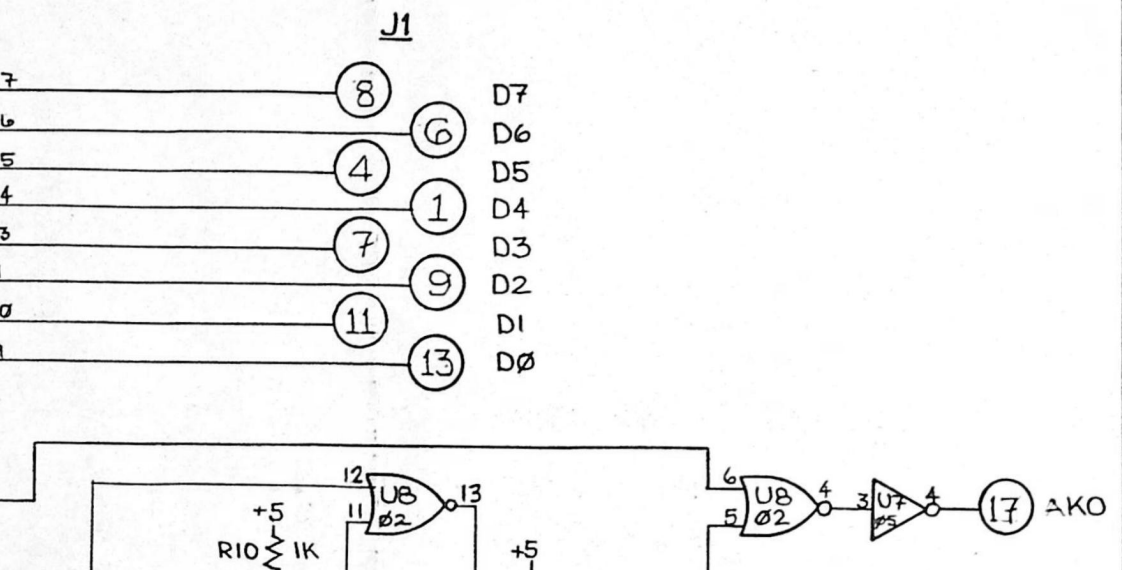
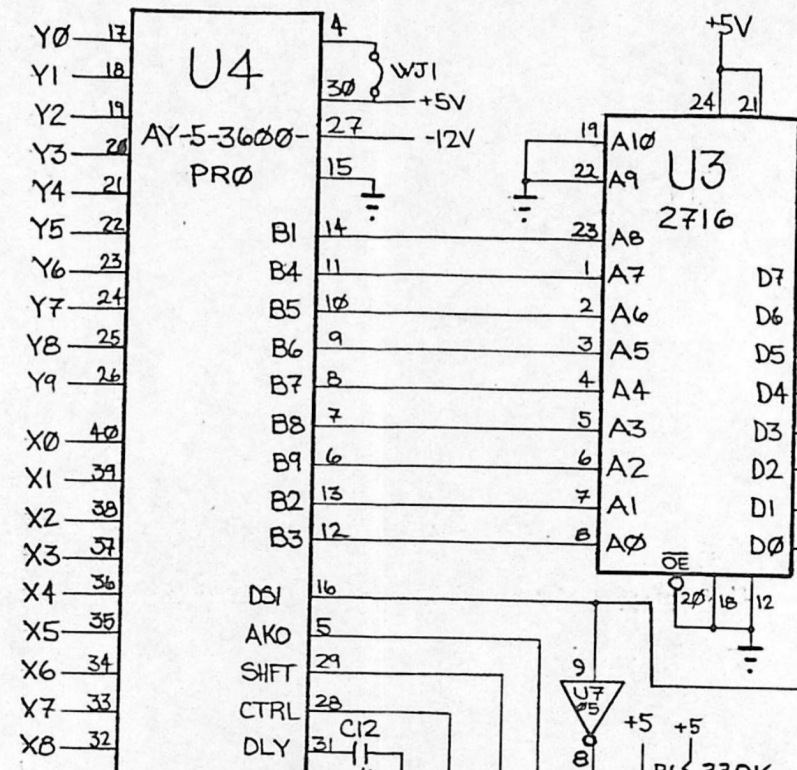
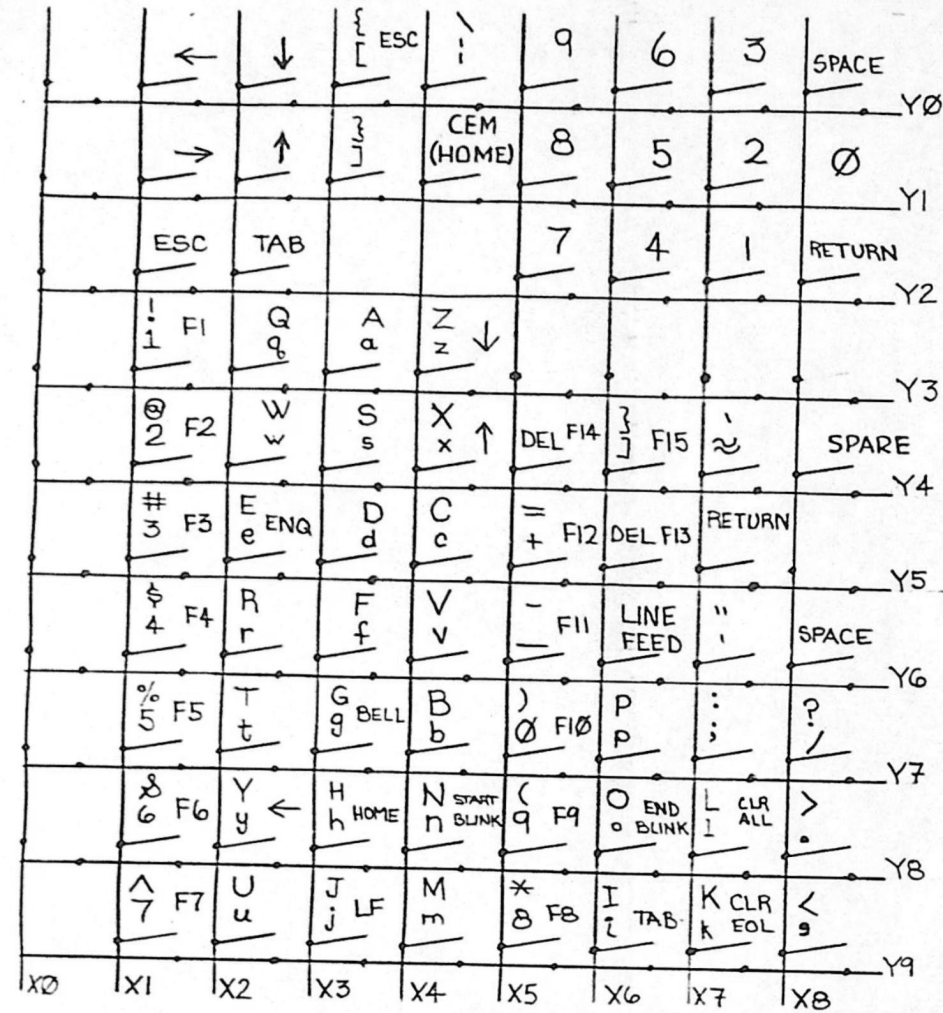
ESC	!	@	#	\$	%	&	^	*	(	)	—	=	DEL	
	1	2	3	4	5	6	7	8	9	Ø	—	+		
TAB	Q	W	E	R	T	Y	U	I	O	P	LINE FEED			
CAPS LOCK	A	S	D	F	G	H	J	K	L	⋮	⋮	RETURN		
											;	/		
SHIFT	Z	X	C	V	B	N	M	<	>	?		SHIFT		
EXT	CONTROL											CONTROL		

7	8	9
4	5	6
1	2	3
RET	Ø	

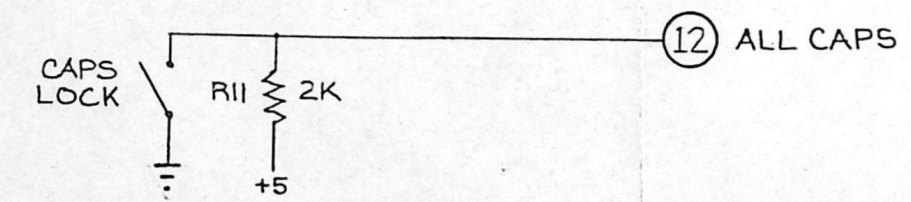
		COMPUTER IMAGE CORP.		DENVER, CO.	
SCALE	NONE	REVISIONS		BY	DATE
DATE	1-12-81				
DRN	QSH	CKD			
APVD					
TITLE				NO	
SYSTEM IV				CIC-W-73	
KEYBOARD					

KEYBOARD PCB SCI-PRO SP-100

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED
A	AS PER E.C.O. NO. 0204-0211	4-2-82	



NOTE: ALL CAPACITOR VALUES IN MICROFARADS UNLESS OTHERWISE NOTED

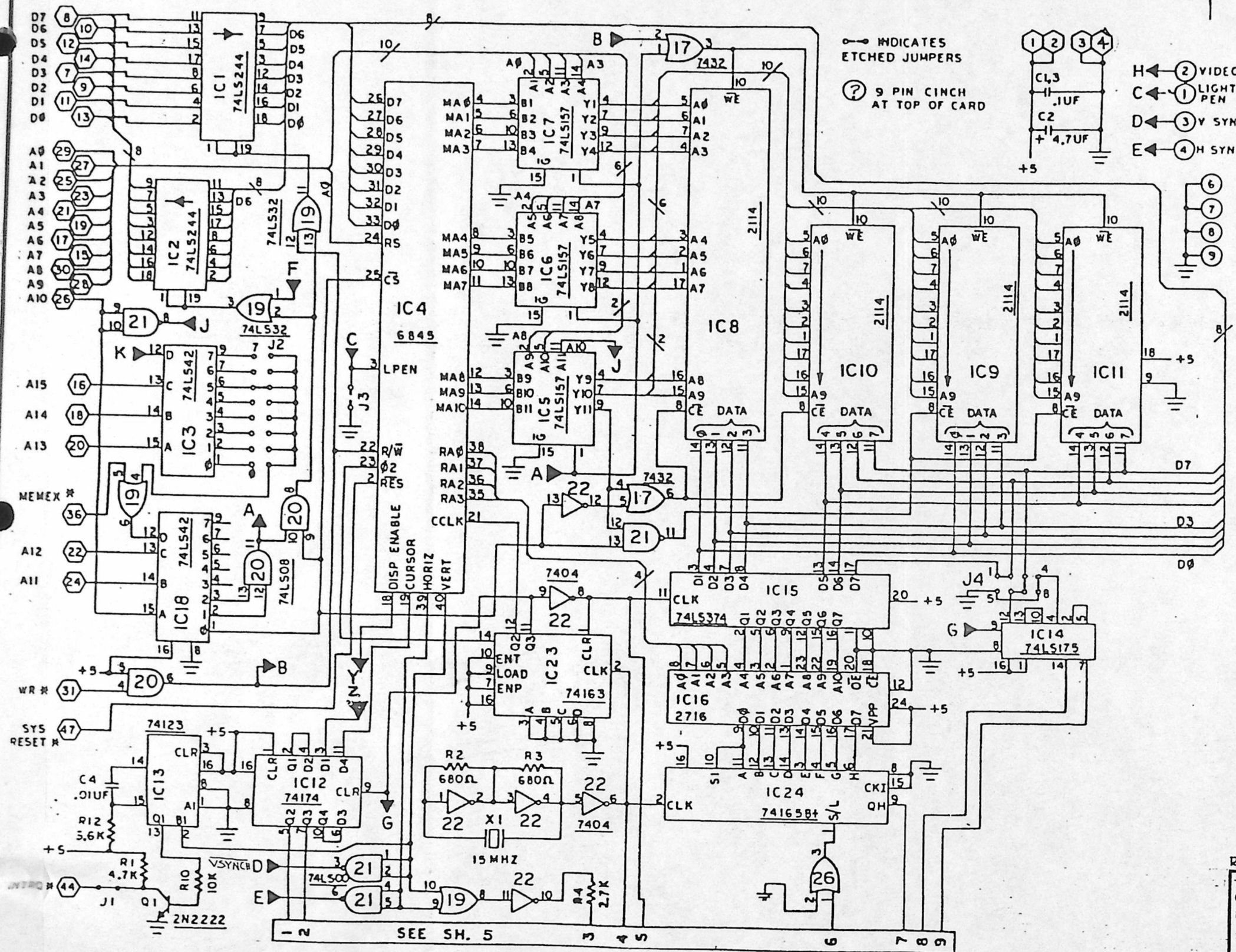


REV: A,

TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
±	±	±
APPROVALS	DATE	
DRAWN LPD	9/10/01	
CHECKED		

ATA Corp.			
COMPUTER IMAGE			
TERMINAL KEYBOARD			
CONTROL PANEL - SYSTEM IV			
CIC-W-73	SIZE B	DRAWING NO.	10918L1
DO NOT SCALE DRAWING		SHEET 2 of 2	

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



Microlink CRT Controller

97098

Jumper Installation

- J1 - Install
Interrupt Enable
- J2 - 5. NO CHANGE (FACTORY INSTALLED)
- J3 - No change
(Factory Installed)
- J4 - Character Highlighting
Cut Etches:
1-5
2-6
3-4
Install Jumpers:
4-8
3-7
1-2

REV: A; B;

TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
$\pm$	$\pm$	$\pm$

APPROVALS	DATE
DRAWN	
CHECKED	

ATA Corp

COMPUTER IMAGE

CRT CONTROLLER

CONTROL PANEL-SYSTEM IV

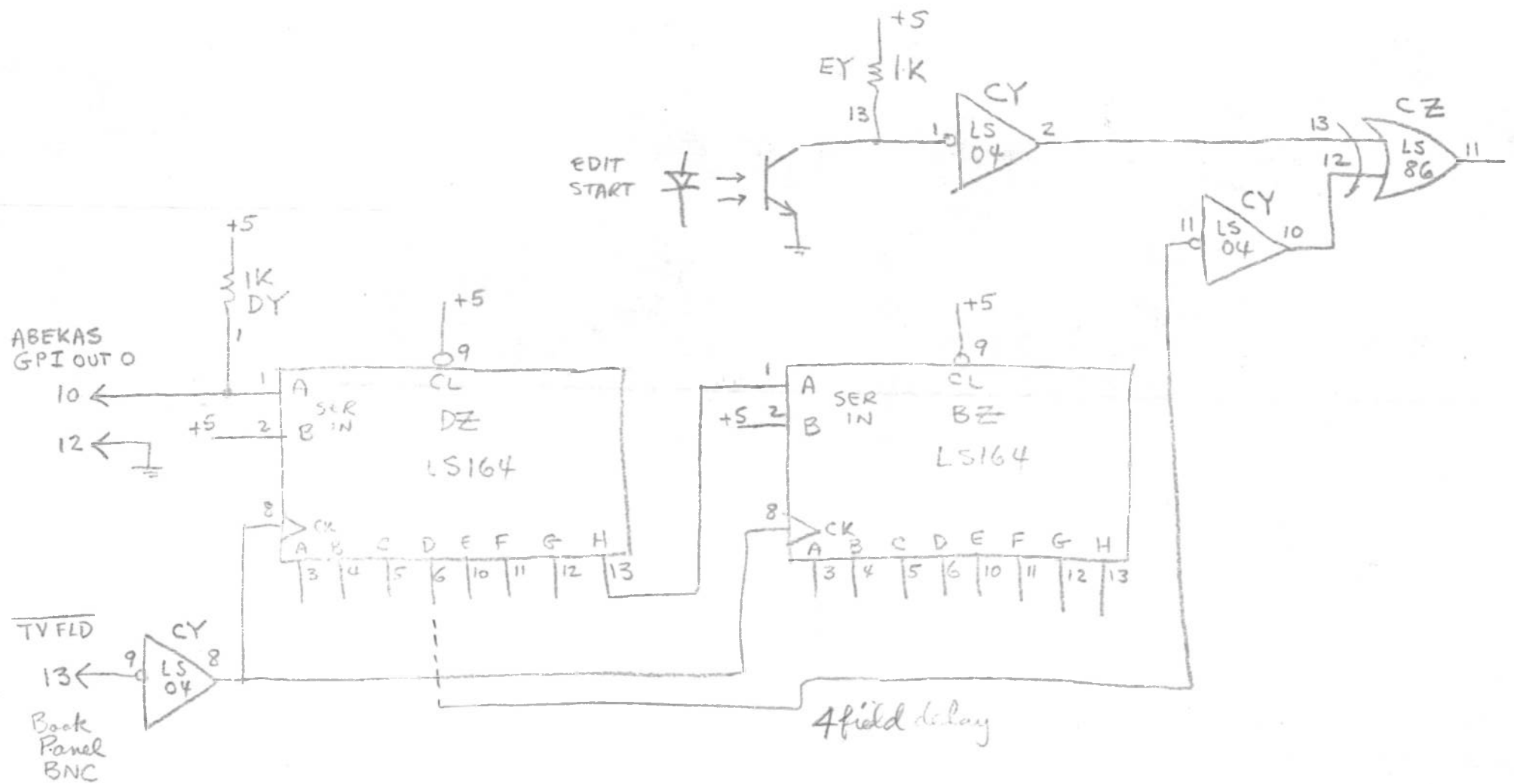
SCALE	SIZE	DRAWING NO.
	B	CIC-W-74

DO NOT SCALE DRAWING

SHEET 1 OF 2

SEE BUILD FILE

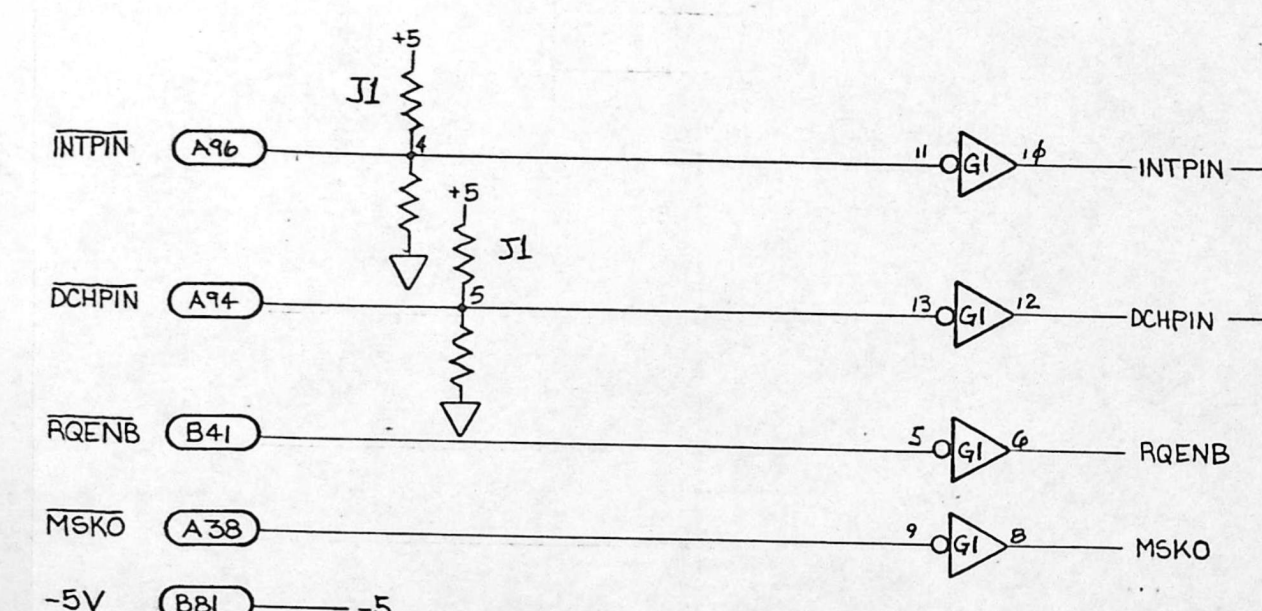
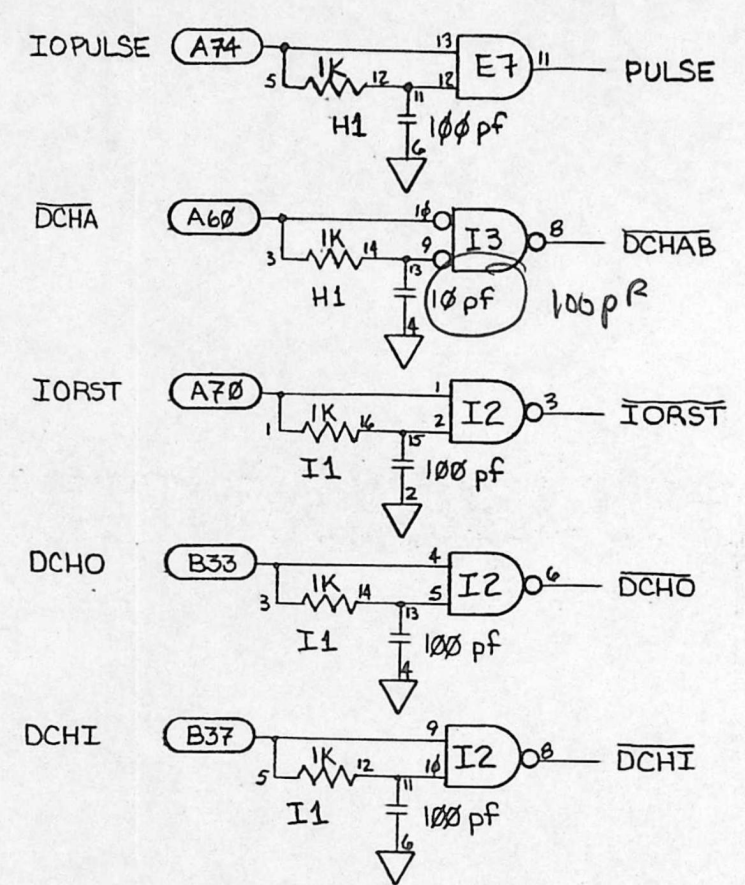
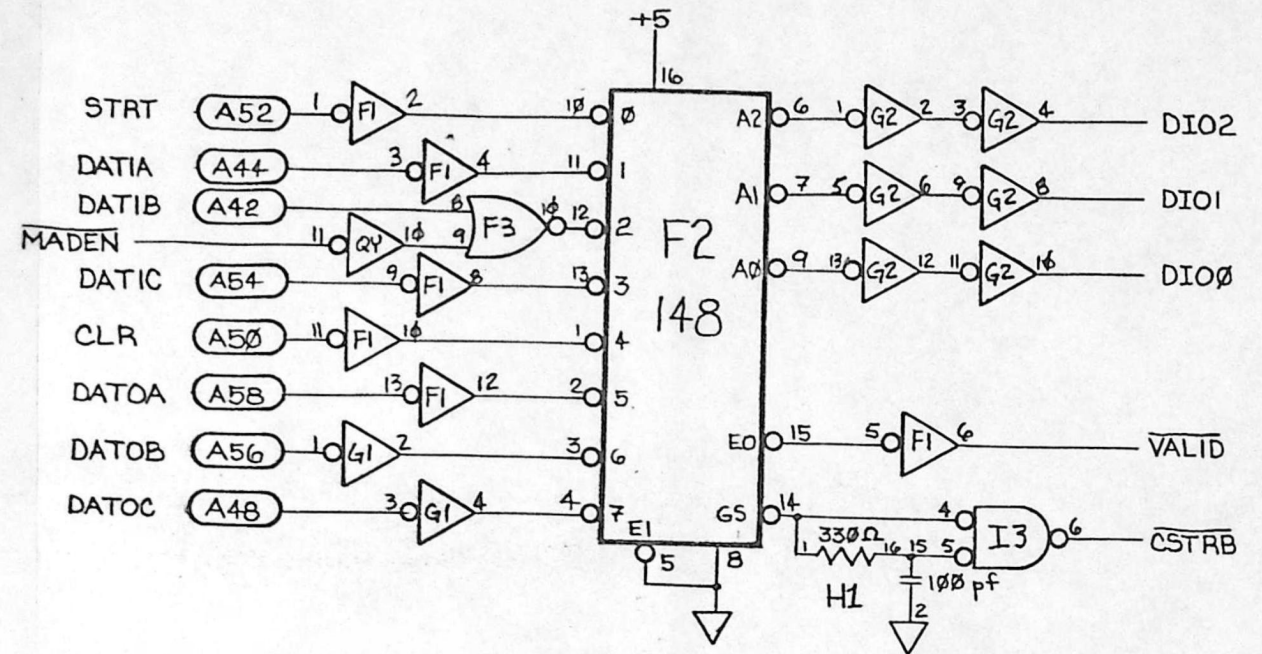
CK-W-75



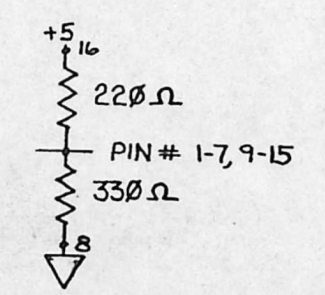
CIC-W-76

Mod to delay
start of Sys 4
by ABEKAS

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



NOTE:
TYPICAL
J1

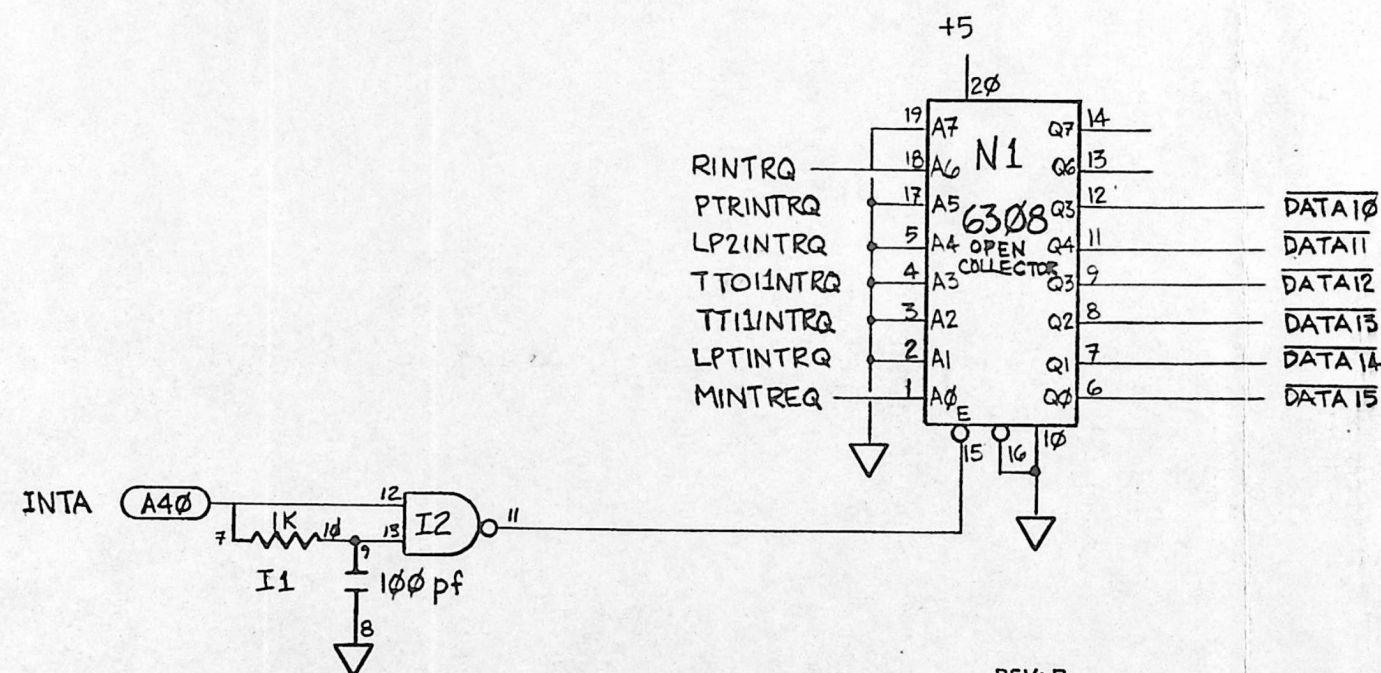
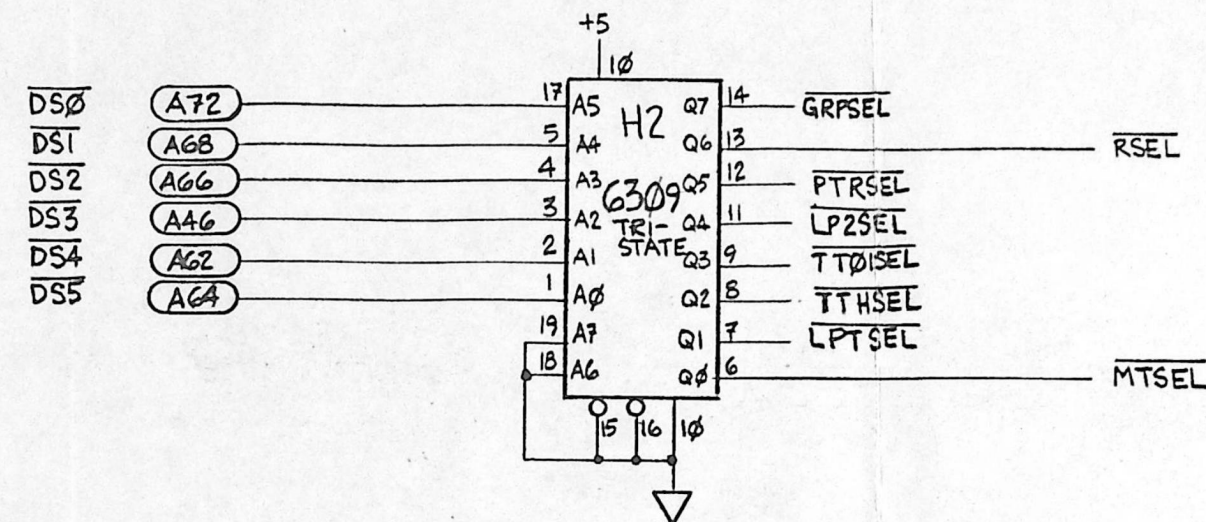
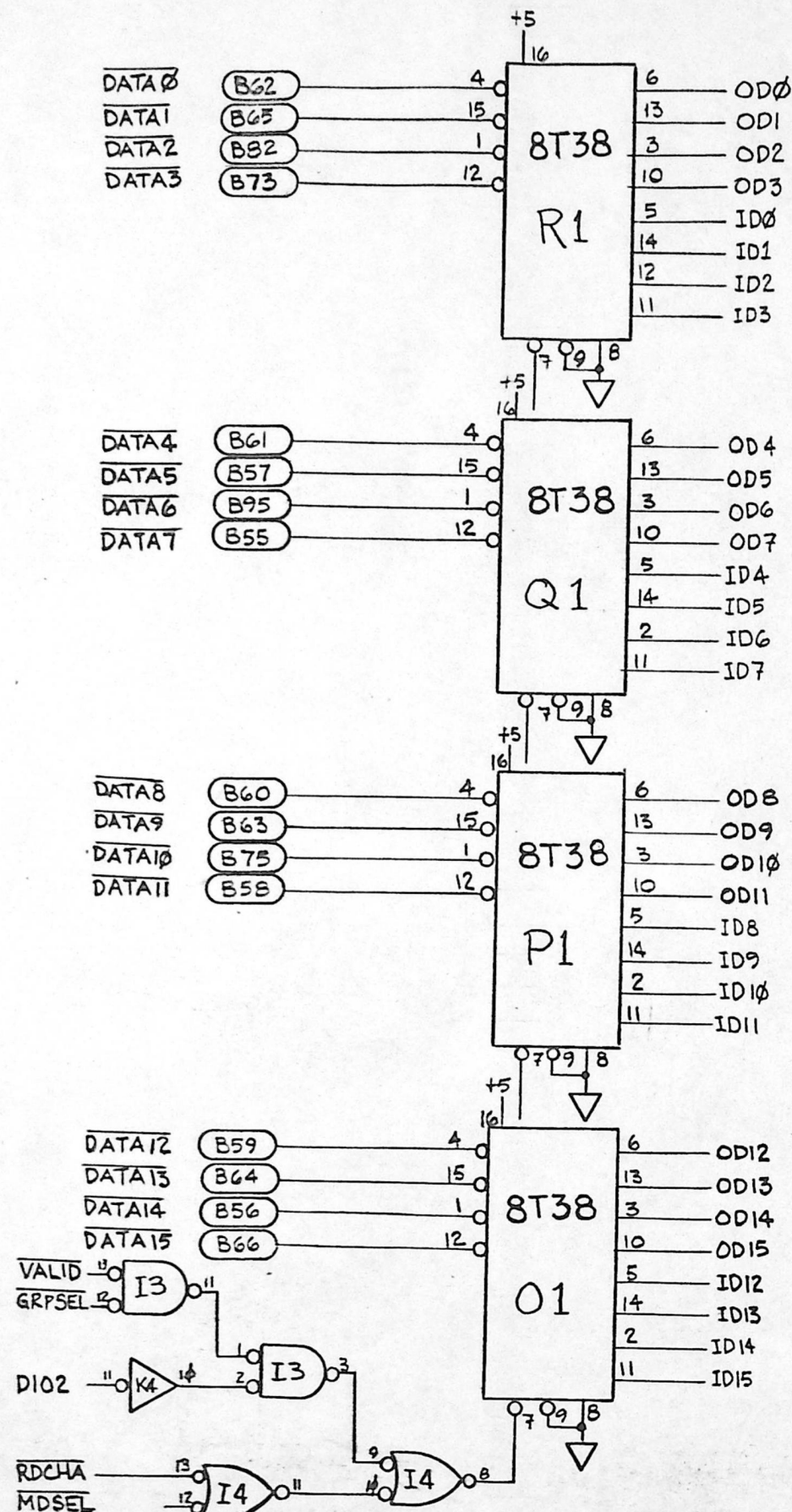


REV: B;

TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
±	±	±
APPROVALS	DATE	
DRAWN <i>[Signature]</i>	3/1/82	
CHECKED		

ATA Corp.			
COMPUTER IMAGE			
RPU/MAG TAPE INTERFACE			
I/O Buffers & Select Logic			
SCALE	SIZE	DRAWING NO.	
	B	CIC-W-60	
DO NOT SCALE DRAWING		SHEET 1 OF 9	

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



REV: B;

TOLERANCES UNLESS
OTHERWISE SPECIFIED
FRACTIONS DEC ANGLES
± ± ±

APPROVALS DATE
DRAWN 3/1/82
CHECKED

ATA Corp.

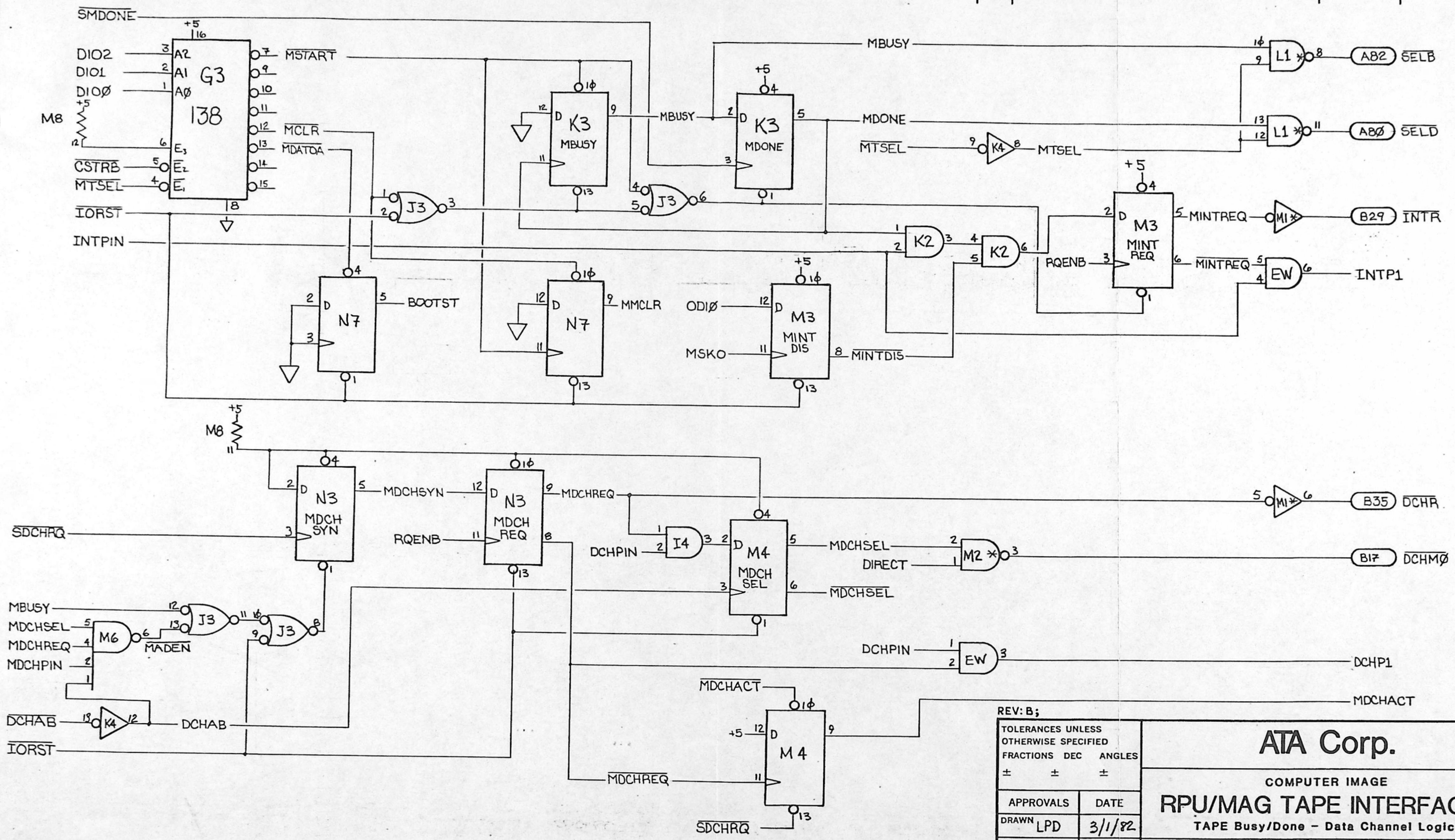
COMPUTER IMAGE
RPU/MAG TAPE INTERFACE

Data Bus Receivers & Drivers, Device Selects

SCALE SIZE DRAWING NO.
B CIC-W-60

DO NOT SCALE DRAWING SHEET 2 OF 9

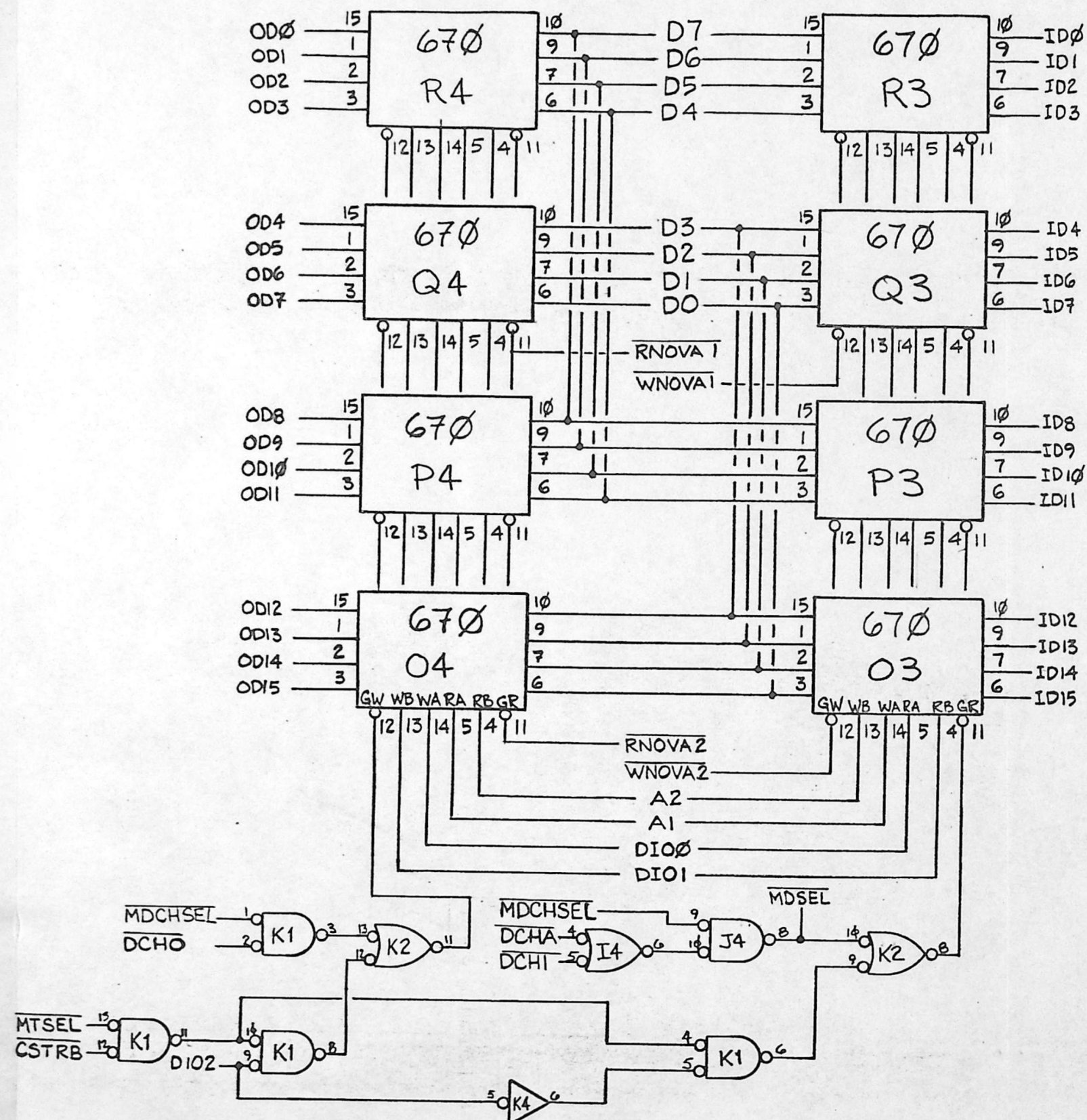
REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



REV: B;		
TOLERANCES UNLESS OTHERWISE SPECIFIED	FRACTIONS	DEC ANGLES
±	±	±
APPROVALS	DATE	
DRAWN LPD	3/1/82	
CHECKED		

ATA Corp.		
COMPUTER IMAGE		
RPU/MAG TAPE INTERFACE		
TAPE Busy/Done - Data Channel Logic		
SCALE	SIZE	DRAWING NO.
	B	CIC-W-60
DO NOT SCALE DRAWING		SHEET 3 OF 9

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



REV: B;

TOLERANCES UNLESS
OTHERWISE SPECIFIED
FRACTIONS DEC ANGLES
± ± ±

APPROVALS	DATE
DRAWN <i>JS</i>	3/1/82
CHECKED	

ATA Corp.

COMPUTER IMAGE
RPU/MAG TAPE INTERFACE

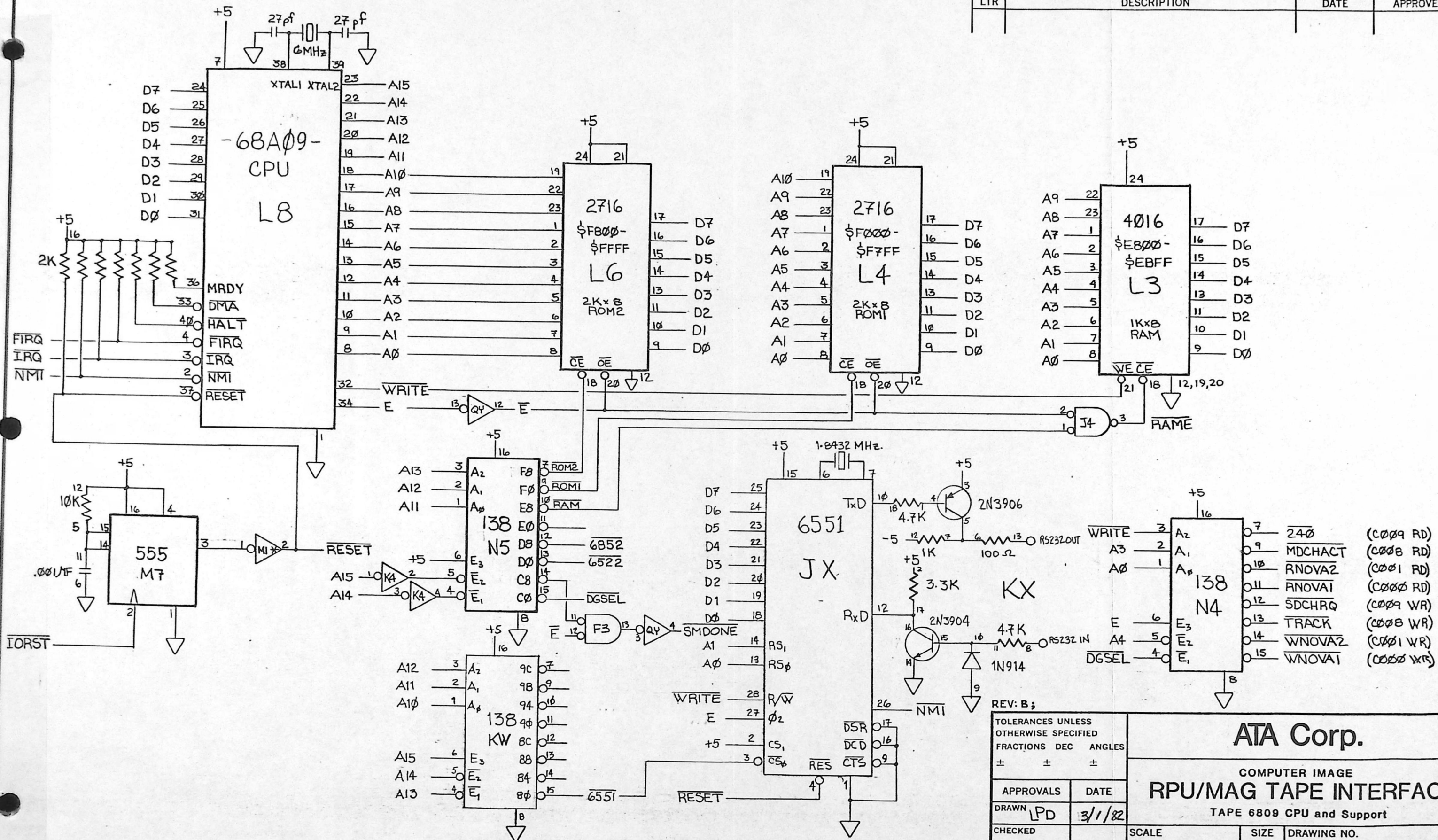
TAPE Data In and Out Buffers

SCALE	SIZE	DRAWING NO.
	B	CIC-W-60

DO NOT SCALE DRAWING

SHEET 4 OF 9

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

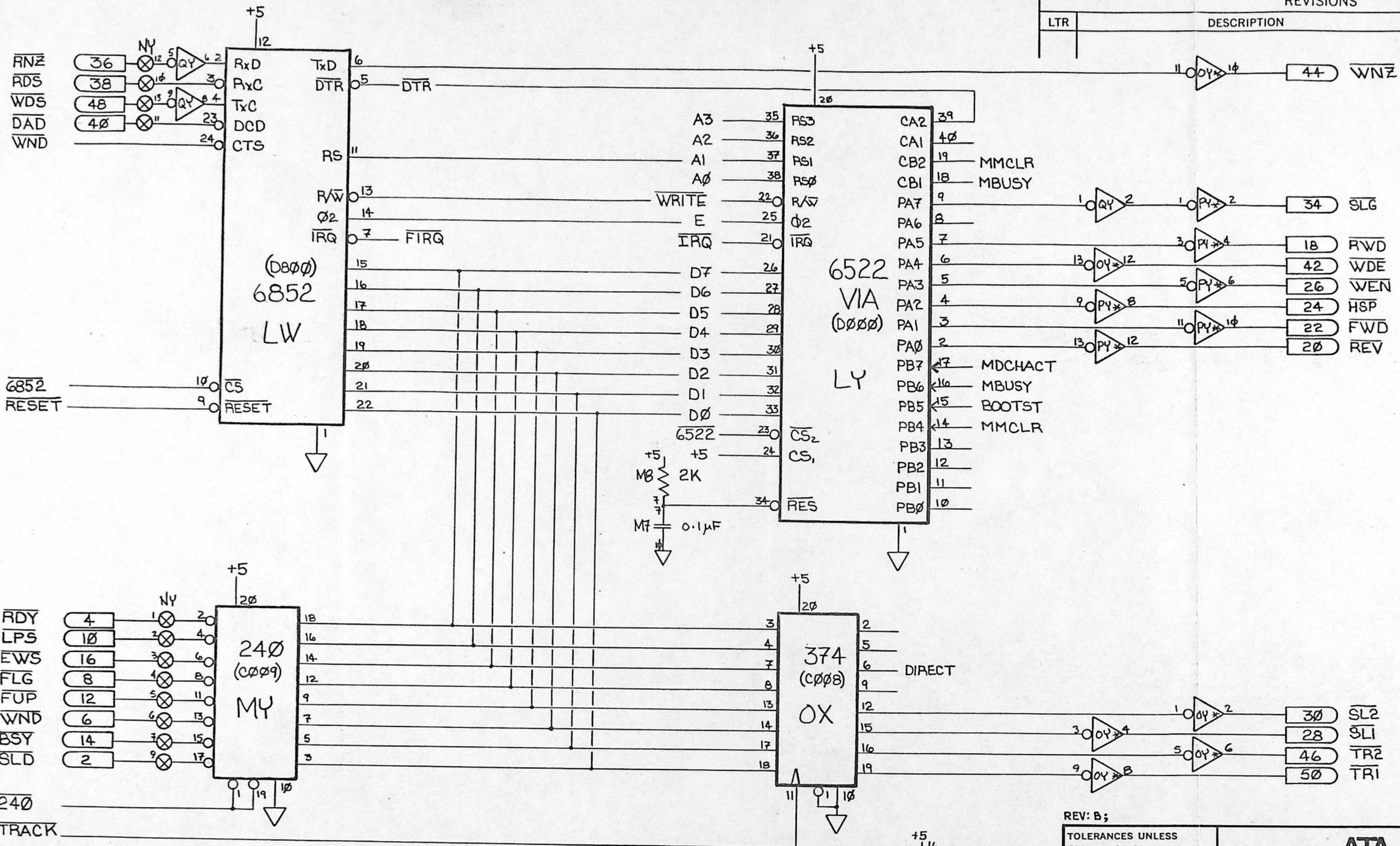


REV: B;

TOLERANCES UNLESS
OTHERWISE SPECIFIED
FRACTIONS DEC ANGLES
± ± ±

APPROVALS DATE
DRAWN LPD 3/1/82
CHECKED

ATA Corp.			
COMPUTER IMAGE RPU/MAG TAPE INTERFACE TAPE 6809 CPU and Support			
SCALE	SIZE B	DRAWING NO. CIC-W-60	
DO NOT SCALE DRAWING		SHEET 5 OF 9	



REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

REV: B;

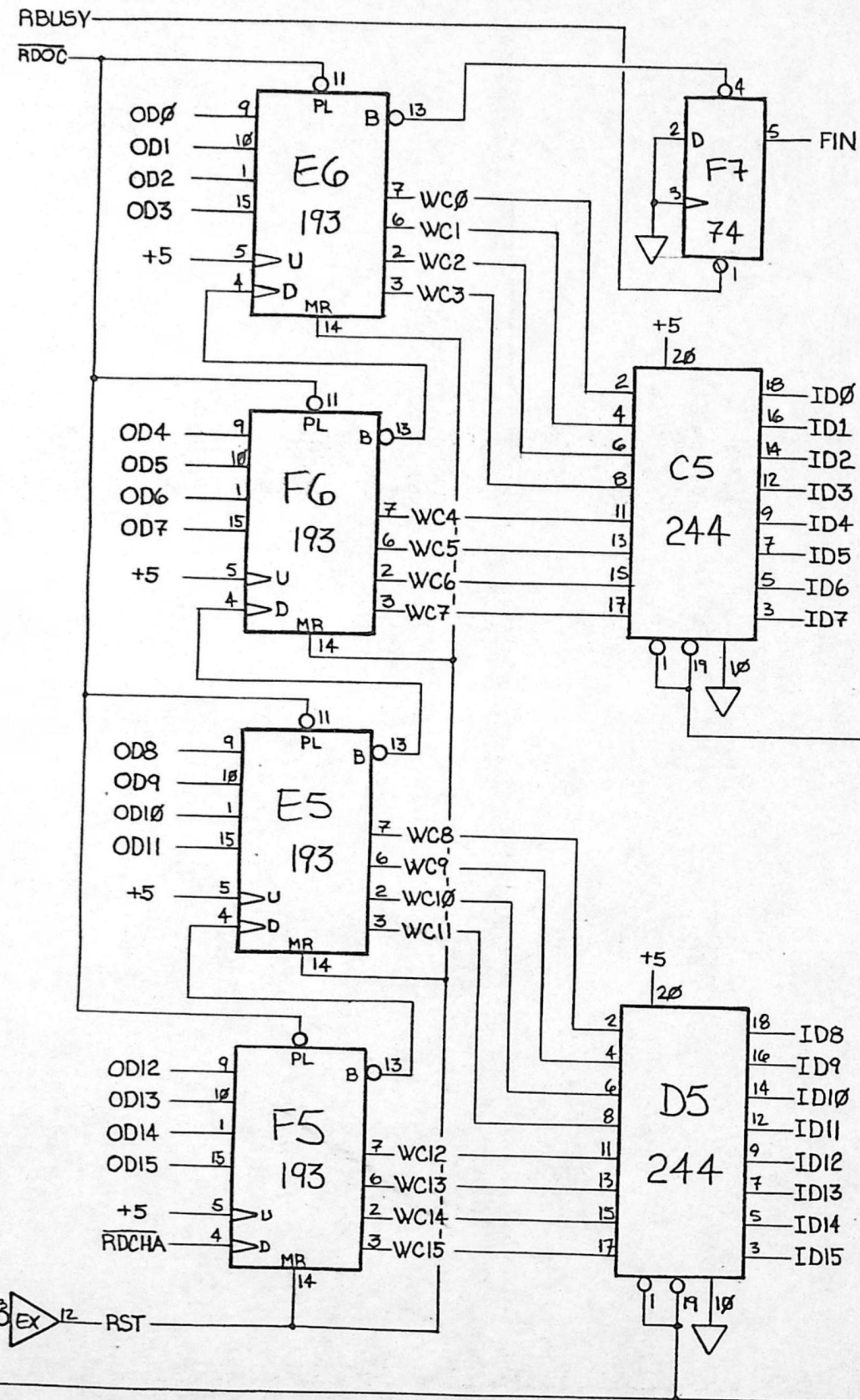
TOLERANCES UNLESS OTHERWISE SPECIFIED		FRACTIONS DEC ANGLES	
±	±	±	±
APPROVALS	DATE	DRAWN LPD 3/1/82	
CHECKED		SCALE	SIZE B
		DRAWING NO. CIC-W-60	

DO NOT SCALE DRAWING SHEET 6 OF 9

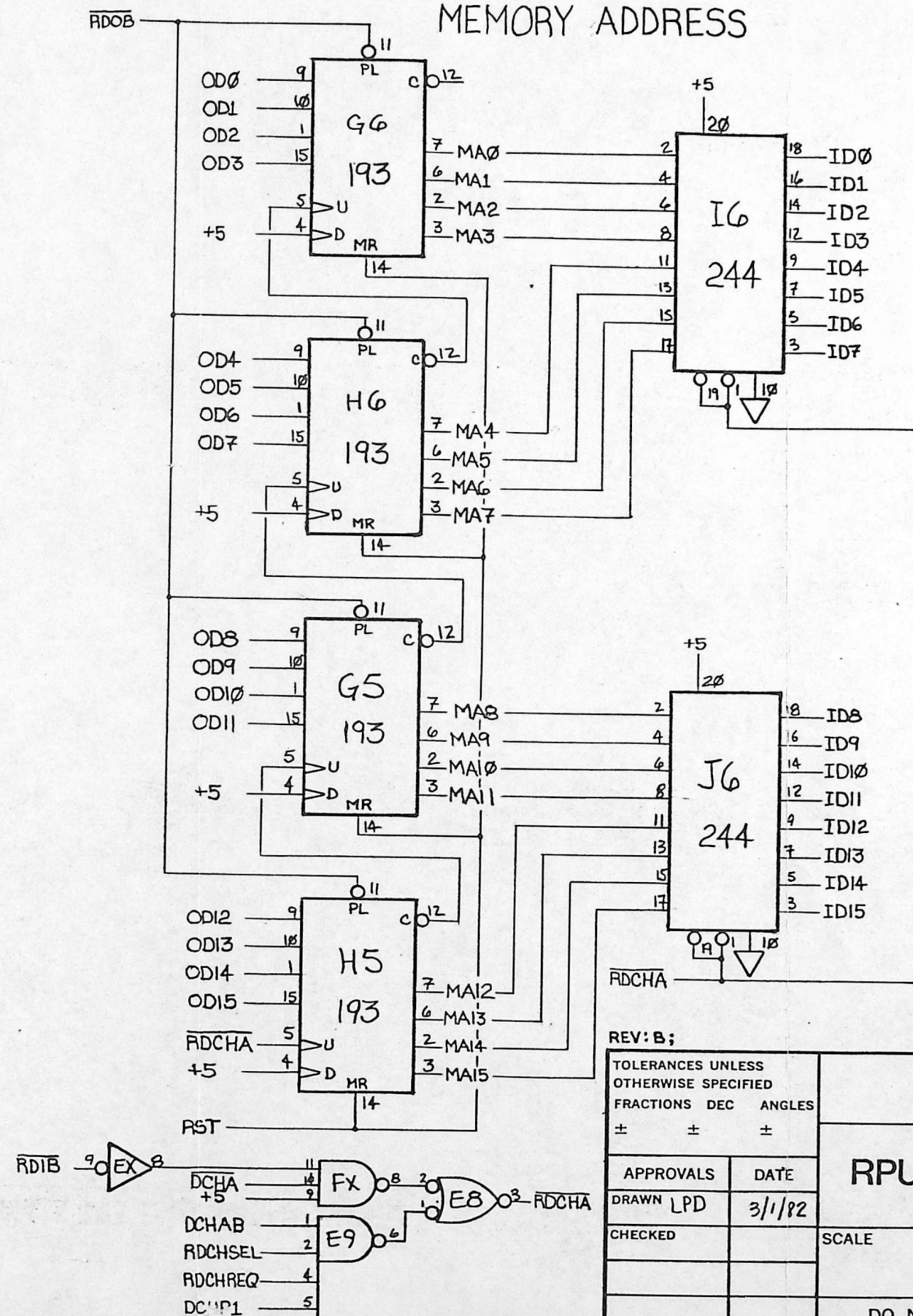
WORD COUNT

REVISIONS

LTR	DESCRIPTION	DATE	APPROVED



MEMORY ADDRESS



REV: B;

TOLERANCES UNLESS
OTHERWISE SPECIFIED
FRACTIONS DEC ANGLES
± ± ±

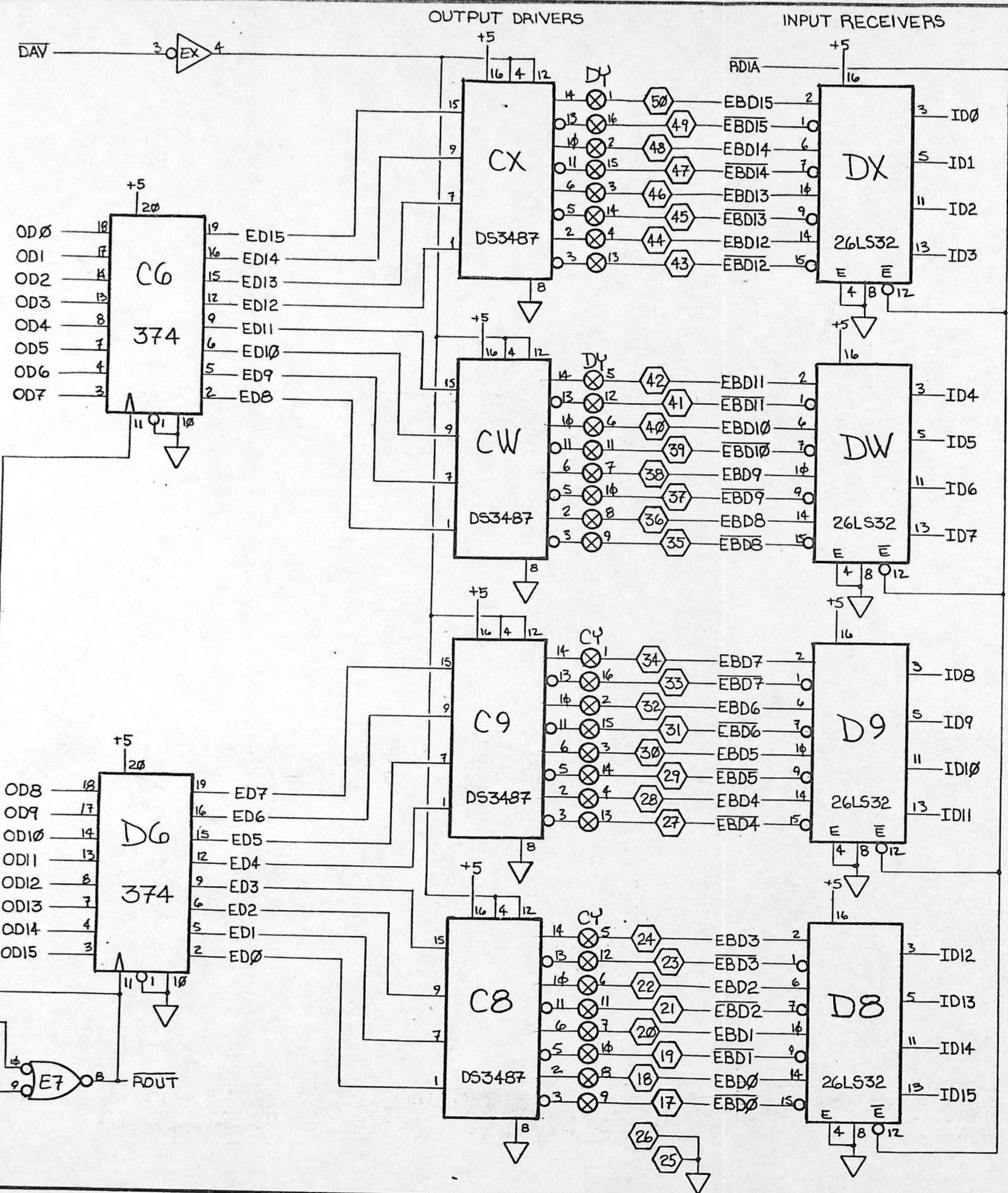
APPROVALS	DATE
DRAWN LPD	3/1/82
CHECKED	

ATA Corp.

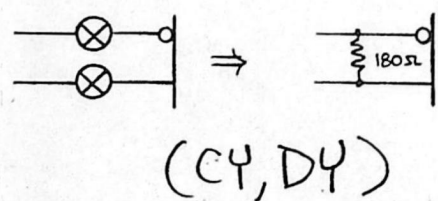
COMPUTER IMAGE
RPU/MAG TAPE INTERFACE

RPU DMA Counters

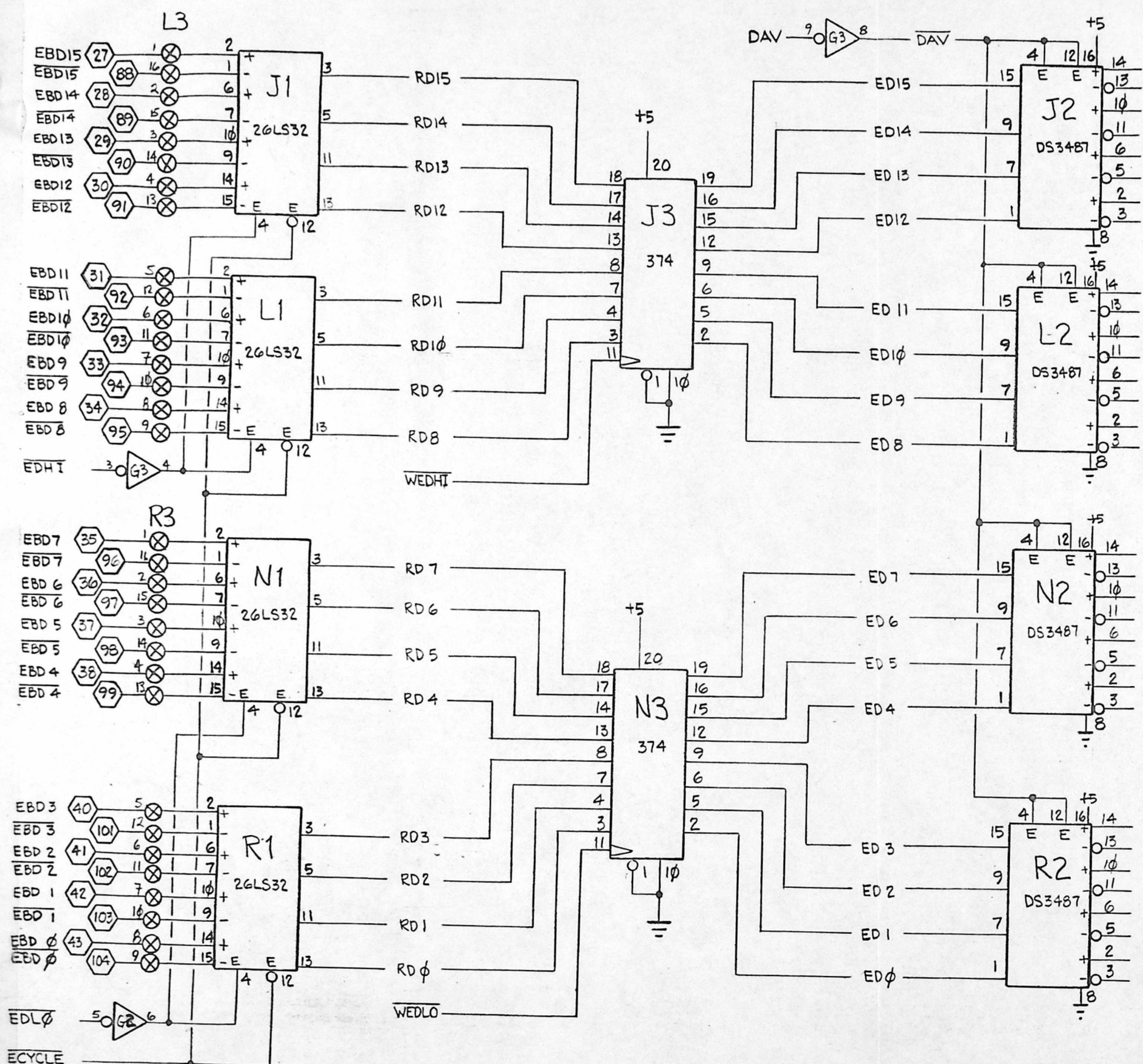
SCALE	SIZE B	DRAWING NO. CIC-W-60
DO NOT SCALE DRAWING	SHEET 8 OF 9	



REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



REV: B;			
TOLERANCES UNLESS OTHERWISE SPECIFIED			
FRACTIONS DEC ANGLES			
± ± ±			
APPROVALS	DATE	ATA Corp. COMPUTER IMAGE RPU/MAG TAPE INTERFACE RPU Drivers and Receivers	
DRAWN LPD	3/1/12		
CHECKED		SCALE	SIZE
			B
		DRAWING NO.	CIC-W-60
DO NOT SCALE DRAWING		SHEET 9 OF 9	



REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

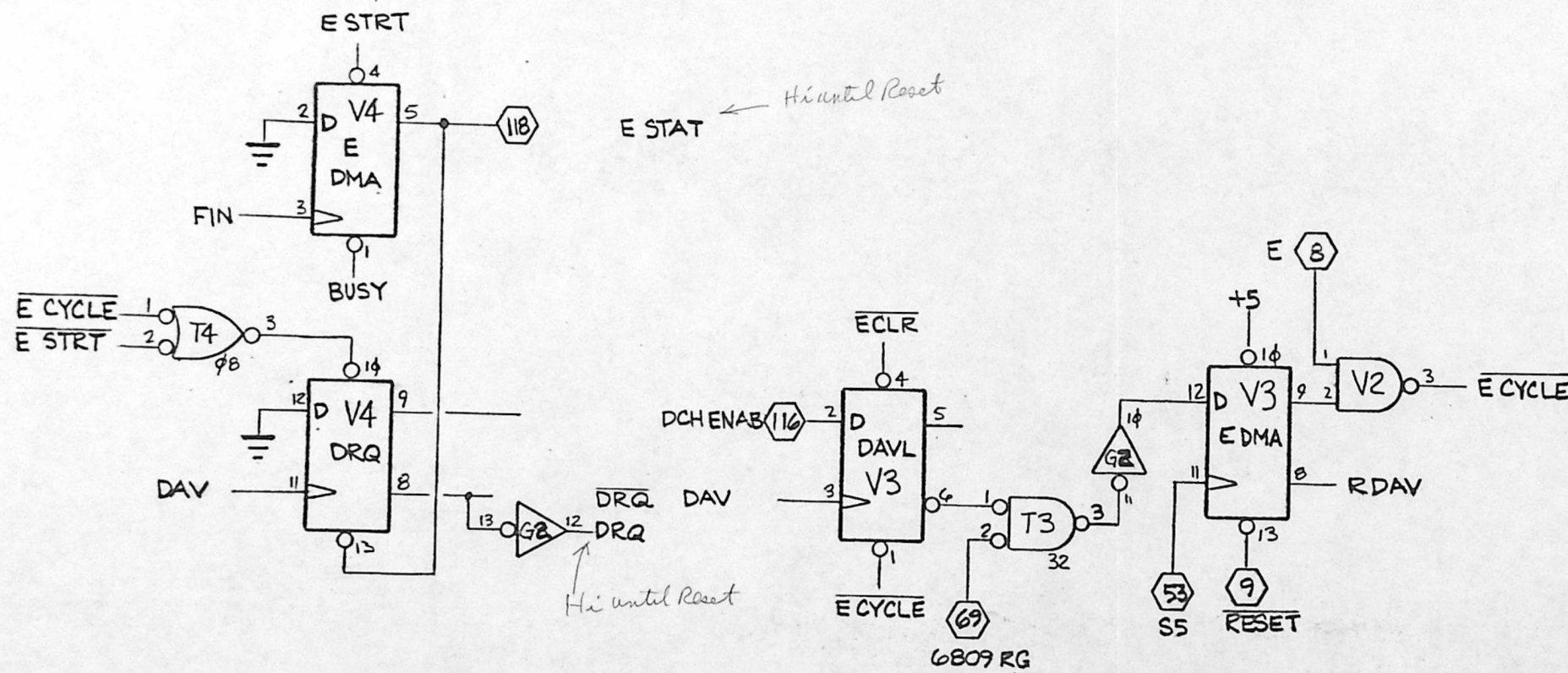
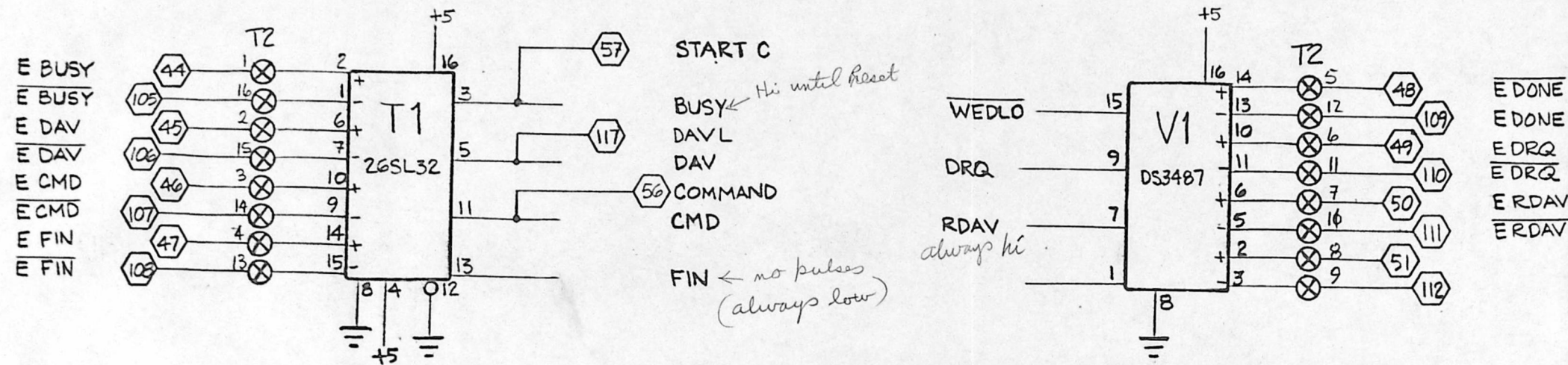
- EBD 15
- EBD 15
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- EBD 13
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- EBD 11
- EBD 10
- EBD 10
- EBD 9
- EBD 9
- EBD 8
- EBD 8
- EBD 7
- EBD 7
- EBD 6
- EBD 6
- EBD 5
- EBD 5
- EBD 4
- EBD 4
- EBD 3
- EBD 3
- EBD 2
- EBD 2
- EBD 1
- EBD 1
- EBD 0
- EBD 0

REV:A;

TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
±	±	±
APPROVALS	DATE	
DRAWN	3/10/82	
CHECKED		

ATA Corp.			
COMPUTER IMAGE			
RPU/ECLIPSE INTERFACE			
DATA BUFFERS			
SCALE	SIZE	DRAWING NO.	
	B	CIC-W-61	
DO NOT SCALE DRAWING			SHEET 2 OF 3

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

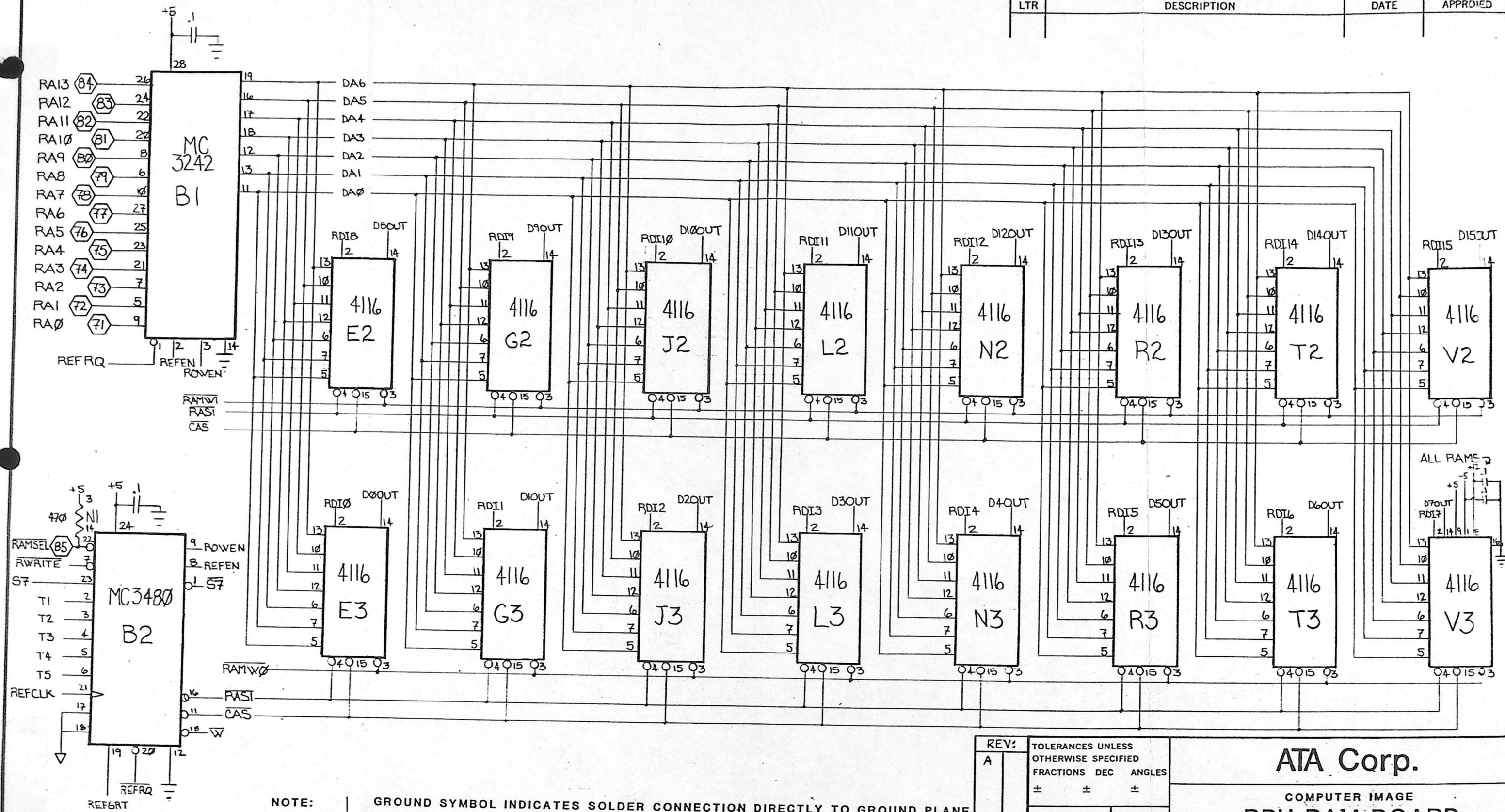


REV:A;

TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
±	±	±
APPROVALS	DATE	
DRAWN	3-1-87	
CHECKED		

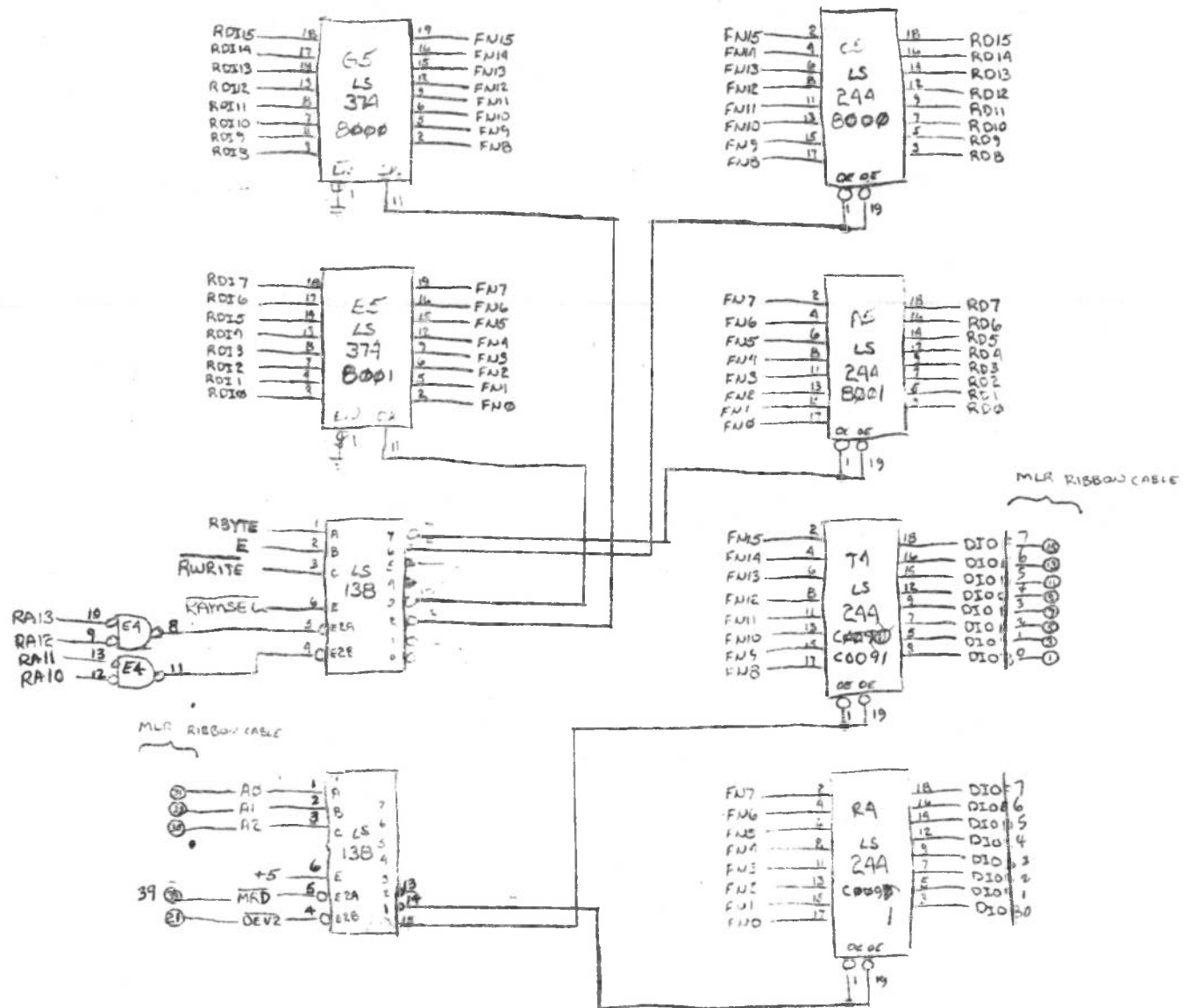
ATA Corp.			
COMPUTER IMAGE			
RPU/ECLIPSE INTERFACE			
Handshake Signals			
SCALE	SIZE	DRAWING NO.	
	B	CIC-W-61	
DO NOT SCALE DRAWING			SHEET 3 OF 3

REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



NOTE: GROUND SYMBOL INDICATES SOLDER CONNECTION DIRECTLY TO GROUND PLANE

REV: A	TOLERANCES UNLESS OTHERWISE SPECIFIED		
	FRACTIONS	DEC	ANGLES
	±	±	±
	APPROVALS	DATE	
DRAWN	LPD	2/13/81	
	CHECKED	REV A 9/30/81	
ATA Corp.		COMPUTER IMAGE	
RPU RAM BOARD		Rams & Controllers	
SIZE B		DRAWING NO. CIC-W-63	
DO NOT SCALE DRAWING		SHEET 1 OF 2	



MLR

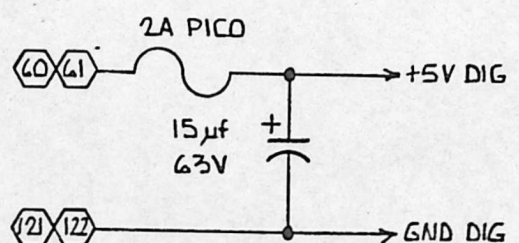
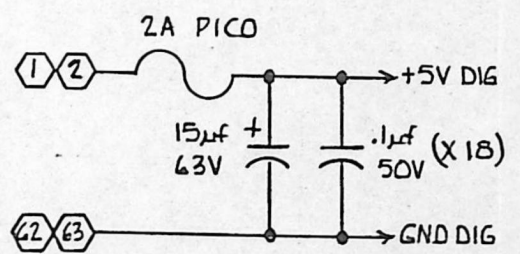
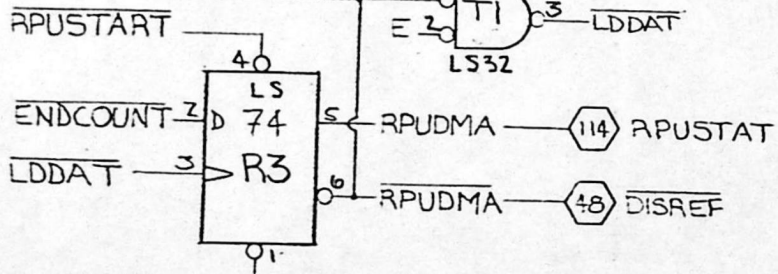
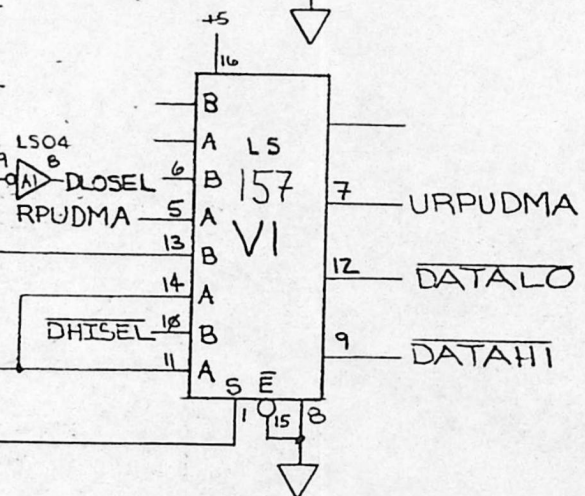
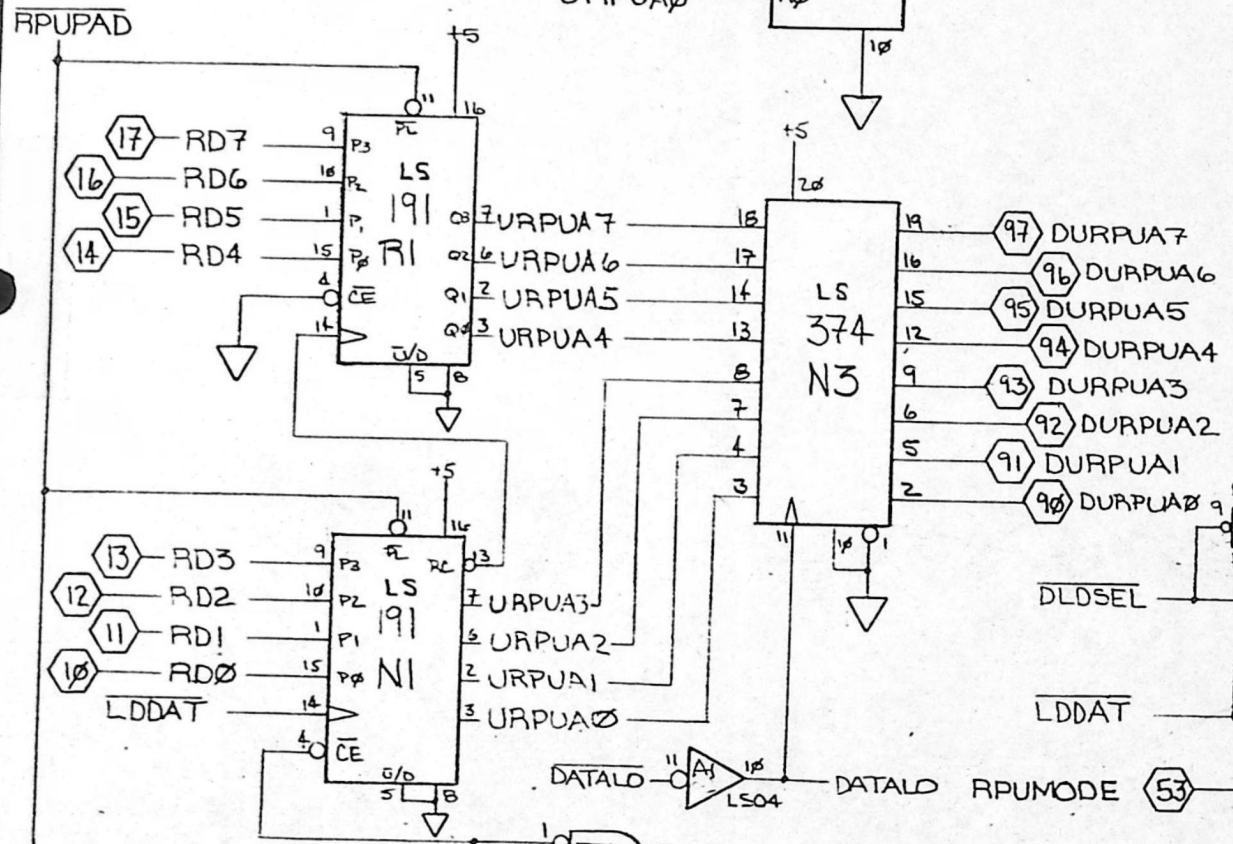
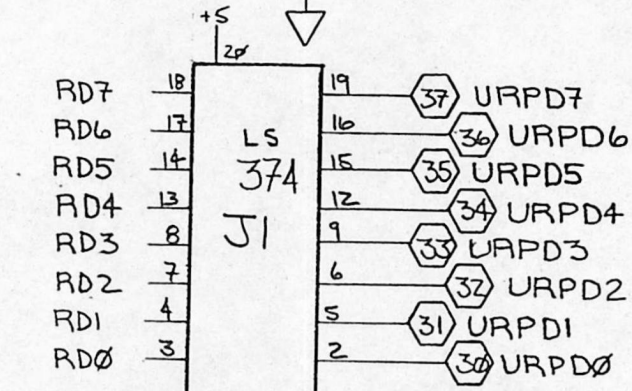
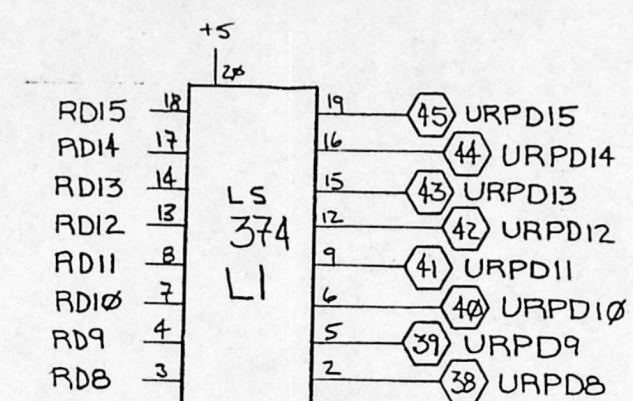
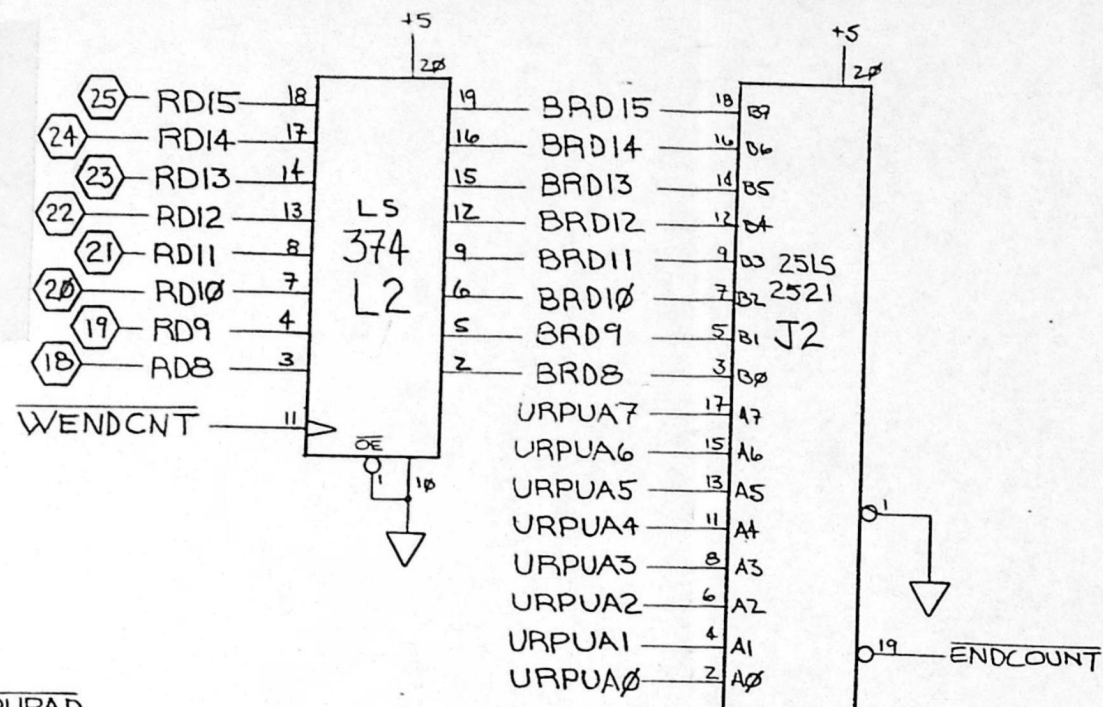
CIC-W-63

RPU RAM BOARD

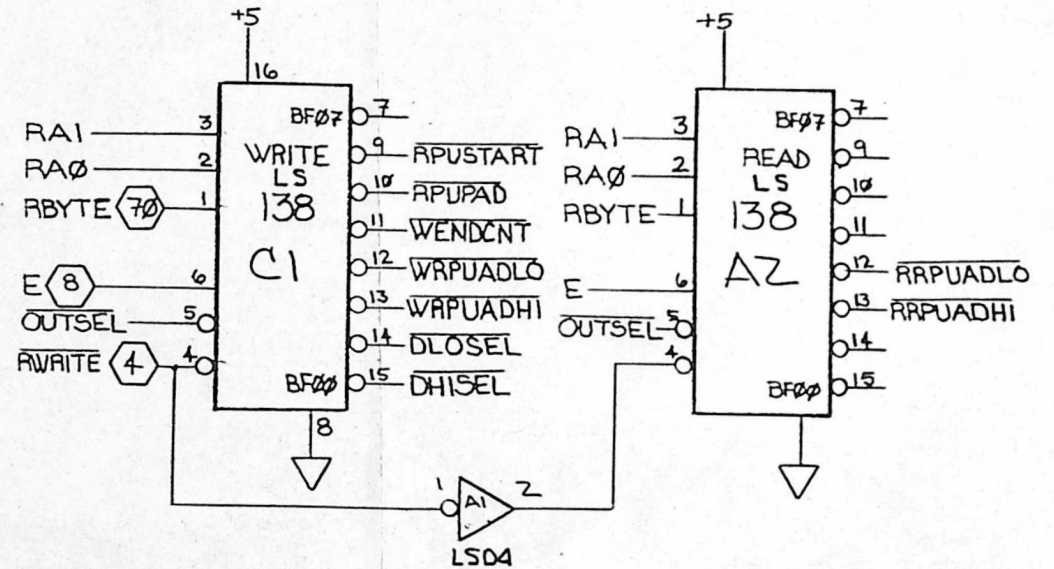
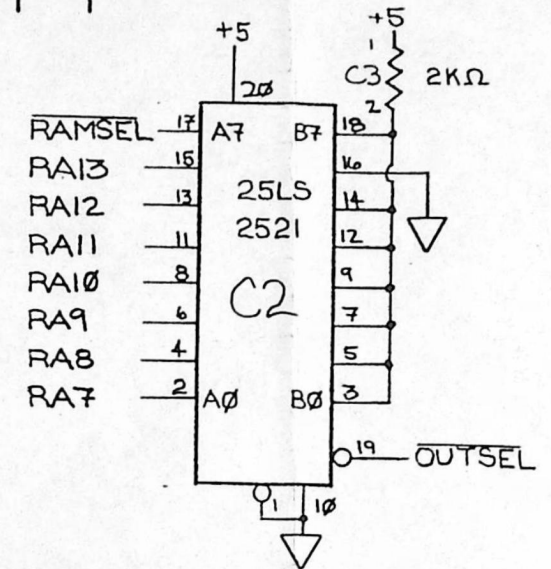
FRAME NUMBER BUFFER

SH3 of 3

9/30/85 GSW
10/5/85 JSM



REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



REV: A;			
TOLERANCES UNLESS OTHERWISE SPECIFIED			
FRACTIONS	DEC	ANGLES	
±	±	±	
APPROVALS	DATE		
DRAWN LPD	7/28/81		
CHECKED	REV A	SCALE	SIZE
			DRAWING NO.
			CIC-W-64

ATA CORP.

COMPUTER IMAGE

RPU OUTPUT CARD

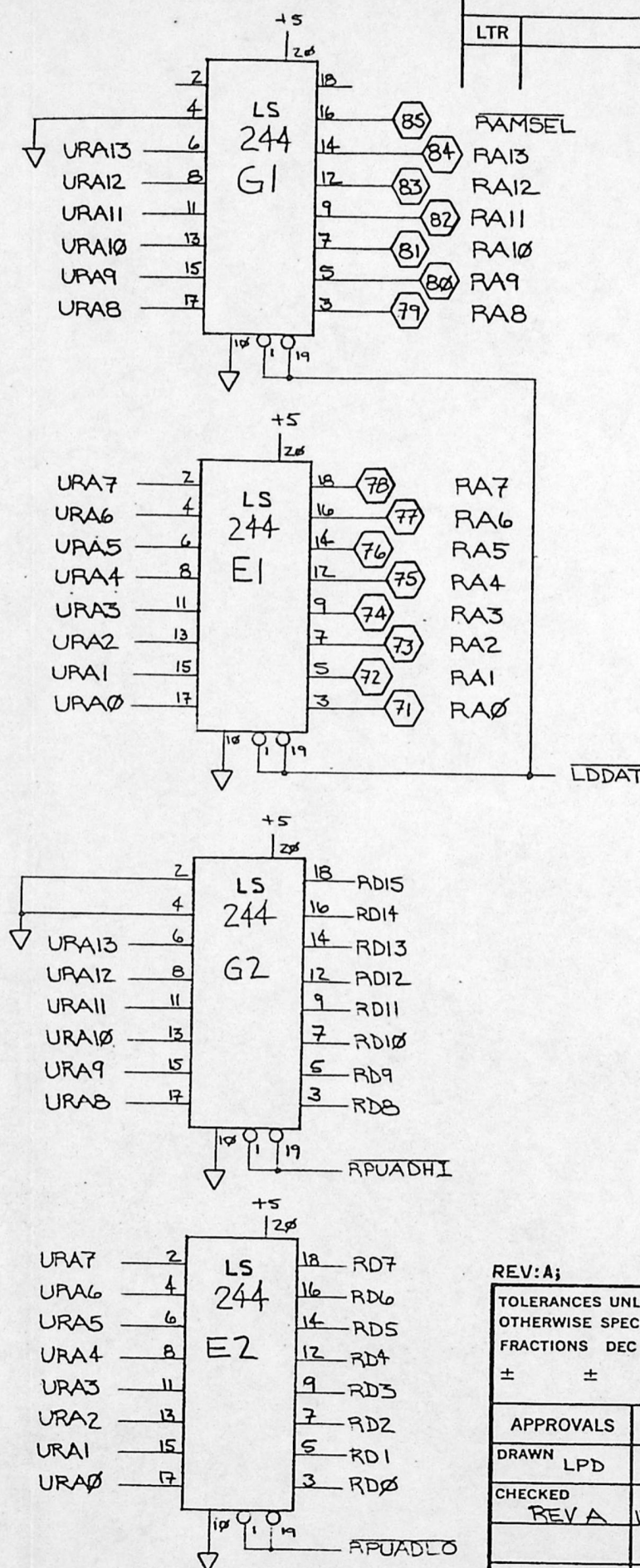
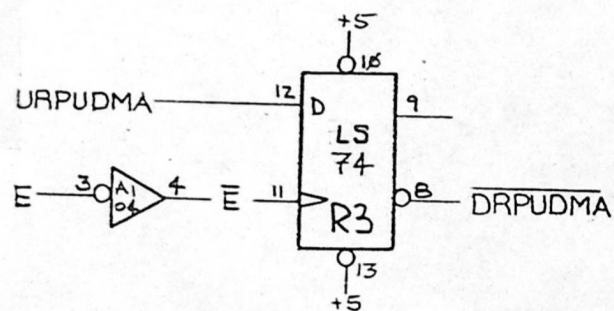
Parameter Address

B

CIC-W-64

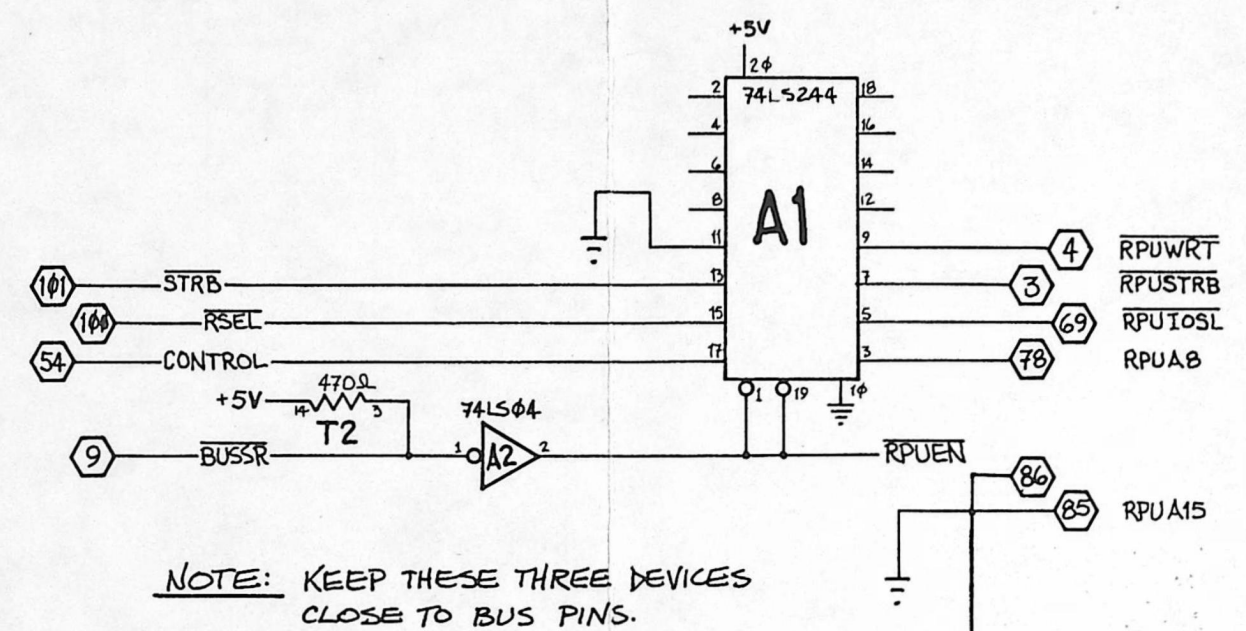
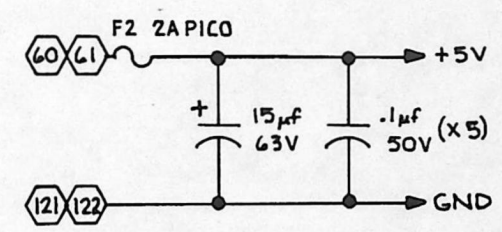
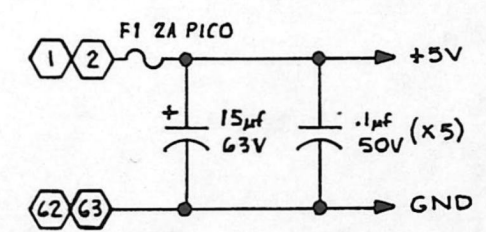
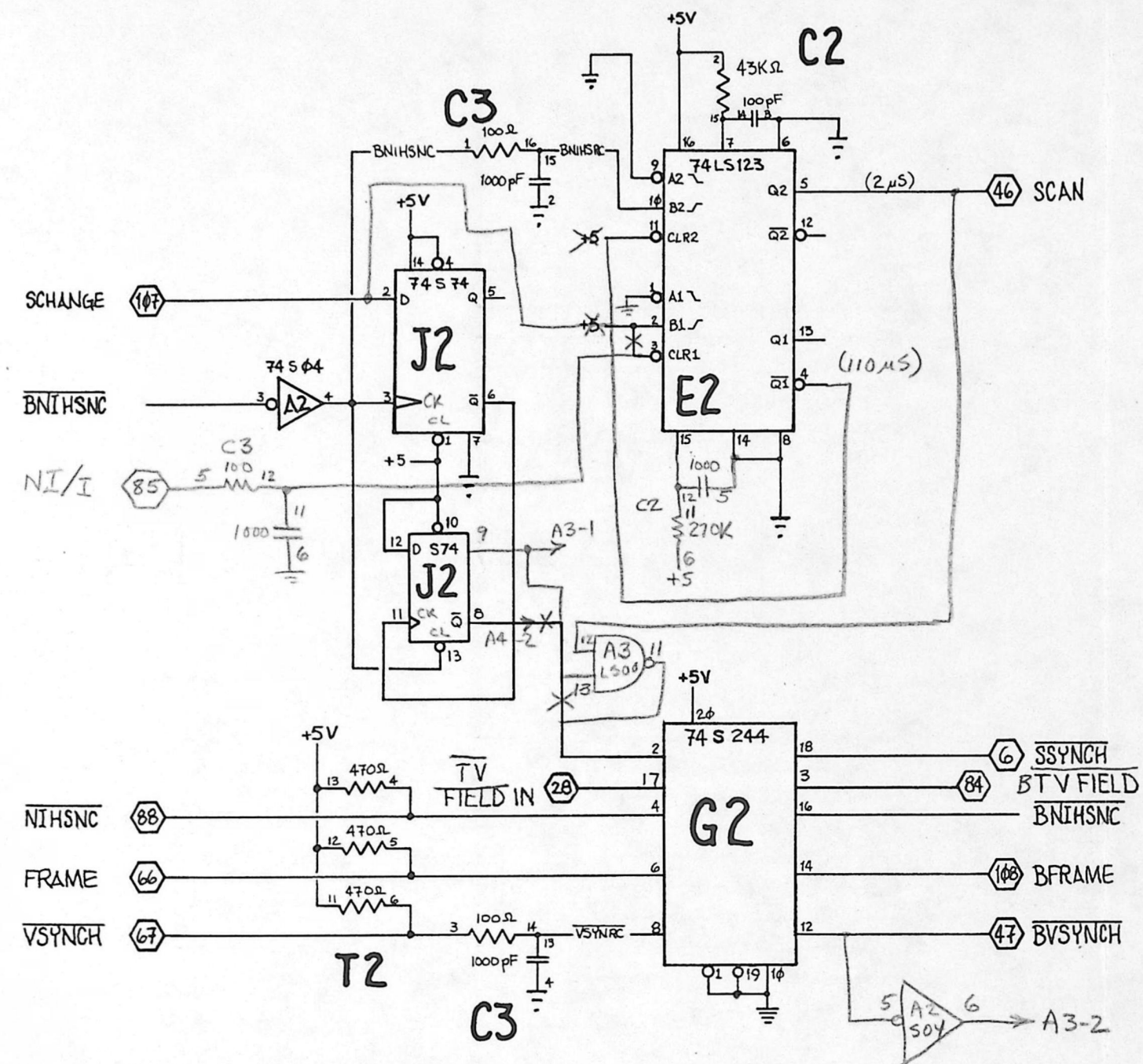
DO NOT SCALE DRAWING

SHEET 1 OF 2

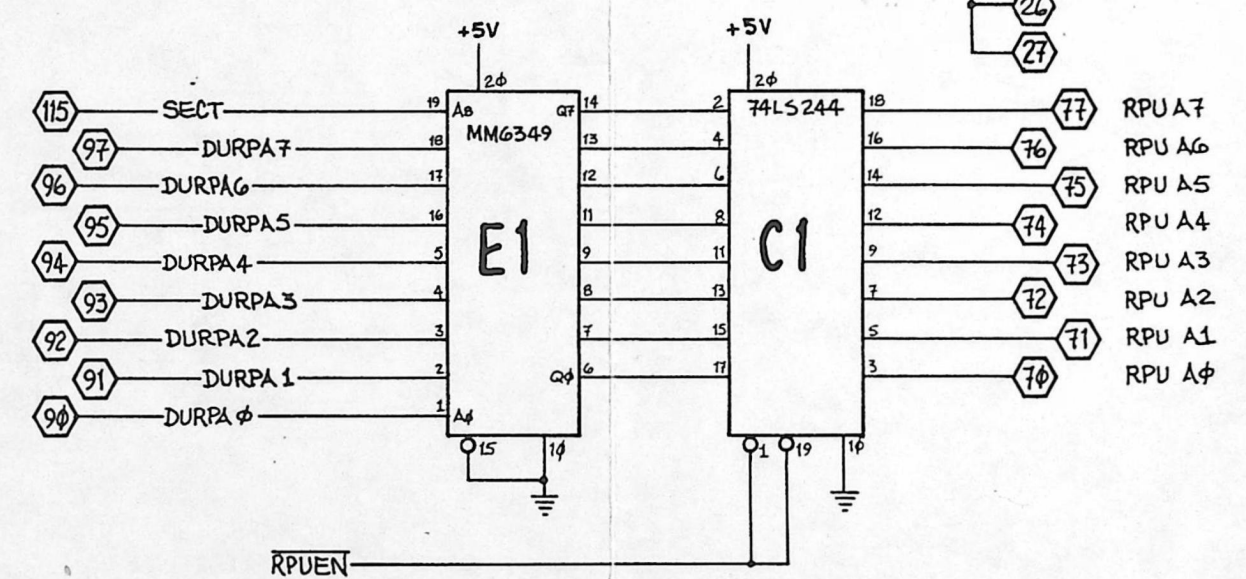


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REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED

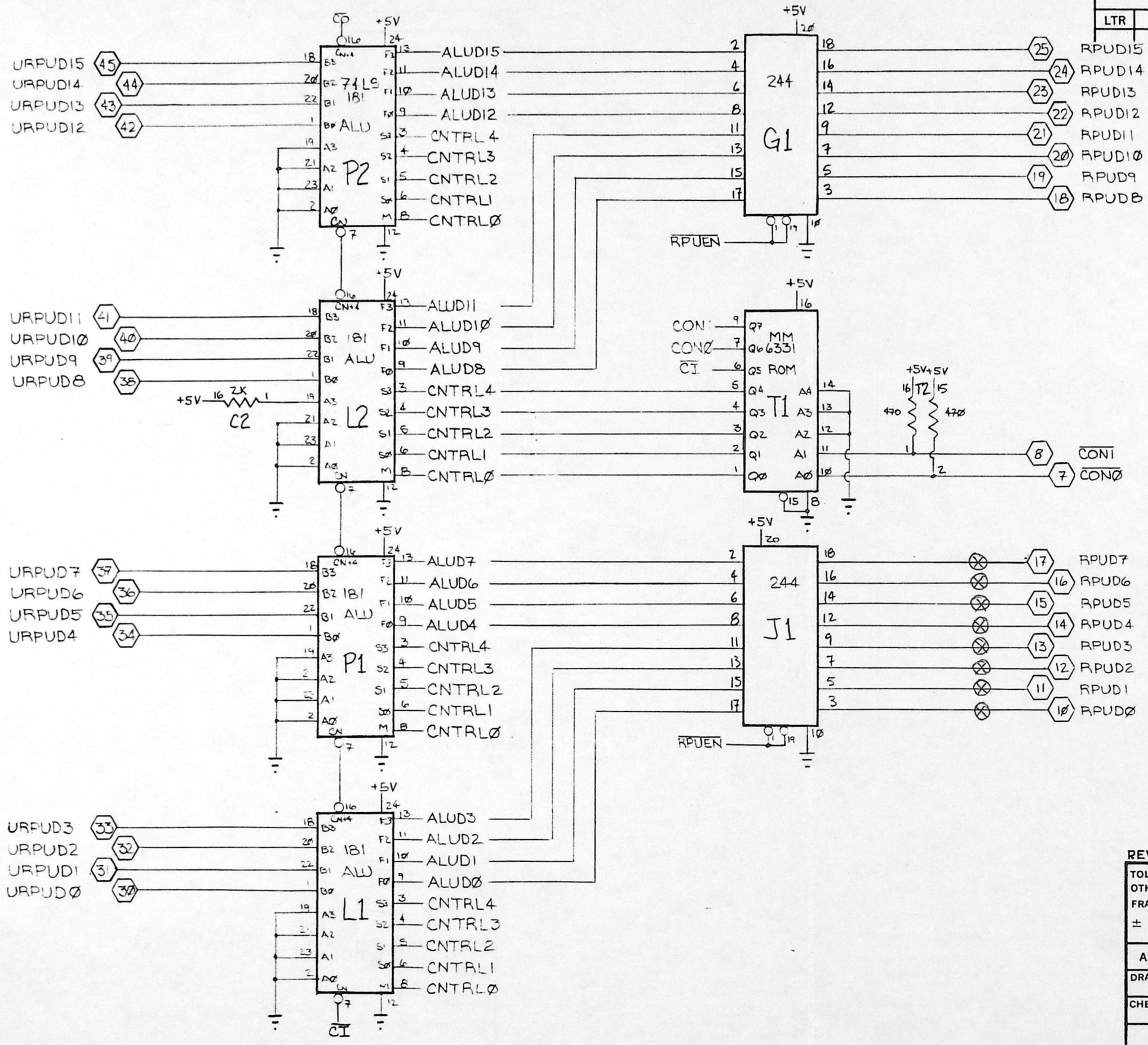


NOTE: KEEP THESE THREE DEVICES CLOSE TO BUS PINS.



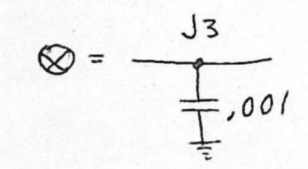
REV: D; D1;		
TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
±	±	±
APPROVALS	DATE	
DRAWN	4-13-82	
CHECKED		

ATA Corp.			
COMPUTER IMAGE			
RPU DRIVER CARD			
ADDRESSING & CONTROL			
SCALE	SIZE	DRAWING NO.	
	B	CIC-W-65	
DO NOT SCALE DRAWING			SHEET 1 OF 2



REVISIONS

LTR	DESCRIPTION	DATE	APPROVED
-----	-------------	------	----------



TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
±	±	±
APPROVALS	DATE	
DRAWN	LPD	
CHECKED		

ATA Corp.

COMPUTER IMAGE

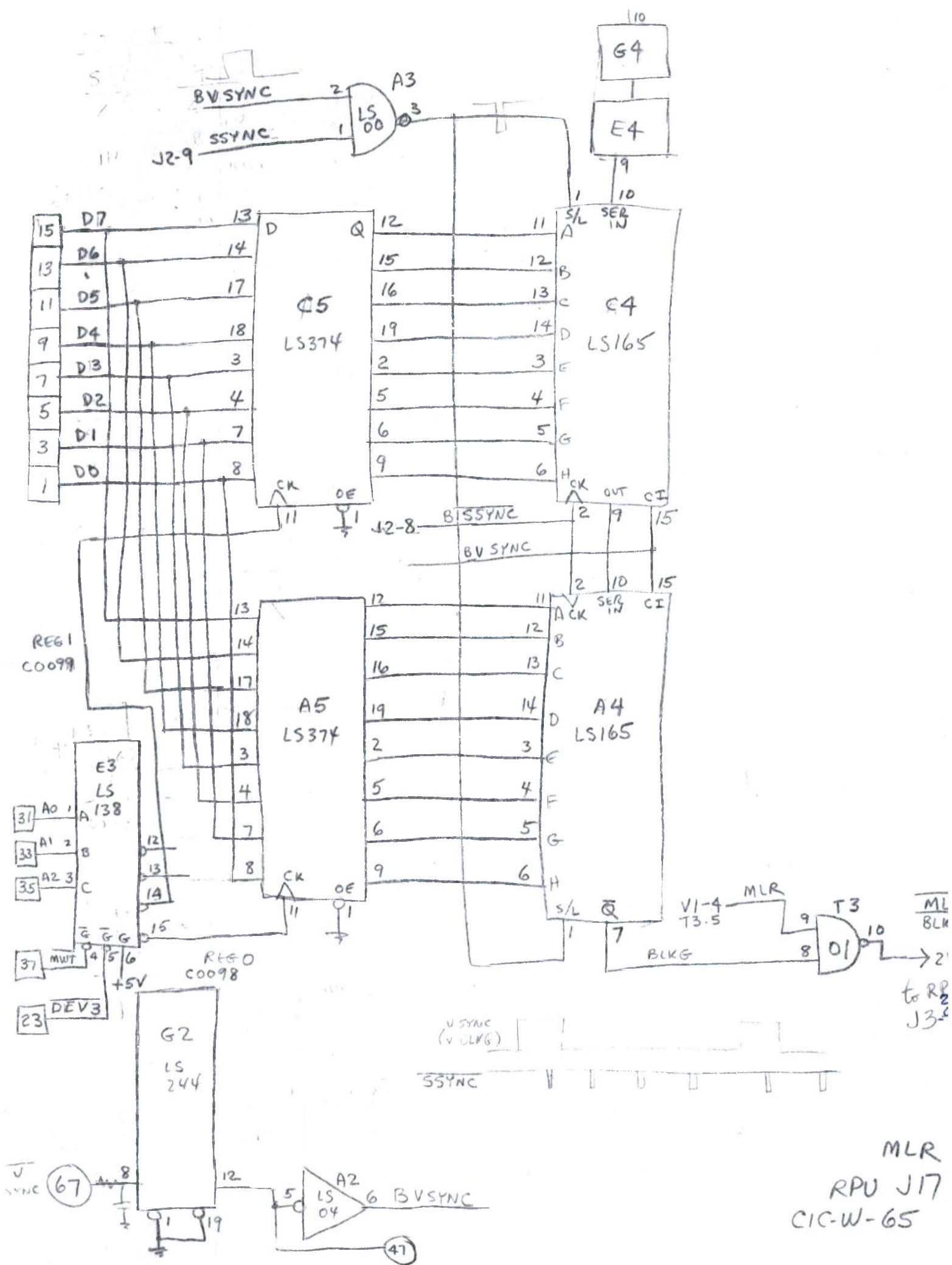
RPU DRIVER CARD

ALU & Addressing

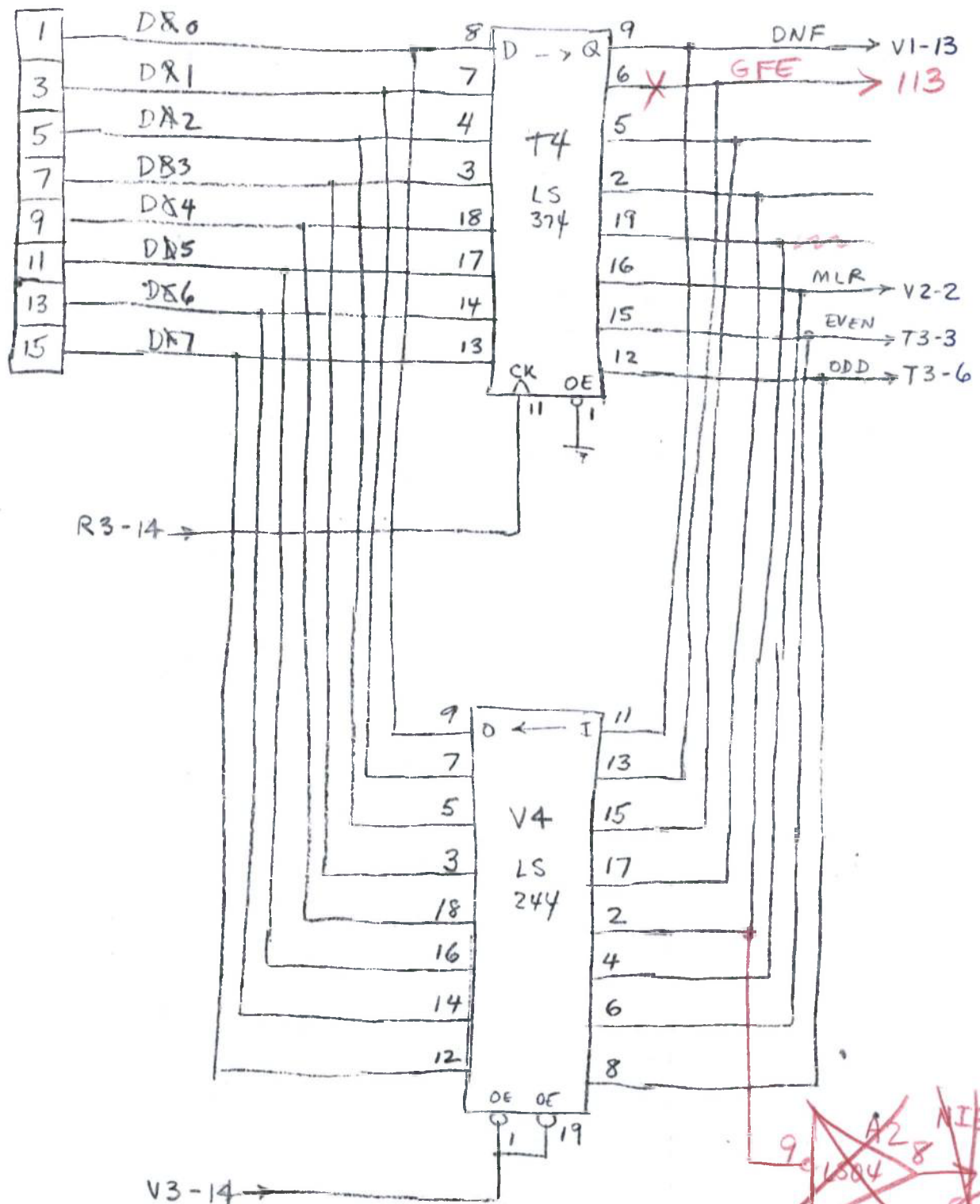
SCALE	SIZE	DRAWING NO.
	B	CIC-W-65

DO NOT SCALE DRAWING

SHEET 2 OF 2

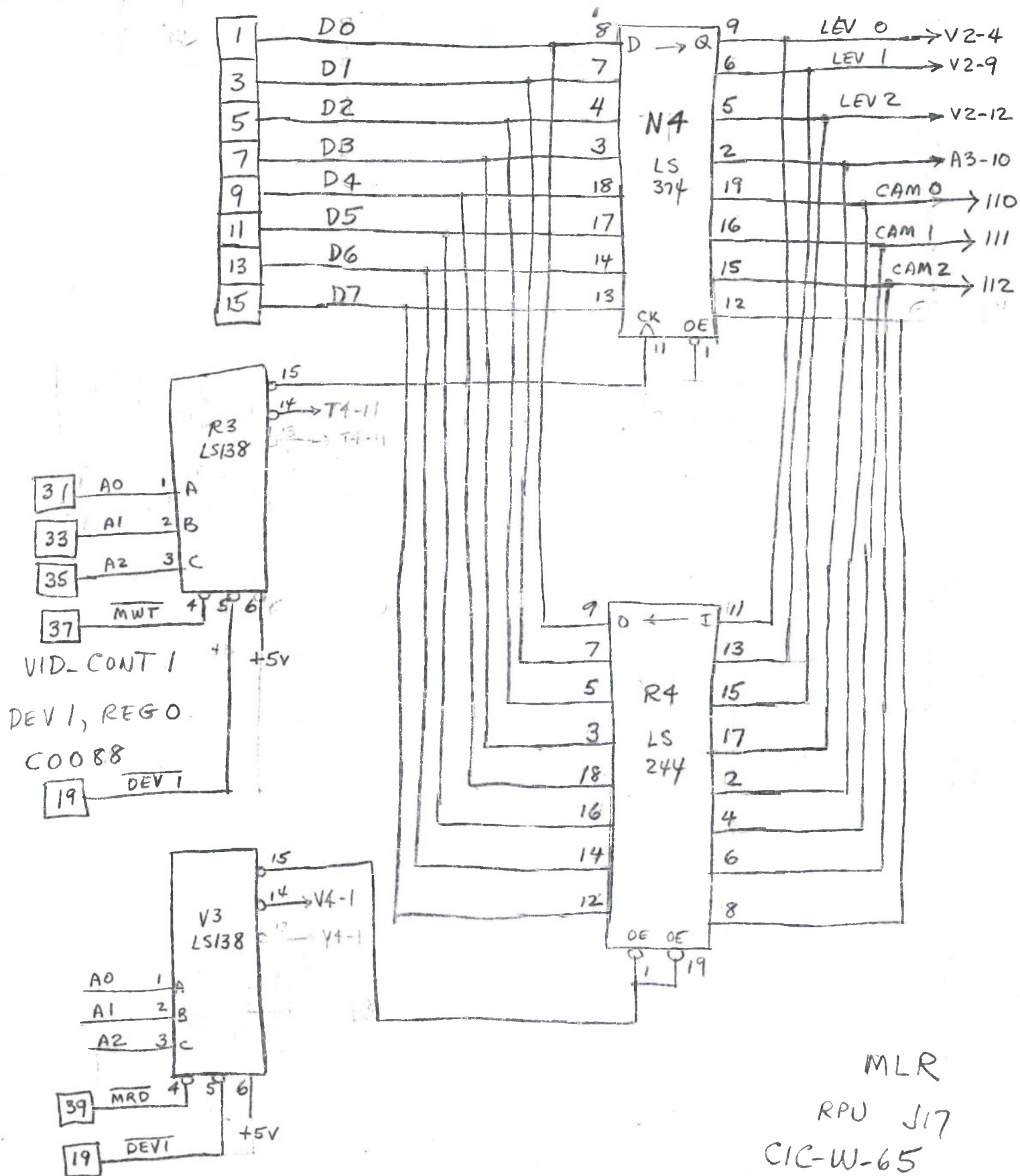


MLR
RPU J17
CIC-W-65



VID-CONT 2
 DEY 1, REG 1
 C0089

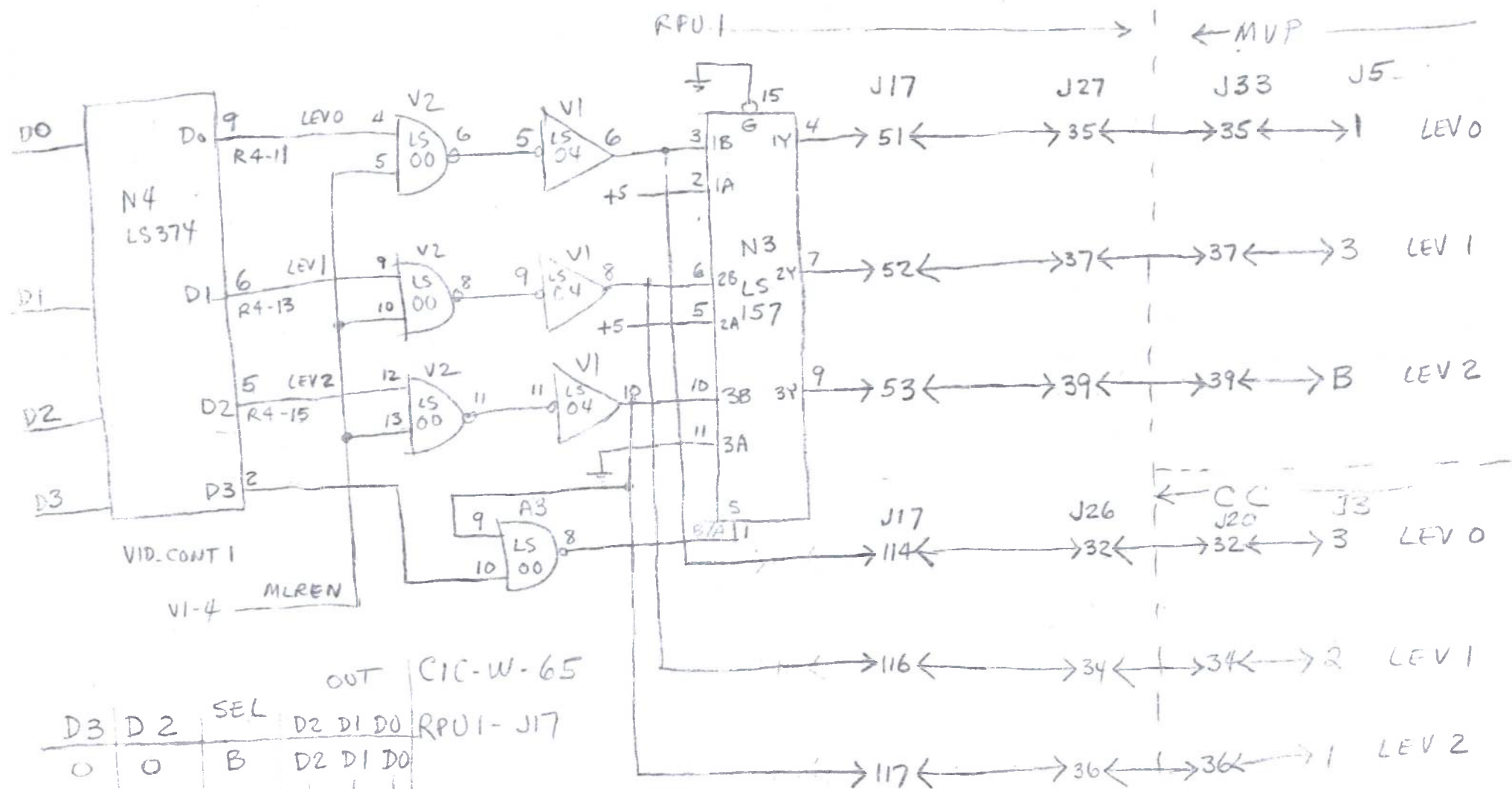
RPU J17
 CIC-W-65



MLR
RPU J17
CIC-W-65

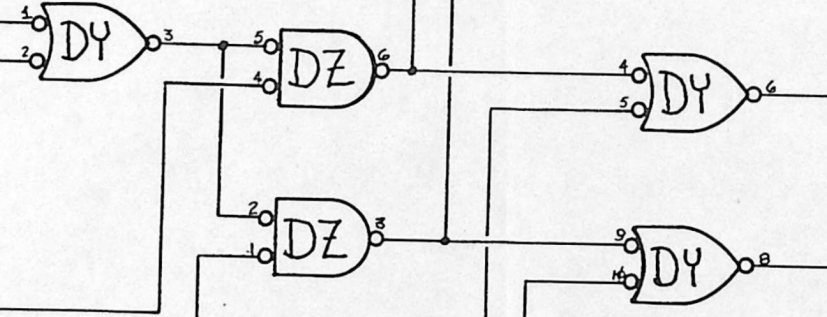
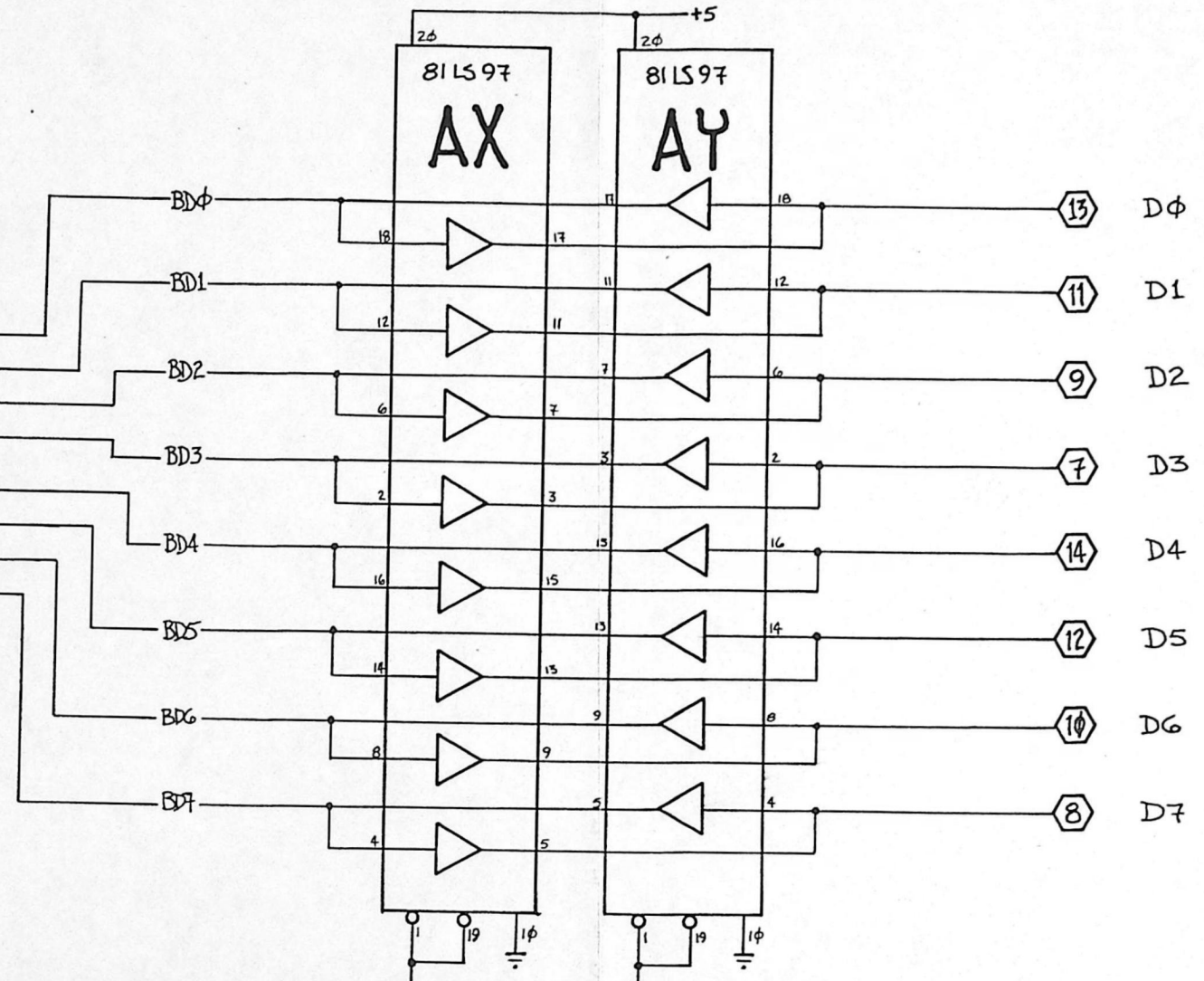
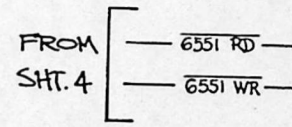
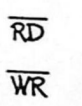
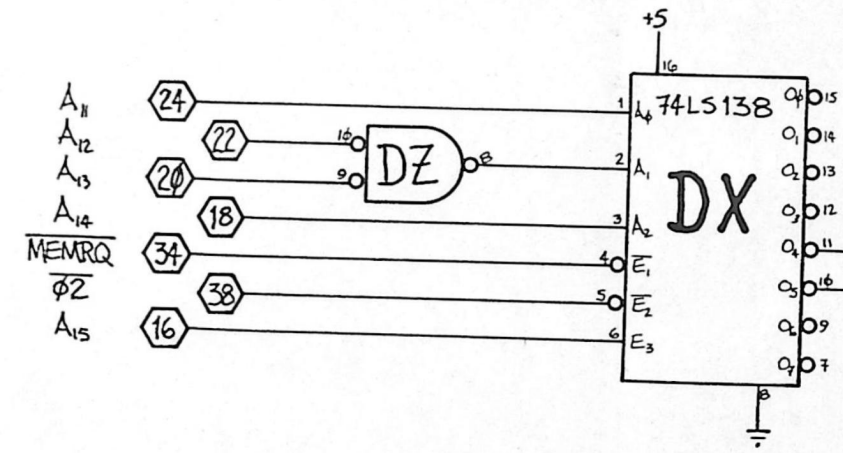
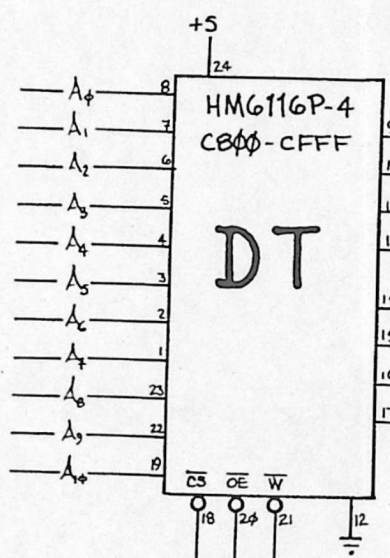
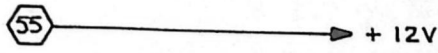
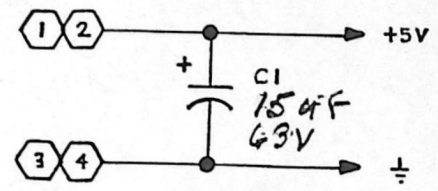
517

$$\begin{array}{r} 20 \\ 2.5 \\ 1.2 \\ \hline 23.7 \end{array}$$

MUR
RPU-1
J17

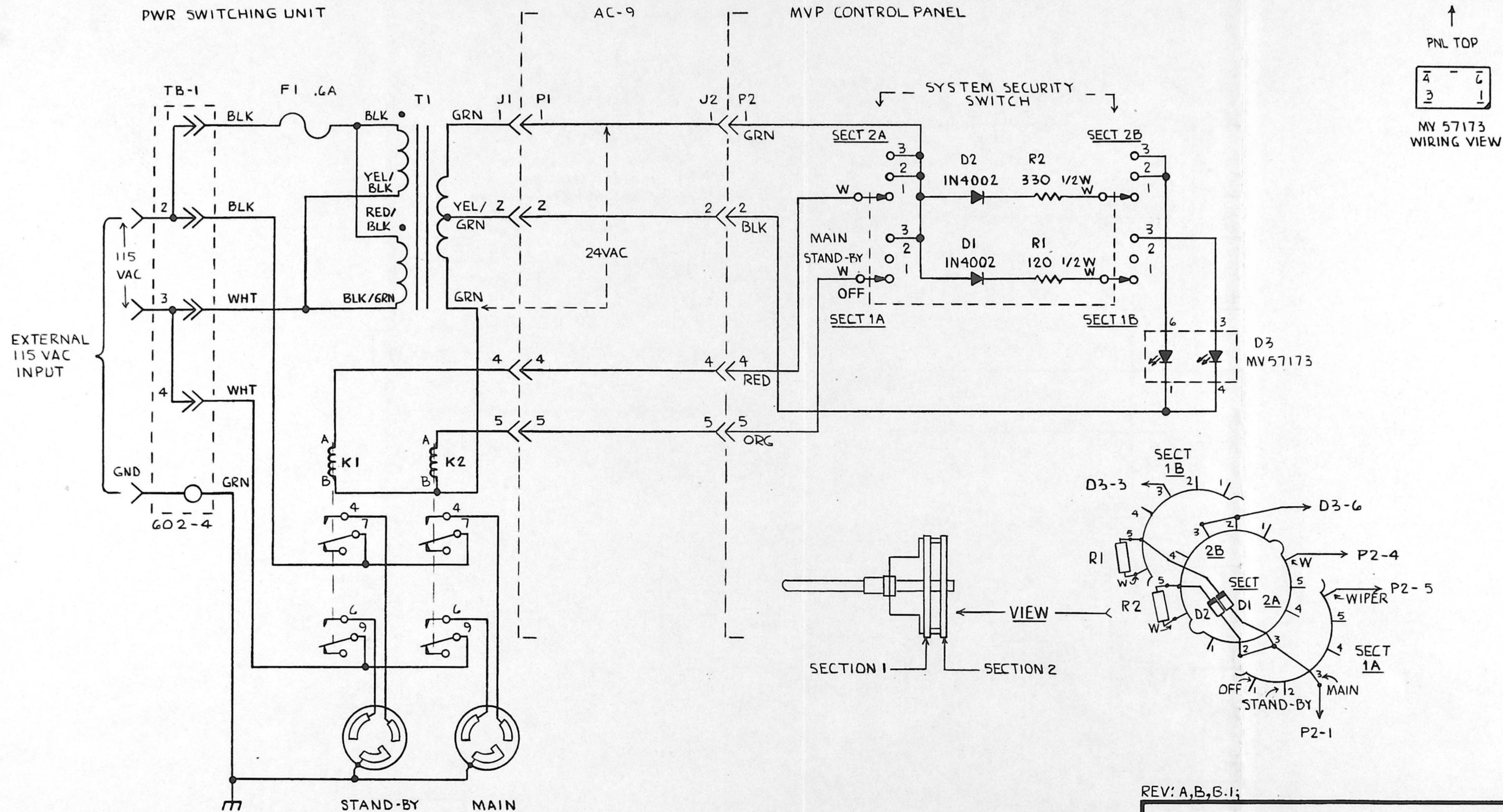
REVISIONS			
LTR	DESCRIPTION	DATE	APPROVED



REV: B;C

TOLERANCES UNLESS OTHERWISE SPECIFIED		
FRACTIONS	DEC	ANGLES
±	±	±
APPROVALS	DATE	
DRAWN SEP.	4-6-82	
CHECKED		

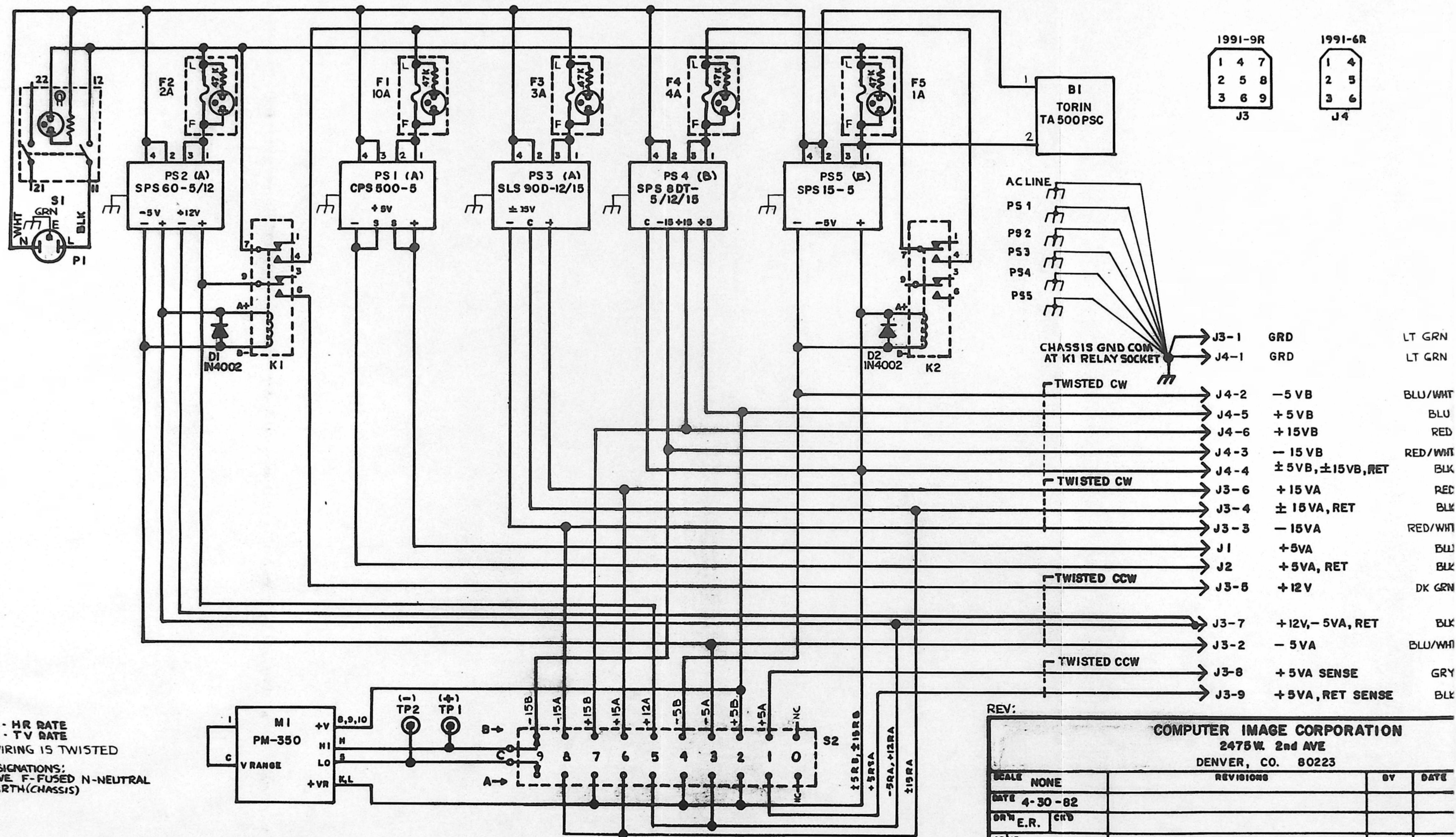
ATA Corp.			
COMPUTER IMAGE			
ECLIPSE/RPU COMMUNICATION			
CONTROL PANEL - SYSTEM IV			
SCALE	SIZE	DRAWING NO.	
	B	CIC-W-69	
DO NOT SCALE DRAWING			SHEET 2 OF 2



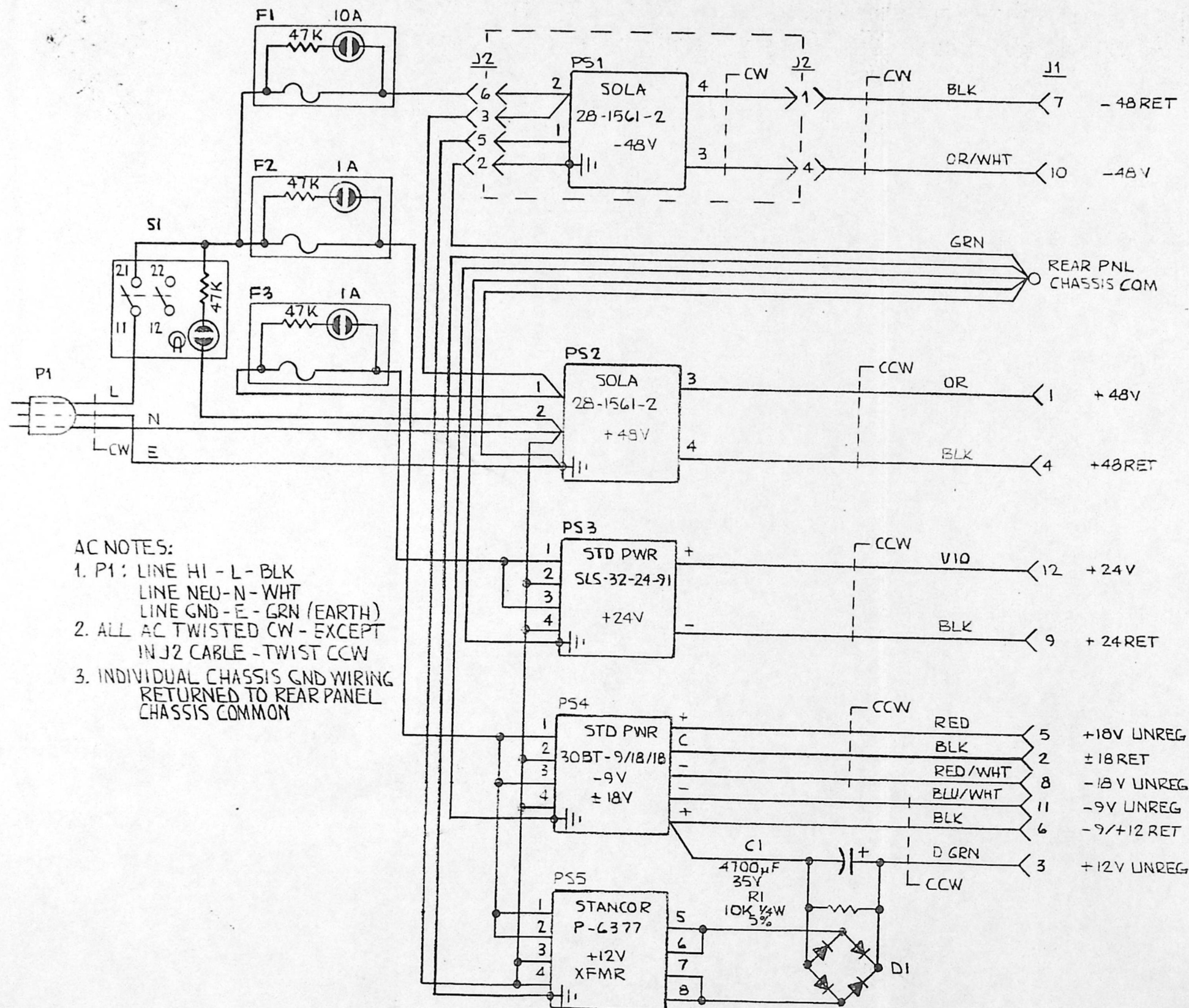
REV: A,B,B.1;

COMPUTER IMAGE CORP
2475 W 2ND AVE, DENVER, CO 80223

SCALE	REVISIONS	BY	DATE
DATE 11/4/81	ECN #46 DELETE K3 & J4	EVN	11/29/02
DRN EVL	CRD BA		
APVD			
TITLE POWER LINE SWITCHING UNIT MAIN POWER INPUT SYSTEM IV E. MVP CONTROL (115V)			NO WCIC-102-1



COMPUTER IMAGE CORPORATION			
2475 W. 2nd AVE			
DENVER, CO. 80223			
SCALE	NONE	REVISIONS	BY
DATE	4-30-82		
DRN	E.R.	CHKD	
APVD			
TITLE			NO
MAIN POWER SUPPLY, 110VAC			WCIC-105-1

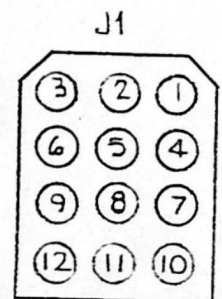


AC NOTES:

1. P1: LINE HI - L - BLK
LINE NEU - N - WHT
LINE GND - E - GRN (EARTH)
2. ALL AC TWISTED CW - EXCEPT
IN J2 CABLE - TWIST CCW
3. INDIVIDUAL CHASSIS GND WIRING
RETURNED TO REAR PANEL
CHASSIS COMMON

DC NOTES:

1. -48VPS WIRING TWISTED CW FOR MAGNETIC
FIELD CANCELLATION
2. ALL OTHER DC WIRING TWISTED CCW

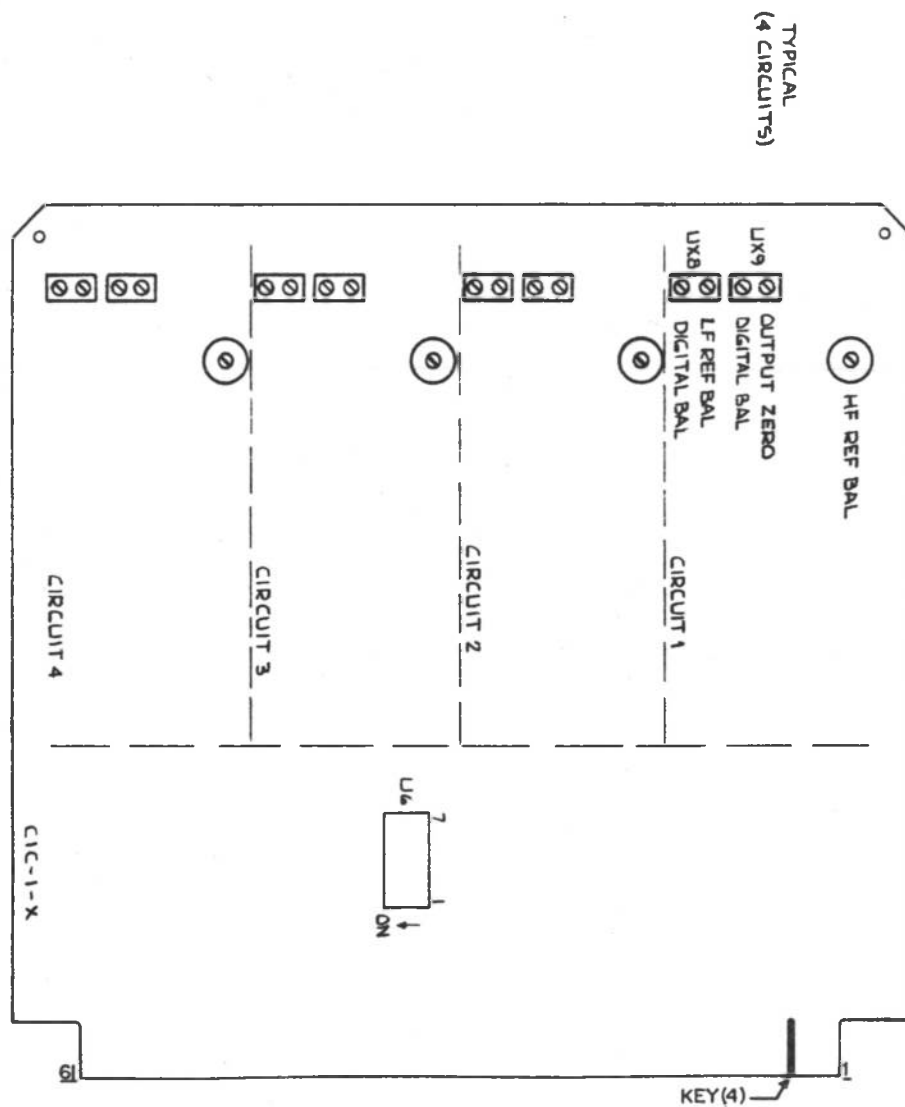


EXTERNAL REAR PNL VIEW

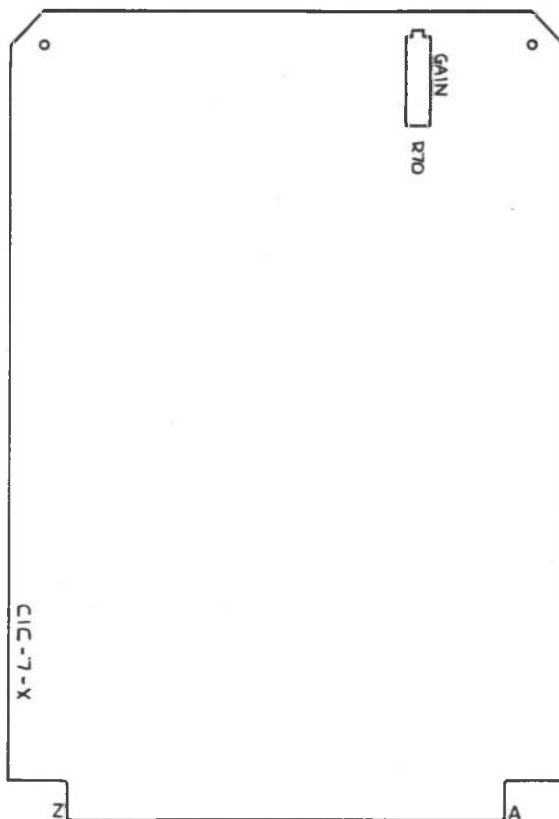
REV SCHEDULE: ORIG 6/1/83 EVL	COMPUTER IMAGE CORP
	SCAN CONVERTER PWR SUPPLY 110V/60 HZ SYSTEM IV NCIC-106-1

Computer Image wire color code

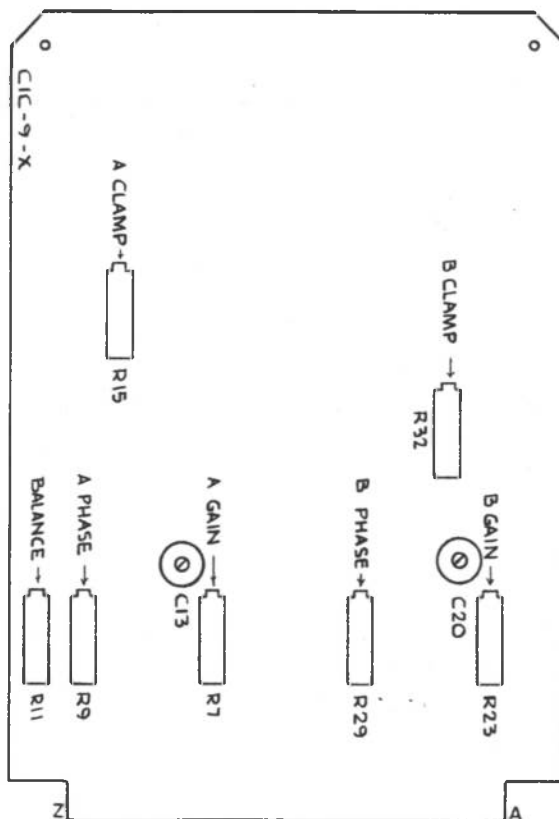
Function	color	AWG
+5VDC	BLUE	#6AWG, #18AWG, #28AWG (ww)
-5VDC	BLUE/WHT	#18AWG
+6VDC (6.3V)	BROWN	#18AWG
+12VDC	GREEN	#18AWG, #28AWG (ww)
+15VDC	RED	#18AWG, #28AWG (ww)
-15VDC	RED/WHT	#18AWG, #28AWG (ww)
+24VDC	VIOLET	#18AWG
+36VDC	YELLOW	#18AWG
-36VDC	YELLOW/WHT	#18AWG
+48VDC	ORANGE	#18AWG
-48VDC	ORANGE/WHT	#18AWG
GROUND (Digital)	BLACK	#6AWG, #18AWG, #28AWG (ww)
GROUND (Analog)	BLACK/WHT.	#18AWG, #28AWG (ww)
AC HI	BLACK	#16AWG
AC LO	WHITE	#16AWG
AC GROUND	GREEN	#16AWG
Hook up wire	WHITE	#22AWG
	GRAY	#22AWG
	YELLOW	#22AWG, #28AWG (ww)



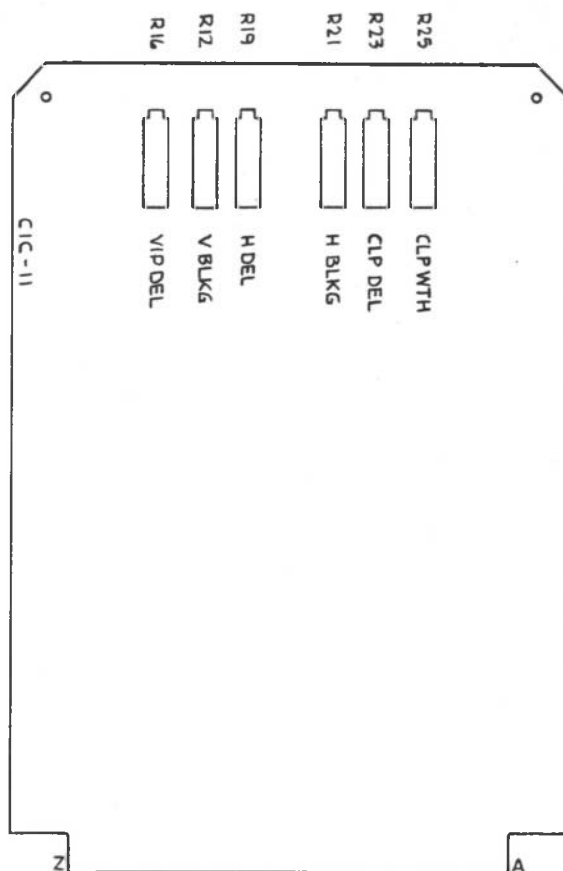
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	BY DATE
DATE	11/30/84		
DRN	EVL		
AP'VD			
TITLE SET-UP PROCEDURE CIC-1-X 12 BIT MDAC		NO SH 1 OF 1 FCIC-42	



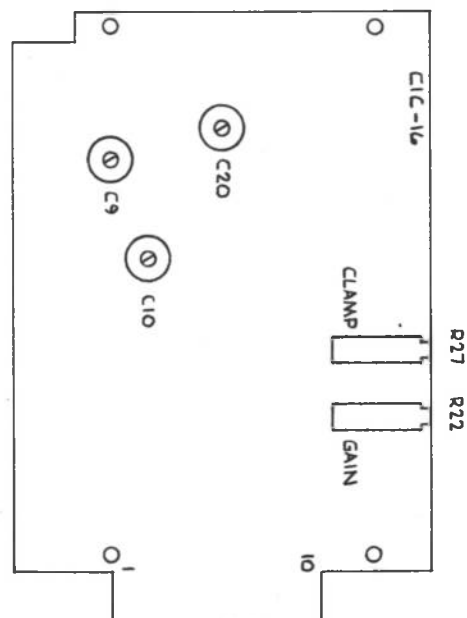
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	DATE
DATE	12/3/84		
DR	EVL	CHKD	
AP	VD		
TITLE SET-UP PROCEDURE CIC-7-X 4 X 1 VID AMP/SWITCH		SH 1 OF 1 FCIC-43	



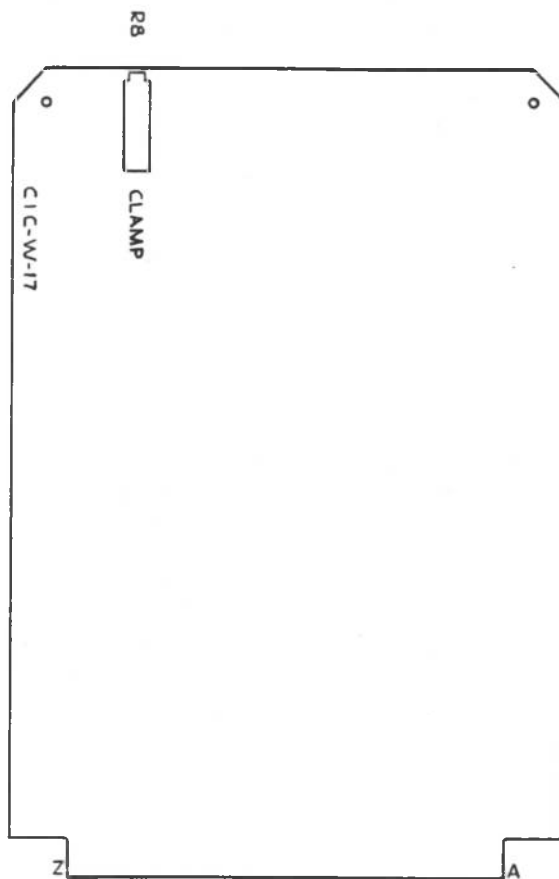
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	DATE
DATE	12/3/84		
DRN	EVL	CAO	
APVD			
TITLE SET-UP PROCEDURE CIC-9-X MIX EFFECT AMP		SH 1 OF 1 FCIC-44	



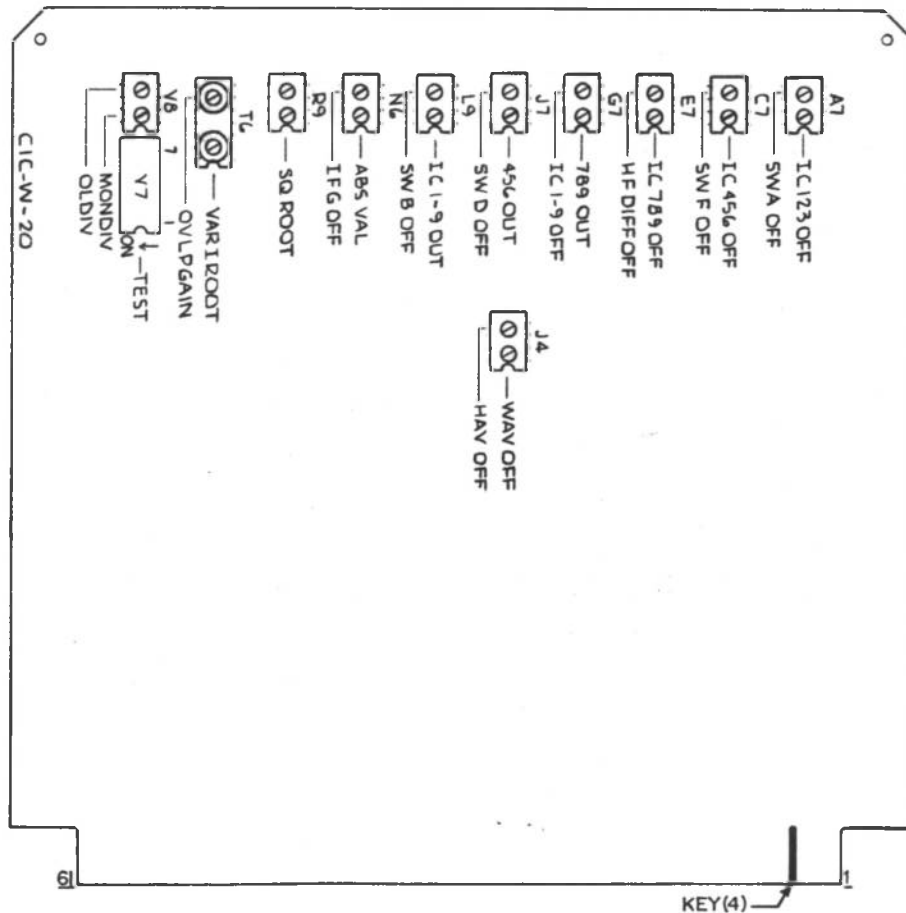
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SCALE	FULL	REVISIONS	BY DATE
DATE	12/4/84		
DRN	EVL	CHKD	
AP VD			
TITLE SET-UP PROCEDURE CIC-11 TIMING CIRCUIT		SH 1 OF 1 FCIC-45	



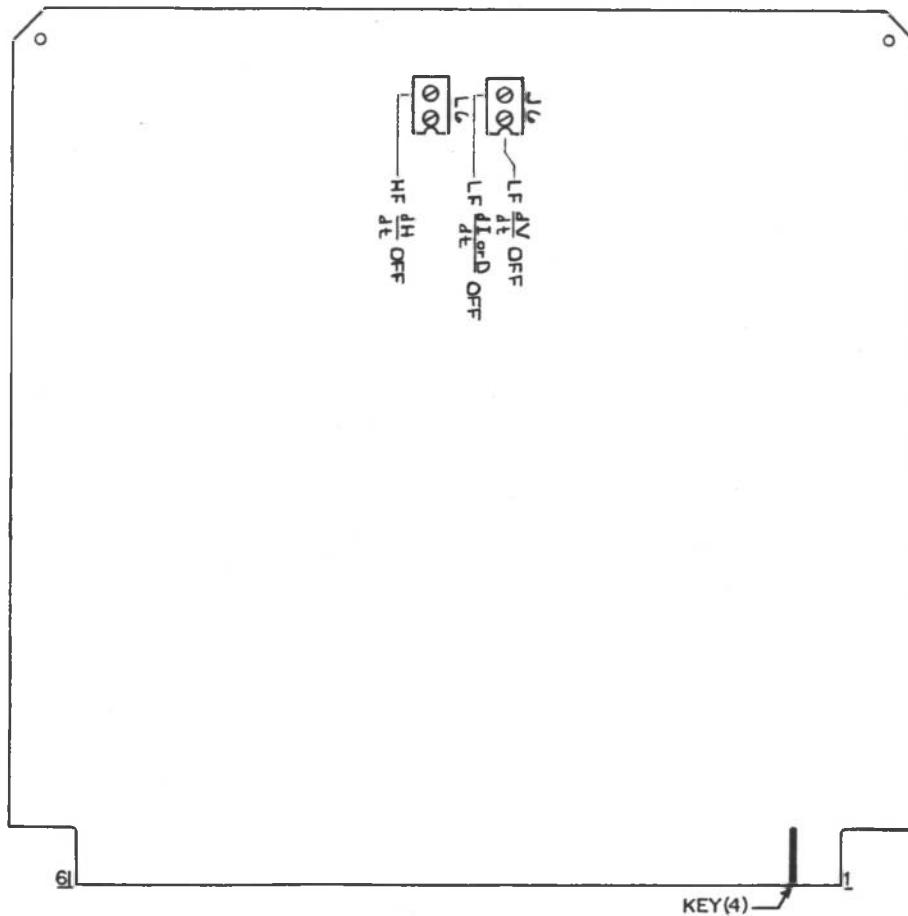
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE. DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	BY DATE
DATE	12/4/84		
DRN	EVL	CKD	
AP'VD			
TITLE SET-UP PROCEDURE CIC-16 CAMERA VIDEO PRE-AMP		NO 5H 1 OF 1 FCIC-46	



 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	BY DATE
DATE	12/4/84		
DR	EVL		
APP	VO		
TITLE SET-UP PROCEDURE CIC-W-17 COLOR CONTRL LOGIC		NO SH 1 OF 1 FCIC-47	

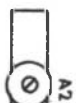
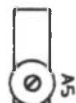


 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	BY DATE
DATE	12/5/84		
DRN	EVL		
APVD			
TITLE SET-UP PROCEDURE CIC-W-20 ^{MD} INTENSITY COMP		SH 1 OF 1 FCIC-48	



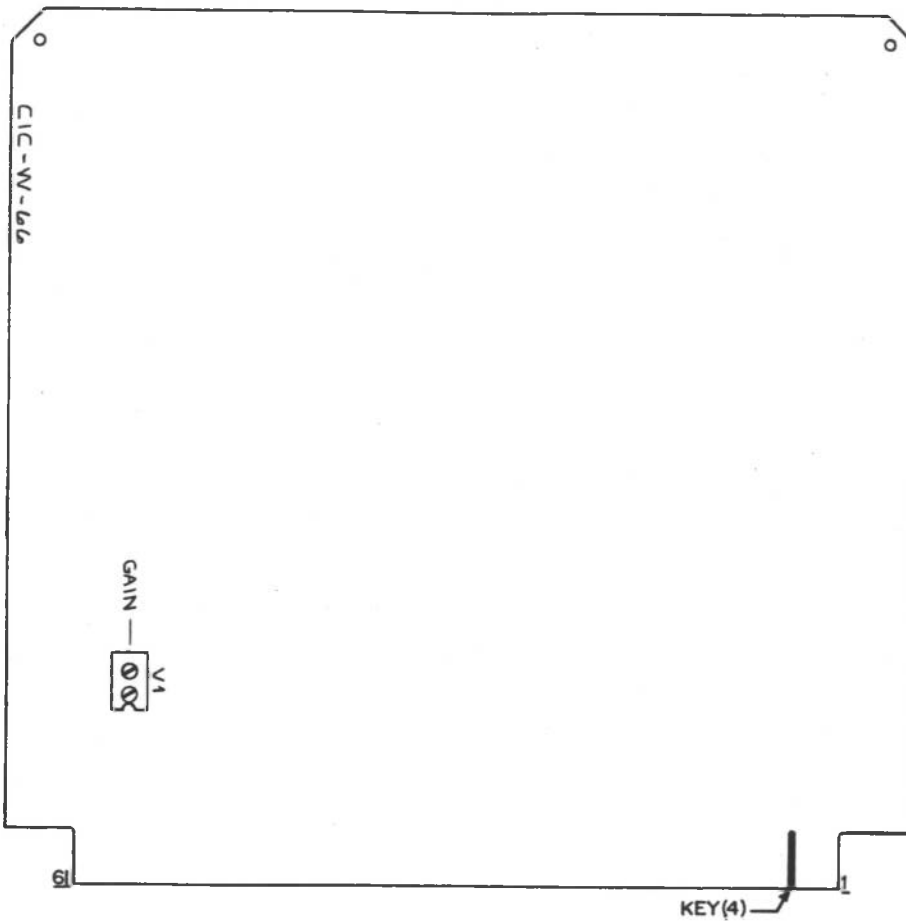
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	DATE
DATE	12/5/84		
DRN	EYL		
APVD			
TITLE SET-UP PROCEDURE C/C-W-23 HORIZ BLANKING		SH 1 OF 1 FCIC-49	

CIC-W-22



KEY(4)

 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	DATE
DATE	12/6/84		
DR	EVL	CHKD	
AP	VD		
TITLE SET-UP PROCEDURE CIC-W-22 NTSC RPU SYNC		SH 1 OF 1 FCIC-50	



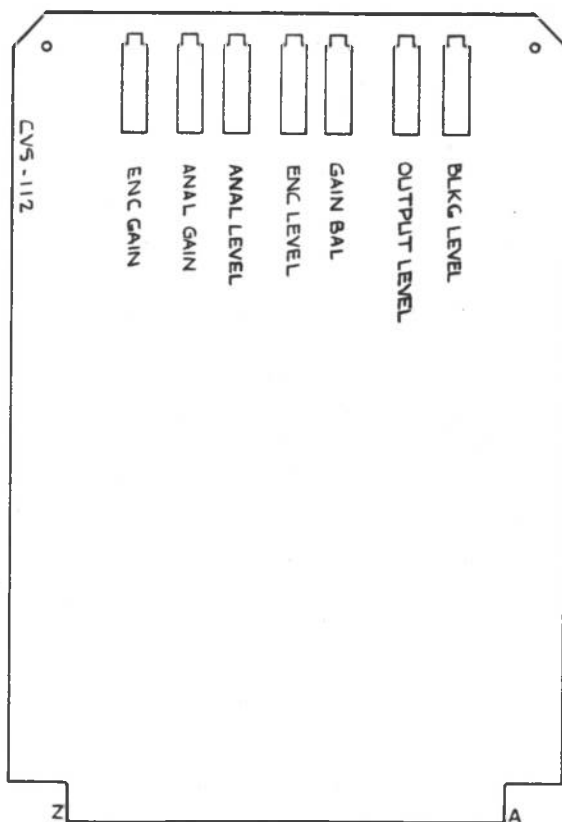
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	BY DATE
DATE	12/7/84		
DRN	EVL	CRD	
AP'VD			
TITLE SET-UP PROCEDURE CIC-W-66 D/A OUTPUT & WFM STORAGE		NO	SH 1 OF 1
		FCIC-51	

CIC-W-67

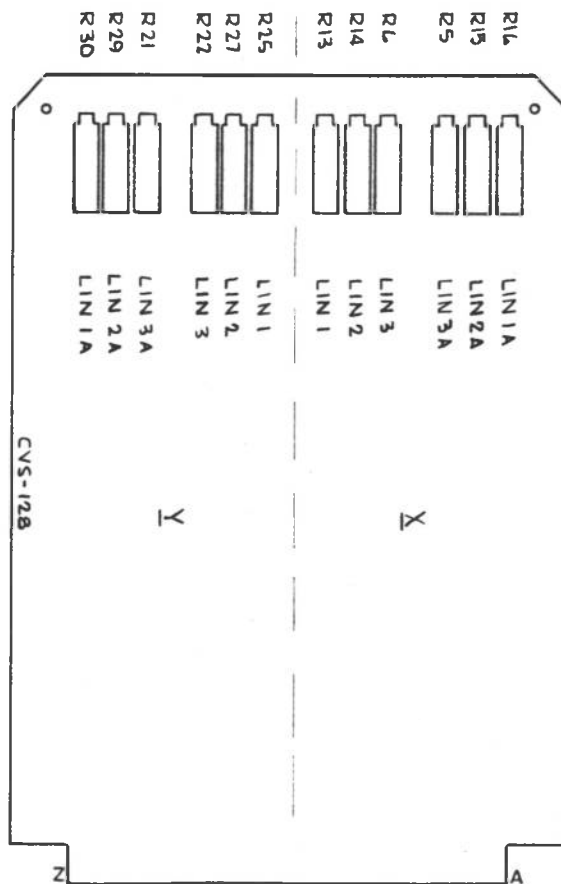
GAIN
1 X 2
V3
GAIN
2 X 3

KEY(4)

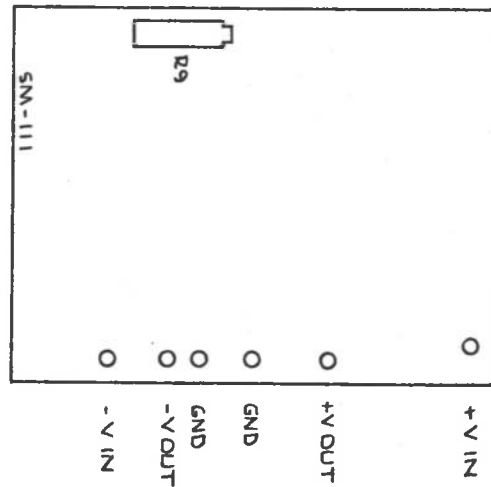
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	BY DATE
DATE	12/7/84		
DR	EVL	CRD	
AP	VS		
TITLE SET-UP PROCEDURE CIC-W-67 FG COMBINER 1X2 & 2X3		SH 1 OF 1 FCIC-52	



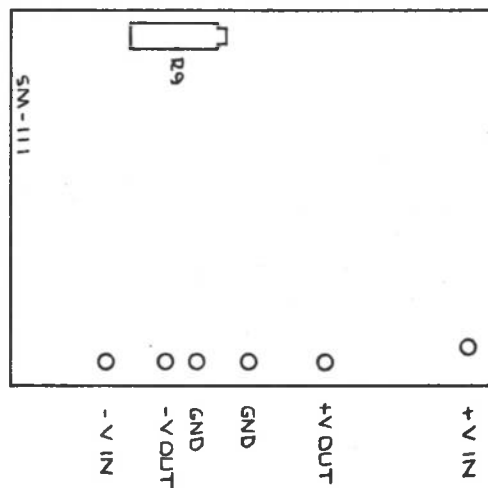
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SCALE	FULL	REVISIONS	DATE
DATE	12/7/84		
DR	EVL	CRD	
AP	VO		
TITLE SET-UP PROCEDURE CVS-112 GRAY LEVEL ENCODER VID AMP		SH 1 OF 1 FCIC-53	



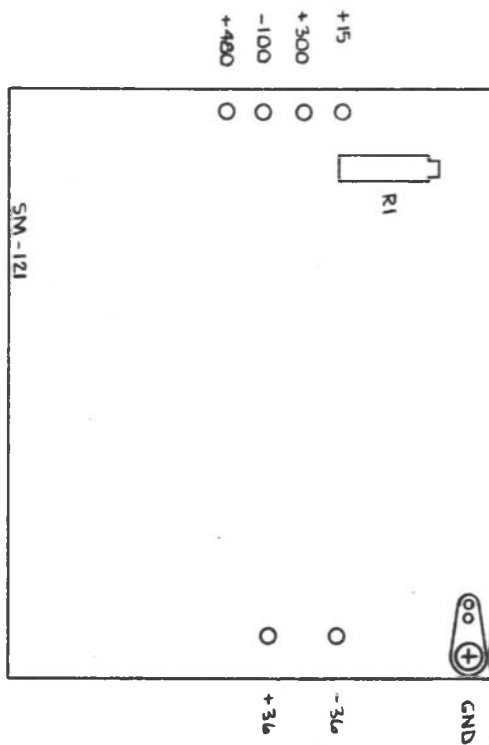
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SCALE	FULL	REVISIONS	DATE
DATE	12/10/84		
DR	EVL	CR	
AP	VD		
TITLE SET-UP PROCEDURE CVS-128 MON YOKE COMP & PINCUSH ADDR		NO SH 1 OF 1 FCIC-55	



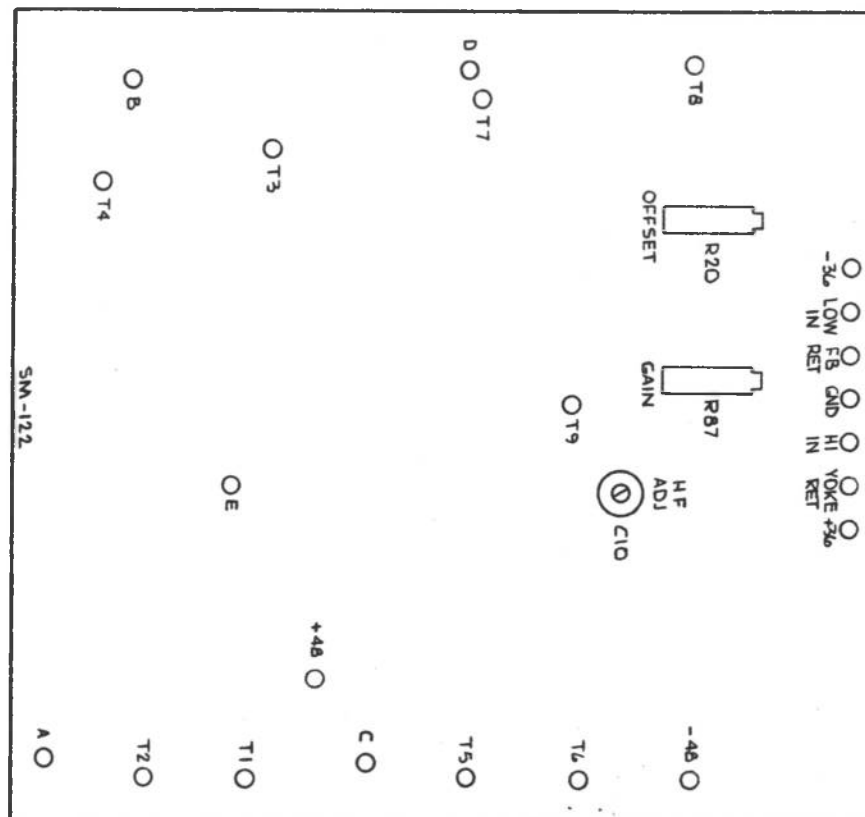
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SCALE	FULL	REVISIONS	BY DATE
DATE	12/10/84		
DR	EVL	CRD	
AP	VS		
TITLE SET-UP PROCEDURE SM-111 ± 36 VOLT REGULATOR		SH 1 OF 1 FCIC-56	



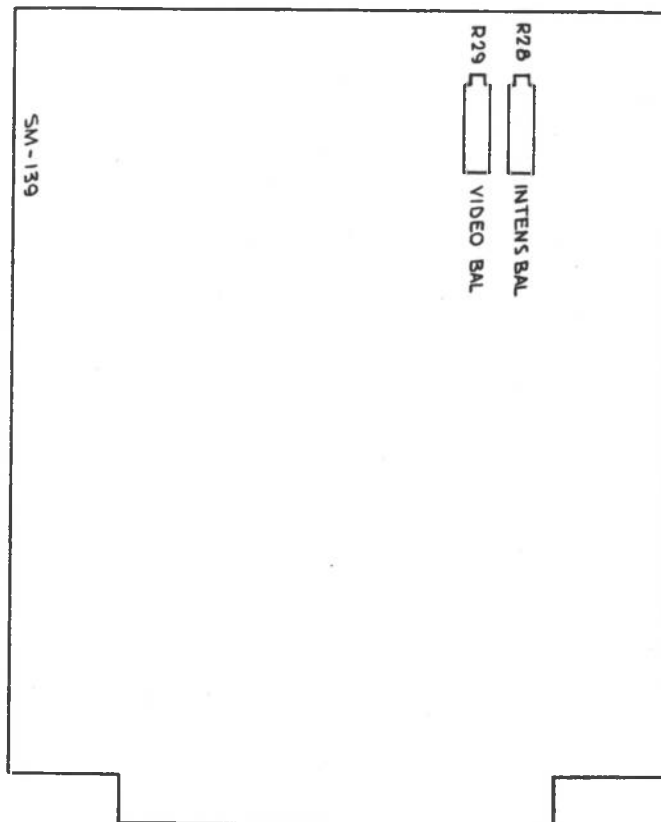
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SCALE	FULL	REVISIONS	BY DATE
DATE	12/10/84		
DR	EVLC		
AP	VO		
TITLE SET-UP PROCEDURE SM-III ± 36 VOLT REGULATOR		NO SM 1 OF 1 FCIC-56	



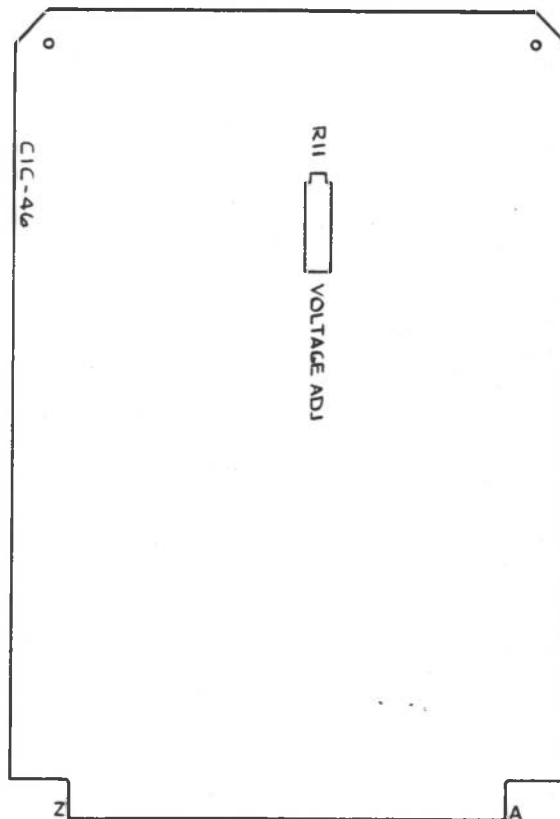
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SCALE	FULL	REVISIONS	DATE
DATE	12/10/84		
DR	EVL	CRD	
AP	VD		
TITLE SET-UP PROCEDURE SM-121 OVP HI-VOLTAGE P.S.		NO SH 1 OF 1 FCIC-57	



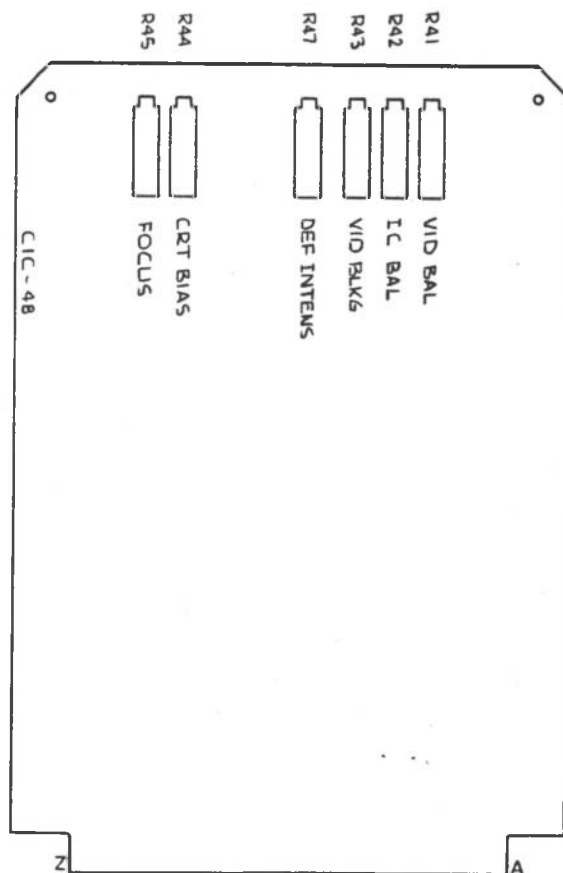
 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	DATE
DATE	12/11/84		
DR A	EVL	CRD	
AP VO			
TITLE SET-UP PROCEDURE SM-122 X-Y DEFLECTION AMP		SH 1 OF 1 FCIC-58	



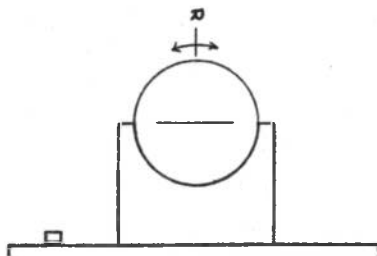
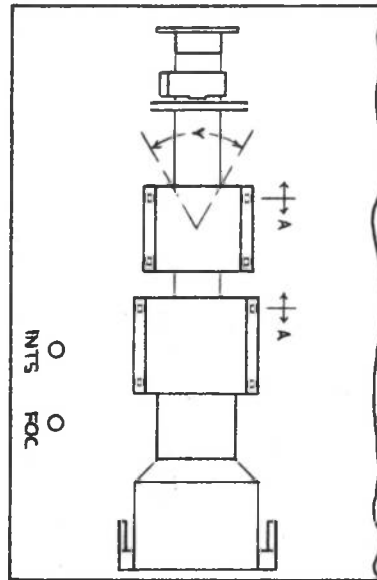
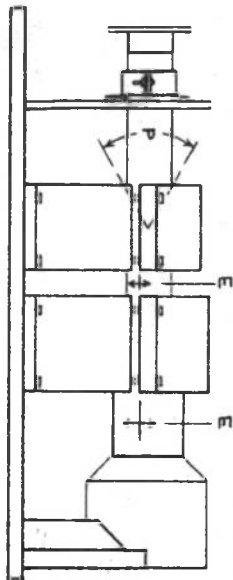
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SCALE	FULL	REVISIONS	BY DATE
DATE	12/11/84		
DRN	EVL	CRD	
AP'VD			
TITLE SET-UP PROCEDURE SM-139		NO SH 1 OF 1	
FOCUS REG/BLKG & VID AMP		FCIC-59	



 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	BY DATE
DATE	12/11/84		
DRN	EVL	CAD	
APP	VD		
TITLE SET-UP PROCEDURE CIC-46 SCAN CONV POWER REG		SH 1 OF 1 FCIC-60	

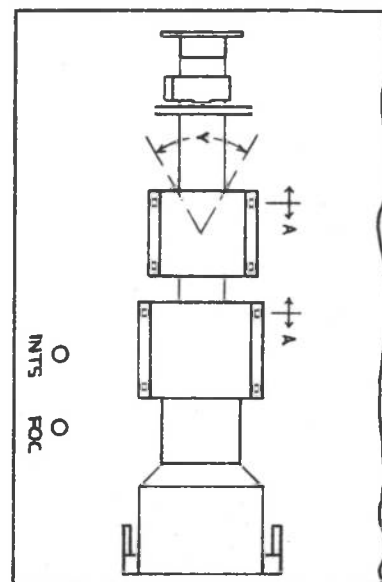
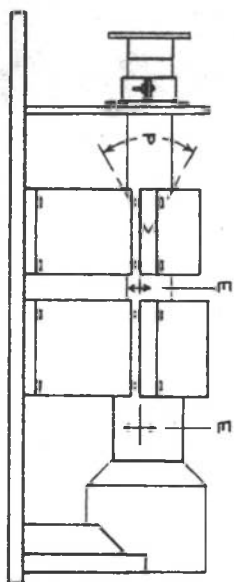


 COMPUTER IMAGE CORPORATION		2475 W. 2nd AVE DENVER, COLORADO 80223	
SCALE	FULL	REVISIONS	DATE
DATE	12/11/84		
DRN	EVL		
APVD			
TITLE SET-UP PROCEDURE CIC-48 VIDEO MULTIPLIER		SH 1 OF 1 FCIC-61	

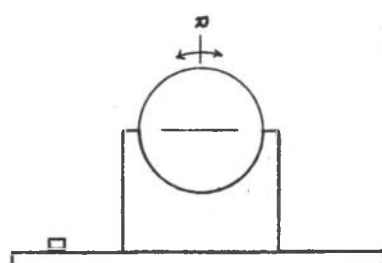


R E P A Y
 ROTATION ELEVATION
 PITCH AXIAL
 YAW

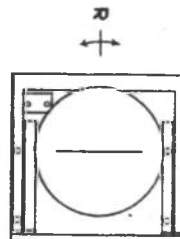
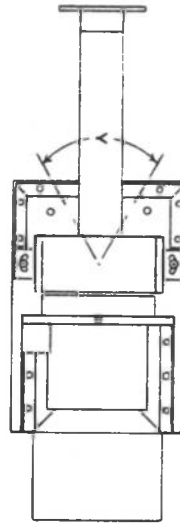
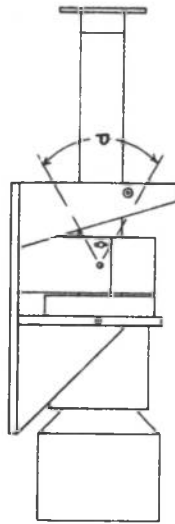
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SCALE	NONE	REVISIONS	DATE
DATE	12/12/84	BY	
DR	EVL	CHKD	
AP	VS		
TITLE SET-UP PROCEDURE SCAN CONV - CONSOLE		SH 1 OF 1 FCIC-62	



R ROTATION
 E ELEVATION
 P PITCH
 A AXIAL
 Y YAW



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SCALE	NONE	REVISIONS	BY DATE
DATE	12/12/84		
DRN	EVL CKD		
AP VD			
TITLE SET-UP PROCEDURE SCAN CONV - CONSOLE		NO SH 1 OF 1 FCIC-62	



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SCALE	NONE	REVISIONS	BY DATE
DATE	12/13/84		
DR	EVL	CRD	
AP	VB		
TITLE SET-UPPROCEDURE SCAN CONV-UPRIGHT		NO SH 1 OF 1 FCIC-63	

